

Vivekanand Education Society's

Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

INDEX

1.2 Prospectus of all programs for the last five year.

Sr. No.	Content	Page No.
1.	Prospectus For Academic Year 2016-17	1
2.	Prospectus For Academic Year 2017-18	76
3.	Prospectus For Academic Year 2018-19	149
4.	Prospectus For Academic Year 2019-20	214
5.	Prospectus For Academic Year 2020-21	278

VIVEKANAND EDUCATION SOCIETY'S

INSTITUTE OF TECHNOLOGY

Affiliated to Mumbai University & Approved by A.I.C.T.E.



Website: http://vesit.ves.ac.inor http://ves.ac.in/vesit

I N D E X

SR.NO.	CONTENTS	PAGE NOS.
1.	ABOUT V.E.S.	01
2.	VESIT GOVERNING BODY	03
3.	VISION	04
4.	MISSION	04
5.	COURSES OFFERED	05
6.	REPORT FROM PRINCIPAL'S DESK A.Y. 2015-16	06
7.	REPORT FROM VICE-PRINCIPAL'S DESK A.Y. 2015-16	12
8.	SCHEME OF STUDY	13
8.1.1	BACHELOR OF ENGINEERING (B.E.)	13
0.1.1	briefieldir di Erranita (b.e.)	10
8.1.2	MASTER OF ENGINEERING (M.E.)	14
0.1.2	WASTER OF ENGINEERING (W.E.)	14
0.1.2	MASTER OF COMPLITED APPLICATIONS (MCA)	14
8.1.3	MASTER OF COMPUTER APPLICATIONS (MCA)	14
8.2	GRADING SYSTEM	15
8.3	TERM GRANT	15
8.4	EXAMINATIONS	15

8.5	USING UNFAIR-MEANS IN EXAMINATION	16
8.6	ATTENDING LECTURES/ LABS	16
9.	FEE STRUCTURE	18
10.	ABOUT CAPITATION FEE	18
11.	SPECIAL PROGRAMS	19
12.	MAJOR LABORATORIES	20
13.	INDUSTRIAL COLLABORATION	25
14.	RESEARCH AND DEVELOPMENT LAB	28
15.	COMMON INFRASTRUCTURE	29
15.1	LIBRARY	29
15.2	OTHER FACILITIES	30
16.	TEACHING FACULTY	31
17.	STUDENTS COMMITTEE/ACTIVITIES	32
18.	PRESS RELEASES	48
19.	PROHIBITION OF RAGGING	52
20.	WOMEN WELFARE/ GRIEVANCE COMMITTEE	53
21.	ANTI SEXUAL HARASSMENT COMMITTEE	53

22.	WOMEN DEVELOPMENT CELL	54
23.	CAREER GUIDANCE, TRAINING, PLACEMENT & ENTREPRENEURSHIP CELL	55
24.	ALUMNI ASSOCIATION	57
25.	PARTICIPATION IN INTER COLLEGIATE FESTIVALS	57
26.	DISCIPLINE	58
26.1	IDENTITY CARDS	58
26.2	LOSS OF I-CARD & ISSUE OF DUPLICATE I-CARD	58
26.3	EX STUDENT'S CARD	58
26.4	LIBRARY & OFFICE TIMING	58
26.5	USE OF SPORTS GROUND	59
26.6	HOLIDAYS & VACATION	59
26.7	RAILWAY & S. T. BUS CONCESSION	59
26.8	HOSTEL	59
26.9	SAFETY PRECAUTION	60
26.10	LOST AND FOUND	60
26.11	DRESS CODE	60
26.12	CLEANLINESS OF PREMISES	60

26.13	INDULGING IN ANTI-SOCIAL ACTIVITIES	60
26.14	CHEWING PAN/TOBACCO AND SMOKING	60
26.15	USE OF ELECTRONIC ITEMS/GADGETS	61
26.16	REFUND OF CAUTION MONEY	61
26.17	LOCKER FACILITY	61
26.18	RETURN OF ORIGINAL CERTIFICATES	61
26.19	NOTICE BOARDS	61
26.20	GRE/TOEFL/GATE/GMAT	62
26.21	CO-CURRICULAR ACTIVITIES	62
26.22	PICNICS & OUTINGS	62
26.23	INTERNET USAGE	62
26.24	THEFT	63
26.25	SUGGESTION BOX	63
26.26	BIRTHDAY CELEBRATIONS	63
26.27	PRINCIPAL'S SIGNATURE	63
	MOST IMPORTANT NOTE	63
	UNDERTAKING	64

ANNEXURE I	66
ANNEXURE II	68

1. About Vivekanand Education Society (V.E.S.)



Founder of Vivekanand Education Society



Shri Hashu Advani along with his 10 friends started Vivekanand Education Society (VES) as a small school, in a barrack, which has grown like a banyan tree, with a spread of 26 institutions.

Shri Hashu Advani was the Municipal Corporator, MLA Urban Development Minister and Finance Minister in the Government of Maharashtra.

Philosophy of Shri. Hashu Advani ji & Values of VES

Samarpan Total dedication and devoted to serving Humanity
Samdrishti Humanitarian feeling, behavior with equality for all
Vishalta Visionary in pursuit of excellence for a value rich education

Karm Yogi Adoer, Actions speaking louder than words

Siddhant Valuing humanity and the importance of education and learning, in building a better

future

Vision

- Increase the intellectual wealth of the country by providing quality education and building excellent technical skills
- Make individuals highly confident, self reliant and truly global citizens
- Provide for holistic development, in terms of Knowledge, Skill and Wisdom, leading to cultural and spiritual integration.

VES Mission – Value Based Education

Develop new generation leaders to bring a positive change in the world using modern education system emanating from India's rich cultural heritage blended with western science!

Educational Facilities Offered

- Professional, Technical & Higher Education
- Junior & Senior Colleges
- Certificate Level Technical & Vocational Education
- · High School Level Education

- · Primary & Preprimary Level Education
- Welfare Activities
- Sports & Fine Arts





Distinctive Essentials

- Admired for Ethics, Values, Discipline and Excellence in education
- Management board of honorary professionals in different fields. Honorary Associates and Alumni assisting in various activities of the trust.
- 26 Institution from crèche to schools to colleges upto Ph.D. level
- More than 18,000 students studying every year and over 1200 teaching & non-teaching Staff.
- No capitation or donation from students or parents
- There are no Management quota seats in all professional Institutions of VES.
- All admissions are purely on merit
- 12 buildings in 3 complex's with over 395000 sq. ft of area
- 3 Large Sports grounds and, Hostel & Staff quarters.
- Financial help to poor & needy students.

Institutions of VES

Professional, Technical & Higher Education Institutions

V.E.S. Institute of Management Studies and Research

V.E.S. Institute of Technology

V.E.S. College of Pharmacy

V.E.S. Law College

V.E.S. College of Science, Arts & Commerce

V.E.S. Leadership Academy & Research Centre

V.E.S. Polytechnic

High Schools & Junior Colleges

Swami Vivekanand Jr. College, English Medium

Swami Vivekanand Kanishta Mahavidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, English Medium, Chembur

Swami Vivekanand Vidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, Marathi Medium, Kurla

Certificate Level Technical & Vocational Institutions

V.E.S. Tulsi Technical Institute,

Sindhi Society, Chembur

V.E.S. Catering Craft Centre

V.E.S. Nari Shala

Sports & Fine Arts Institutions

V.E.S. Vivekanand Sports Academy

V.E.S. Vivekini

Other Institutions

V.E.S. Creche & Day Care Centre

V.E.S. Vocational Guidance Bureau

V.E.S. Welfare Section

V.E.S. Vivek Sindhi Kendra

Primary & Preprimary Schools

Swami Vivekanand Preprimary & Primary School English Medium, Sindhi Society, Chembur

Swami Vivekanand DBC Pre-Primary & Primary School English Medium, Collectors Colony, Chembur

Swami Vivekanand Prathmik & Purva Prathmik Shala Hindi Medium, Kurla

Swami Vivekanand Pre-Primary & Primary School English Medium, Kurla (E)

Swami Vivekanand Prathmik & Purva Prathamic Shala, Marathi Medium, Kurla

Swami Vivekanand Nursery & Play School, Chembur

VIVEKANAND EDUCATION SOCIETY

VES Managing Committee Members

1. Shri. Mahesh Tejwani (Chairman)

2. Shri. Dinesh Tahiliani (Secretary)

3. Shri. Amar Asrani (Treasurer)

4. Shri. B. L. Boolani (Member Trustee)

5. Shri. Bansi Wadhwa (Member Trustee)

6. Adv. Shri. Laxman Kanal (Member Trustee)

7. Shri. Suresh Bharwani (Member Trustee)

8. Shri. Prakash Lulla (Member Trustee)

9. Shri. Suresh Malkani (Member Trustee)

10. Smt. P. Vasvani (Member Trustee)

11. Shri. Vishu Lala (Member Trustee)

VESIT Governing Body Members

• Shri. Mahesh Tejwani Chairman

• Shri. Dinesh Tahiliani Member

• Shri. Amar Asrani Member

• Shri, B. L. Boolani Member

Shri Sunil Khanna
 Member Industry Expert

• Shri. R.N.Mukhija Member Industry Expert

• Shri. R. Unnikrishanan Regional Officer, AICTE Nominee

• Dr. M. D. Desai AICTE Nominee

• Dr. N. R. Gilke Mumbai University Nominee

• Dr. S. K. Mahajan Director DTE, DTE Nominee

• Shri. P. K. Krishnamurthy State Government Nominee

• Dr. (Mrs.) J. M. Nair Principal /Secretary

• Dr. (Mrs.) M. Vijayalakshmi Vice Principal

• Dr. P. P. Vaidya Staff Representative

• Mrs. Gresha Bhatia Staff Representative

• Shri. K. D. Ahuja Special Invitee

Vision:

"To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of socially conscious application of technology".

Mission:

- To inculcate culture of value based education.
- To enthuse students to develop in an ambient environment of caring and of sharing information.
- To enable students to work towards excellence in their chosen fields with professional bent of mind.

5. COURSES OFFERED

National Board of Accreditation (NBA) has approved the accreditation of all Under-Graduate Programmes for a period of 2 years after the visit of Expert Committee

Starting Year	Course/Programme/Intake
1984 – 1985	BE Electronics Engineering – 60
	BE Computer Engineering – 60
	BE Instrumentation Engineering – 60
1992 – 1993	BE Electronics and Telecommunication Engineering – 60
1999 – 2000	BE Information Technology – 60
2002 – 2003	ME Electronics and Telecommunication Engineering – 18
2002 – 2003	ME Information Technology – 18
2003 - 2004	ME Instrumentation and Control – 18
2003 - 2004	Master of Computer Applications – 60
	Increase in intake of the existing programmes
2011 – 2012	BE Computer Engineering – 60
2012 – 2013	BE Electronics and Telecommunication Engineering – 60
	BE Electronics Engineering – 60
	BE Computer Engineering – 60 (Regular Second Shift)
	Master of Computer Applications – 60 (Regular Second Shift)
	Recognized as Ph.D. Centre for Electronics
	Direct Second Year BE
2013-2014	
2013-2014	Electronics and Telecommunication Engineering – 60(Regular



6. From Principal's Desk: A. Y. 2015-2016

In this fast paced 21st century, where technology permeates day to day lives, economies are driven by innovation, knowledge, competition and rapid change, it is only natural that education that prepares our new generation for life, must be aligned with it. Established with the singular objective to make quality professional education accessible to all students, VESIT today reckoned as one of the most sought after institutes in Mumbai, also pays equal importance in adapting to the changes the 21st century education demands and was declared as one of "Asia's Best & Fastest Growing Education Institutes" for academic year 2014-15. The continuous review of System and Processes to ensure quality has been the practice in the institute.

This semester, we have constituted a new cell 'VESIT Renaissance Cell' (VRC), similar to Double Operating System Model- Accelerators – proposed by John P. Kotter, well known thought leader in the fields of leadership and change management. This model ensures sustained growth and innovation at the same time. The cell consisting of young motivated staff members from all the departments, suggested innovative schemes such as projects for teachers and Skill Development Programme for both students and teachers to enable them to work towards excellence with a professional bent of mind. As per their suggestions, Skill development Programs on FPGA and Wireless Networks were conducted for the third year students in this semester. Our staff members, who were trained by Wiley WROX for "WROX Certified Big Data Analyst" (WCBDA) in January, conducted a Skill Development Program (SDP) for faculty. Similarly, Certified Labview Associate Development Program (CLAD) certified teachers from National Instruments, conducted Labview training for students.

Vesit Renascence Cell also organized an expert lecture on "How to build smart things" by Dr. Kriti Ramamritham, Professor, Department of Computer Science and Engineering (CSE) and Head, Center for Urban Sc. and Engg. IIT Mumbai on 27th January 2016.

In order to bridge the gap between industry and students various industry interaction programs were conducted during this year.

Confederation of Indian Industries conducted an interaction event on 3rd March, 'CII Tech Connect' in association with Instrument Society of America and Emerson Network Power. Mr. Neeraj Agarwal, Associate Director of Nuclear Power Corporation of India was guest of honour and Mr. Sunil Khanna, President, Emerson Network Power delivered inaugural address. The speakers of technical series were Mr. Akshay Hudar of Environmental Greenliness, Mr. C K Asher of Ex buzz Fire and Security System followed by motivational games by Mr. Keyur Kapadia of Tusya Travel and Hospitality Service.

A two day winter school program for third year students were conducted by TCS and General Mills Ltd. Two of the speakers from TCS were VESIT alumni Mr. Sachin Gosar, Electronics & Telecommunication Engineering -2002 and Mr. Paresh Ardekar Electronics & Telecommunication Engineering-2004. Mr. Rahul Shukla, Manager, Application Development from General Mills and Mr. Utkarsh Patkar, Application Architect and Technology leader from General Mills also delivered an expert lectures on ERP / SAP and open source tools. A similar two day lecture series was conducted by L & T in February 2016. The speakers Mr. Gadgil, Mr. N.R.K. Rao and Mr. Shah shared their expert knowledge on Mobility, Internet of Things (IOT) and Project Management respectively.

VESIT has been chosen as the **Competency Development Centre** by Automation Industry Association, Delhi to conduct "A course on Integrated Industrial Automation" as a part of **CAMPUS CONNECT** initiative by them. The Newly developed Integrated Automation Lab was inaugurated by Padmashri Dr. G.D. Yadav, Director, Institute of Chemical Technology, Mumbai on March 2nd. Fourteen teachers from Electronics, Electronics & Telecommunication, Instrumentation and General Engineering have been trained for this course by experts from Festo, Siemens, B & R Automation and Pepperl & Fuchs.

VESIT also organized Industrial visit to Ahmedabad for third year students during the semester break of December. They visited EQDC, Electronics & Quality Development Centre and AMTech leading manufacturer in motion control products and automation.

The thirteenth edition of our intercollegiate technical festival Praxis 2015 was conducted on 6^{th} and 7^{th} of August with this year's theme as Entertainment technology. Accenture was the chief sponsor for the event which went on to record the highest turnout of all time.

I am happy to report that our students are continuously performing very well in Mumbai University Examinations. Two of our students **Teni Gada** of MCA and **Neha Sidhwa** of computer engineering were felicitated for academicsby Mr. Sanjay Deshmukh, Vice Chancellor of University of Mumbai, for securing top positions in Mumbai University. Our first year overall B.E. pass percentage was 71 against university pass percentage of 43. The pass percentage of B.E. final year in three departments, Electronics & Telecommunication, Electronics, Computer and Master of Computer Application was 100% and Instrumentation and Information Technology secured 97.6 and 94.6% respectively.

The Degree Distribution Ceremony was conducted on 23rd January, Mr. Dilip Kumar Khandelwal, Managing Director of SAP labs India and distinguished alumnus of VESIT was the chief guest of the function.

We are also proud to report that 76.41% of eligible students are placed in **34 companies** who visited us this year with Accenture as the major recruiter. Nomura, JPMC, CapGemini, LTITL, ATOS, Sap Labs and Majesco were the other recruiters.

We do believe in creating opportunities for students and encouraging them to apply practical knowledge to real life scenario. Hearty congratulations to Harsh Gupta and Nirbhay Pherwani who won prizes in **Hackathon Competition** held at **IIT Bomba**y as a part of make in India event at the Bandra Kurla Complex (BKC) held in February 2016. They worked on a smart irrigation model and were selected out of 150 aspiring entrepreneurs across the country.

Dr. A. P. J. Abdul Kalam said, "Educationists should build the capacities of the spirit of inquiry, creativity, entrepreunial and moral leadership among students and become their role models". Inspired by him, VESIT conducted a new competition for first year students called "Awakening the Scientist", on his birthday October 15, which is celebrated as World Students Day. This gave an opportunity to the students to analyze the application of basic sciences in equipment and tools used in day to day life.

"VESIT CONNECT" a monthly newsletter for the college was also initiated this year. VESIT Connect gives a panoramic view of activities in the institute and creative abilities of students and faculty. A column "Alumni Talk" in every issue has also been included. A special course for Soft Skills improvement for students and weeklong literary activities under VES Literary circle was also organized, this year 'A Communication Skill Enhancement Programme' was also conducted for non-teaching staff by Mrs. Geeta Ajit.

The enthusiastic participation of the students in the co-curricular activities "Looking Beyond Syllabus", "Exclusive Scholar Program", and "Vacation Project" gives them an upper edge over their peers. Regular Skill Enhancement Lectures, Mini Projects, Professional society activity makes the students keep abreast of the latest developments in the industry. VESIT premier Football League, VESIT cricket League and Illusion cultural festival held in a league format ensures strong senior junior interaction and perfect sportsmanship.

Alumni meet of this year was conducted on 10th January and distinguished alumni awards were presented to Mr. Kuntal Joisher, Computer Engineering 2001, a mountaineer, Mr. Satyen Kothari, Computer Engineering– 1995, Managing Director at Citrus Pay and our chief guest Mr. Saurabh Nanavati, Electronics – 1995, CEO of Religare Invesco. Mr. Kaustubh Takre, 2010, Computer Engineering batch who secured 6th rank in UPSC exam of Central Armed Police Forces, delivered an informative lecture to the second year students to create awareness about the various opportunities available in national administrative services. The institute has been making continuous efforts to do focused research and it resulted in the Research & Development projects with Larsen & Toubro, IIT Bombay, BARC, Persistent Technologies, ADAPTI & SAMEER.

I am also happy to report that our staff members Dr. Rajani Mangala, Mr. P.B. Birajdar, Mrs. Dipti Khimani, Mrs. Madhumati Khuspe, Mrs. Shoba Krishnan and Mr. Shobhit Khandare, received minor research grant from Mumbai University. We are also proud of our teachers Dr. M. Vijayalakshmi, Dr. Nupur Giri and Mr. Prashant Kanade who published books in their domain with Wiley India Publications.

We are very happy to note that the many of our teachers adopted the innovative teaching methods of "POGIL" Process Oriented Guided Inquiry Learning and Project

Based Learning. Skill mapped Practicals was also introduced on pilot basis this year. Under this scheme students are graded according to their interest and skills, based on the pre lab e-survey. The Practicals / mini projects are then designed at different challenge levels. The student feedback about this novel approach of conducting practicals is encouraging. Some of our teachers also contributed to 'Text book Companion Project', 'Lab Migration Project', 'e-yantra' and 'Virtual Labs' Project initiated by IIT Bombay. Tinkerer's labs have also been started in Electronics and Instrumentation departments to give a platform for the students who are turned on by Technology. We hope that Tinkerer's Lab will help students become innovators, converting their creative ideas to actual engineering products.

VESIT Entrepreneurship-Cell conducted Internship mela, which resulted in 120 winter internships and provided an opportunity to students to gain the industry perspective during their vacations. They also organized a TedX gateway lecture by Shri. Angad Nadkarni who developed free online radio software at the age of 14.

VESIT was also nominated as the host college for the "National Level Youth Entrepreneurship Development Program" initiated by Rajiv Gandhi National Institute of Youth Development Cell, Ministry of youth affairs, Government of India, Tamilnadu. 25 staff members from 12 colleges in Maharashtra were trained in VESIT by ICTACT. The trained faculties, Mr. A. Nagananda and Mr. N. Gopalkrishnan conducted a training programme for VESIT students in the month of July 2015.

Every year, VESIT celebrates **national Youth Day**, the birth anniversary of Swami Vivekanand. This year a lecture on Vivekananda's teachings was conducted by Mr. Abhay Bapat, Maharashtra Prant Pramukh of Vivekanand Kendra. An exhibition of Vivekananda's books was also arranged in the amphitheater. We also initiated "study circle "in collaboration with Vivekanand Kendra Kanyakumari in which regular lectures and group discussions are conducted by them. A quiz competition on Swami Vivekanand was conducted as a part of Illusion, the league format cultural festival of institute. An online quiz based on the book "The story of Vivekanand Rock Memorial" – was also conducted by VESIT literary team.

VESIT has always strived to give back to the society and is a useful contributor in social causes. Our Socially conscious students conducted the clean-up drive of Chembur station in October 2015 and January 2016. They have also taken up the

project of sanitation in Sakhre gaon this year in addition to the regular activities of blood & organ donation drive, cleanliness drive, clothes and stationery distribution. This project which envisages construction of toilets in Sakhre gaon received contribution from our alumni also.

A mock evacuation drill was conducted with the help of NGO 'Freedom for you foundation' on 27th February where we improved on our earlier evacuation time of 7 min to 6 min.

To help the needy students, VESIT initiated 'Vivekanand Jyoti' Scholarship scheme this year. We are indebted to our trust who has donated a corpus amount of Rs. 20 Lakhs for Vivekanand Jyoti Scholarship. The scholarship amount of Rs. 40,000 will be given to the students selected by the team of senior staff members. This scholarship is in addition to the Students Aid Scholarship which was introduced since three years.

A special award 'Shreshtata Award', a pan-VES award was given for students, acknowledging their endeavor to make a difference in society, fighting adverse and unaccommodating circumstances in their life. Three of our students Manish Bharati, Nikita Parab and Jyoti Mane were selected for this award.

We are proud of our passionate students and dedicated faculty who are always ready to take ROAD - "Responsibility, Ownership, Accountability, Discipline" to growth and strengthen VESIT Brand. We strive to remain as a "Learning Organisation" where people continuously expand their capacity to create the results they truly desire, where collective aspiration is set free and where people are continuously learning together.

Dr.(Mrs) J. M. Nair Principal

"Stand up. Be bold. Be strong. Take the whole responsibility on your own shoulders and know that you are the creator of your own destiny. All the strength and succor that you want is within yourselves. Therefore make your own future."

- Swami Vivekananda



VESIT takes pride in being able to impart holistic education to all its students. Our vision indicates our dream of being able "To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of

socially conscious application of technology".

I am indeed proud to say that year after year we have been able to deliver on this promise.

We are constantly striving to improve and bring innovation in our teaching practices. POGIL, Project Based Learning, Flipped Classrooms, using NPTEL, E-Yantra and Virtual Labs initiatives from MHRD, continuously collaborating with industry experts to deliver current trends to our students are all efforts towards this goal.

VESIT boasts of a well-equipped library, state of the art laboratories with industry calibre hardware and software. Further several student run professional societies, industry collaboration labs help to create and nurture a vibrant knowledge oriented environment ideal for creating next generation engineers ready to take our nation into a nation of job creators as versus job seekers.

We are VESIT never forget our commitment of "Giving Back to Society". Our SORT team is continually associating itself with several social organizations to participate in society beneficial activities, like village adoptions, educating girl children, cleaning up of railway stations, cancer walks etc.

As John Ruskin has said,

"Quality is never an accident; it is always the rest of intelligent effort."

At VESIT Quality is indeed the sum product of all the endeavours of the Faculty, staff and off course the students.

Wishing you all the very best. May you live in Innovating Times.

Dr.(Mrs) M.Vijayalakshmi Vice Principal

8. SCHEME OF STUDY

National Board of Accreditation (NBA) has approved the accreditation of all Under-Graduate Programmes for a period of 2 years after the visit of Expert Committee.

8.1.1 BACHELOR OF ENGINEERING (B.E) (DTE CODE: EN3185)

The Bachelor of Engineering is a four year course, which is divided into eight semesters. The first year (F.E.) course spreads over two semesters dealing with general engineering, common to all branches of engineering. The basic objective is to give elementary foundation needed in different aspects of engineering.

During the second year (S.E.) and third year (T.E.), students gain basic knowledge in their chosen branches. The final year i.e. fourth year (B.E.) is designed to strengthen the specialized skills, and students learn to apply the concepts studied earlier, by participating in " *project work* ". The subjects and the syllabus are as prescribed by the University of Mumbai.

Approved Intake for Academic Year 2016-17

Regular First Shift	<u>Intake</u>
B.E Electronics Engineering	120
B.E Computer Engineering	120
B.E Instrumentation Engineering	60
B.EElectronics and Telecommunication	120
B.E Information Technology	60

Regular Second Shift	<u>Intake</u>
B.E Computer Engineering	60

B.E. -Direct Second Year (Elect.and Tele.) 60

Eligibility: - As per DTE, Mumbai University and State Government Rules.

<u>Note: -</u>51% Minority quota admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

8.1.2 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME 3185)

The M.E. Program comprises of four semesters with duration of two years. The courses are designed/prescribed by the University of Mumbai. The numbers of seats offered are as mentioned below:

Approved Intake for Academic Year 2016-17

Regular First Shift	<u>Intake</u>	
M.E Electronics and Telecommunication.	18	
M.E Instrumentation and Control	18	
M.E Information Technology	18	

Eligibility: - As per DTE, Mumbai University and State Government Rules

<u>Note:</u> -51% Minority quota admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

8.1.3 MASTER OF COMPUTER APPLICATIONS (M.C.A) (DTE CODE: MC 3185)

Master of Computer Application (M.C.A) is a full time three year course (six semesters). The course is conducted as per the syllabus laid down by the Mumbai University. Examinations are conducted by the University for all the six semesters and the degree is also awarded by the University of Mumbai.

Approved Intake for Academic Year 2016-17

		<u>Intake</u>
MCA	(Regular First Shift)	60
MCA	(Regular Second Shift)_	60

Eligibility: - As per DTE, Mumbai University and State Government Rules.

<u>Note: -</u>51% Minority quota admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

8.2 GRADING SYSTEM OF THE UNIVERSITY OF MUMBAI.

Mumbai University introduced Credit and Grading System from academic year 2012-13. All information about pattern and syllabus is available on Mumbai University's official web site www.mu.ac.in

Salient Features:

- 1. Final grade for B.E. Degree will be based as the average of all eight semesters. So each semester/each subject is important.
- 2. 75% attendance is compulsory.
- 3. Journal submission must be done on or before every term end.
- 4. Two tests will be conducted according to the given model by the University for Internal Assessment of each subject. The average of the two scores of internal assessment will be considered as the final grade in the subject and will be printed in the final marksheet. No retest shall be held.
- 5. Failing in eight heads in a year is the maximum limit for allowed in the ATKT system. (Maximum five theory exams and remaining Prac./Oral/Internal etc.)
- 6. Failure to submit the Journals in any subject (even for a single subject) will result in detention (i.e. loss of one year). Admission for the next academic year is to be taken by repayment of fees.

8.3 TERM GRANT

Term Sanctioning (allowing the students to appear for the University examination) entirely depends upon the following:-

- Minimum of 75% attendance in all subjects.
- Satisfactory completion of all term work
- Practical and Assignments as specified in Syllabus or as instructed within prescribed periods
- General behaviour of students, both inside and outside the classroom.

8.4 EXAMINATIONS:

The University of Mumbai conducts examination in every semester, and class two tests are conducted by the college in every subject. The performance in these tests is considered for grade calculations as per the MU circular. The submission of

term work in each subject should be taken very seriously. If a student fails to submit his/her Term work or Sessional work before the prescribed date, he/she will not be allowed to sit for the concerned examination.

8.5 USING UNFAIR- MEANS IN THE EXAMINATIONS

During an examination, if a student is found, either possessing any material (written or printed) with the intention of copying or actually copying, he/she is liable to be severely punished according to the rules laid out by the University. In such circumstances, a student may either be debarred from appearing for the entire examination or expelled from the University of Mumbai for a minimum period of three years as per the decision of "Unfair - Means Committee".

8.6 ATTENDING LECTURES/LABS

- a. Students coming late to the Institute will not be allowed to enter the premises.
- b. Bunking classes is strictly forbidden. If the student misses even one period, he/she may be marked absent for the whole day.
- c. In order to monitor the academic progress as well as the overall development of our students, we nominate one staff member as faculty advisor to whom a student can approach for guidance. For day to day functioning, one class teacher and one class counsellor is also associated with each class. The students, as well as their parents, are welcome to contact either the class teacher/class counsellor or the faculty advisor any time during the session.
- d. While the lecture or the lab is in progress, a student is supposed to keep his/her **mobile phone switched off**, failing which severe punishment can be given.
- e. After their classes are over, the students should leave the premises in a proper disciplined manner, without making any noise or crowding in the corridors.

Students should submit the undertaking at the time of admission. (Refer Page No.64)

Infrastructure and Salient Features

- ✓ Sprawling Complex
- ✓ Equipped Library having more than 45000 technical books
- ✓ Professional Societies
- ✓ Active Placement Cell
- ✓ Strong Alumni Association with Alumni Web Portal
- ✓ Conference Hall
- ✓ Seminar Halls
- ✓ Auditorium
- ✓ Music Room
- ✓ Huge Canteen
- ✓ Inviting Playground
- ✓ Amphi Theater
- ✓ Recreation Room
- Labs set up in collaboration with industry
- ✓ Automation Industry Association Lab
- ✓ Cloudera Big Data Analytics Lab
- ✓ National Instruments Labview Academy
- ✓ D Link Academy

9. FEE STRUCTURE

Final Fees approved by Shikshan Shulka Samiti for the			
Name of course	A.Y. 2015-2016		
	Tuition Fee	Development Fee	Total Fees
	(Rs.)	(Rs.)	(Rs.)
B.E. and M.E.	91073	9107	100180
MCA	62955	6295	69250

NOTE: At the time of Admission, all candidates will have to pay the interim fee as fixed by Fee Regulating Authority for the A.Y. 2016-2017. Final fees approved by Fee Regulating Authority for the A.Y. 2016-2017, will be borne by the Candidate.

Most Important Note:

All Admissions and Cancellations will be conducted/done as per the DTE/UNIVERSITY rules and Guidelines.

10. INSTITUTE/ MANAGEMENT does not accept any type of capitation fee

(Refer to Anti Capitation Fee Act as available on our college website: http://vesit.ves.ac.inor http://vesit.ves.ac.in/vesit

11. SPECIAL PROGRAMS

• Exclusive Scholar Programme

Exclusive Scholar Programme launched in January 2013, is a program which is envisaged for academically bright students, who are interested in any extra practical/study project beyond the curriculum .These students, if needed, may be given exemption in either theory or practical classes, depending as per the evaluation of the student and the requirement of the extra project work by the mentor /subject teacher.

Looking Beyond Syllabus

Looking beyond syllabus was launched in the year 2011, with an aim to expose the students to emerging technologies and opportunities and to work as teams on multidisciplinary projects under the mentorship of a faculty member assigned to them, in their area of interest/passion that may not necessarily be his stream of study.

The main objective of **looking beyond syllabus** of technical education is to infuse prospective technocrats with the ability to apply basic science and experimentation skills, to understand problems in the industry, academia and humanity at large and also be able to develop practical solutions. It also aims to model them into wholesome professionals in the field of Technology. Looking beyond the syllabus also prepares the students to use their skills with a strong base for higher education and acquisition of knowledge to sustain a life-long career in related areas.

Awakening the Scientist

Awakening the Scientist is an opportunity given to the First year students to take the first step in this transformation of a student to a professional. It is a group activity. The objective is to identify the science, engineering and technology associated with gadgets, equipments and tools used in day to day life and also relate the past present and future of such gadgets, equipments and tools used in day to day life.

Skill Enhancement Lectures.

VESIT believes in giving its students an invigorating academic environment. The regular syllabus of Mumbai University is conducted by our faculty very effectively. Anextra edgeis given to our students by inviting experts from the industry and research institutions .This endeavor is termed as 'Skill Enhancement Lectures'.

12. MAJOR LABORATORIES

Department Of Electronics Engineering

BASIC ELECTRICAL ENGINEERING LAB

Laboratory courses conducted: - Basic Electrical and Electronics Engineering, Electrical Workshop, Electronic and Electrical Measuring Instruments and Machine. Lab is equipped with 1-phase and 3-phase Motors, different types of Transformers, A.C. and D.C. Generators, Motors. Trainer kits for network theorems, analog and digital measuring instruments etc.

VLSI DESIGN LAB

Laboratory courses conducted: - Basics of VLSI Design, IC Technology and CMOS VLSI Design. The department is a member of Cadence University Program; subscribed to cadence VLSI design suite, running on 1- IntelXeon processor,19 clients with 20 cadence licenses and FPGA boards.

ELECTRONIC CIRCUIT DESIGN and ANALYSIS LAB

Laboratory courses conducted: - Basic of Electronic Circuits, Electrical Network Analysis and Synthesis, Electronic Circuit Analysis and Design. This lab is equipped with sufficient number CRO, Function Generator, DMM etc. Software's required for design and simulation of electronic circuits are also available.

DIGITAL LOGIC DESIGN LAB

Laboratory courses conducted: - Digital System Design I and II, Linear Integrated Circuits and Design. Lab is equipped with software's for digital and analog circuit simulation, FPGA and CPLD training kits, Digital measuring instruments etc. This Lab also extends support to the final year students for their Project Work.

ADVANCED MICROPROCESSOR LAB

The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. All the electronic testing equipments are available, Digital Trainer Kits and Demonstration kits (RAM and ROM), Universal Programmer. Cards for ISA, PCI and different connectors helps in better understanding of computer architecture.

POWER ELECTRONICS LAB

Laboratory courses on Industrial and Power Electronics are conducted in this lab. The Lab is adequately equipped with Test equipments for Power Electronic Devices and circuits, various demonstration kits and measuring instruments are available in this lab.

ELECTRONICS WORKSHOP - PCB LAB

Laboratory courses conducted: - Electronics Workshop - I and II, PCB Workshop. Lab Facilities - PCB Exposer, Film Exposer, Dip Coat Machine, Etching Tank, Drilling Machine etc. Students perform their Electronic Workshop project designing, testing and implementation in this lab.



Department Of Electronics and Communications Engineering



ANALOG and DIGITAL COMMUNICATION

Laboratory courses conducted: - Principle of Communication Engineering, Digital Communication, Mobile Communication Systems and other analog and digital communication related subjects. Lab is equipped with DSO-(Agilent, Aplab, Tektronics, Rigon), Spectrum Analyzer-3GHz (Agilent, Hameg), Signal Generators-

V.E.S Institute of Technology

AM/FM, Computers with latest configuration etc COMMSIM:- Network and Communication Simulation Software, Digital Communication Trainer kits like 16-QAM, QPSK, PSK, TDMD, Error correcting codes etc.

MICROWAVE and ADVANCED COMMUNICATION

Laboratory courses conducted: - RF Circuit Design, Microwave Devices and Circuits, Computer Communication Networks, Electromagnetic wave Theory etc. Lab is equipped with DSO-(Agilent, Aplab, Tektronics, Rigon), Spectrum Analyzer-3GHz (Agilent, Hameg), Standard Signal Generators for RF-range, Network Analyzer (Agilent), Transmission Line Trainer, Computers with latest configuration etc. Software like MATLAB, Ansoft, and PCADD are used simulation.

NETWORK and WIRELESS COMMUNICATION

Laboratory courses conducted :- Discrete Time Signal Processing, Computer Communication Networks, Image Processing, Wireless Communication, ICE etc. This LAB consists of 25-PCs with latest configuration, server, Wirless LAN, PDA's and Mobile phones for experimental purpose. All computers are installed with necessary simulation software's.

OPTICAL FIBER COMMUNICATION

Laboratory courses conducted: - Optical Fiber Communication, MOC. This Lab also extends support to the final year students for their Project Work. The lab consist of the latest equipments/softwares like OTDR, Optiwave, Trainer kits for calculation of IV Characteristics, WDM, Chromatic dispersion analog and digital signal transmission, 1550nm and 1310nm optical sources etc.

T.V. and SATELLITE COMMUNICATION

This lab supports conducting practical and some project work for SE to BE students. The practicals conducted for the subjects like TV and Video Engineering, Satellite Communication, Antenna and Wave Propagation, RADAR etc. equipments like different TV models, B/W and colour pattern generators, Satellite communication module, and complete setup of cable- TV and CCTV etc.





Department Of Computer / Information Technology / MCA









Department Of Instrumentation Engineering

PROCESS AUTOMATION LAB

The Process Automation Lab aims at training the students in the field of Process Instrumentation, control and optimization. PLC and Emerson DELTAV DCS controlled systems are also used to train the students.

PROCESS INSTRUMENTATION LAB

The Process Instrumentation Lab., where the study of some major process control instruments /equipment's is done. This lab covers mainly the digital and analog I/O PLCs of Allen Bradley and Technimount companies, PC based flow and level process control loop, Ellipse SCADA based Ratio and Cascade controls and pilot plants setups,

Differential Pressure Transducer (DPT) Calibration set up , I/P Converter, Control Valve Positioner, Control Valve Sizing set ups, and Hydraulic and Pneumatic trainers.

TRANSDUCER LAB

This lab is equipped with all the latest transducers used in the industry.

BIOMEDICAL /ANALYTICAL /NUCLEAR INSTRUMENTATION LAB

This Laboratory has been established to familiarize the students with capturing, analyzing and processing different bio signals. Biopac is an integrated instrument which the students use for such experiments. Physiology Lab kits and digital temperature feedback system are also used for training the students in the area of Biomedical Instrumentation.



CONTROL SYSTEMS and DIGITAL SIGNAL PROCESSING LAB

The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. Digital Trainer Kits and Demonstration kits (RAM and ROM), Universal Programmer, Cards for ISA, PCI and different connectors in the lab, aids better in understanding of computer Architecture.





ANALOG and DIGITAL LAB

Laboratory courses on Analog & Digital Electronics are conducted in this lab. The Lab is adequately equipped with Test equipments for Electronic Devices and circuits, various demonstration kits and measuring instruments are available in this lab.

13. INDUSTRIAL COLLABORATION:

1. AIA (AUTOMATION INDUSTRY ASSOCIATION):

VESIT is the only technical institution in Mumbai which is authorized by AIA (automation industry association) to be a 'Competency Development Centre' for conducting course on "Industry Automation" as a part of its Campus Connect Initiative. The newly developed Integrated Automation Lab was inaugurated by Padmashri Dr. G. D. Yadav, Director, Institute of Chemical Technology, Mumbai on March 2nd 2016.



2. NI LabVIEW Academy:

V.E.S. Institute of Technology has started NI LabVIEW Academy in collaboration with National Instruments, (USA) **Bengaluru** on 9th July, 2013. This is the first of its kind in Mumbai region.

LabVIEW is a graphical programming software which provides comprehensive tools that are required for any type of test, measurement or control application in dramatically less time. After completing this course, the participants will be able to appear for CLAD (Certified LabVIEW Associate Developer) examination, conducted by National Instruments (NI), USA. The CLAD distinguishes basic proficiency in using NI LabVIEW to develop applications. The certification and course work conducted by academy is useful for developing expertise in the LabVIEW environment for career advancement and employment opportunities. NI LabVIEW Academy will provide strong platform for interaction with industries and R & D centers to undertake research projects.

Under this academy, six training programmes on "LabVIEW Core 1 and Core 2 course" were conducted. Total 69 UG and PG Engineering staff and students of Instrumentation, Electronics had participated and successfully completed this course.

3. E-Yantra Lab Setup Initiative (eLSI):

V.E.S. Institute of Technology is equipped with state of art facility for Robotics, e-Yantra Robotic Lab under eLSI (e-Yantra Lab Setup Initiative).

e-Yantra Lab Setup Initiative (eLSI) is initiative by IIT, Bombay under which colleges are encouraged to set up robotics labs. It is designed as a scalable and sustainable approach that addresses infrastructure creation and teacher training – to create an eco-system at the colleges to impart effective engineering education. This facility is availed to the students, so that they can focus on embedded system, robotics projects.



e-Yantra Lab is equipped with Firebird V , Spark V , Qu-Bot robots, various hardware, software platforms required to build develop embedded projects. Every year 20-25 teams of students participate in Robotic competition.

4. Accenture Lab:

"VESIT-Accenture Innovation Centre" is a specialized lab developed in accordance of VESIT's MoU with Accenture. This lab is used by Accenture to conduct their Head Start Foundation Program (HSFP) for students selected through campus placement, faculty development program and other similar activities.



5. Cloudera Lab:

The Big Data Analytics lab in collaboration with Cloudera provides a high end distributed environment for Big Data processing, through which training is provided in specialized area 'Hadoop' and projects are undertaken to apply the concepts using Cloudera tools. A six weeks course is regularly conducted for final year students.



6. VESIT D-link Academy Lab:

D-Link academy at VESIT started on 1st April 2015. This academy will train students in batches which will equip them for better placement opportunities. As a part of this venture a specialized laboratory is set up in Lab no. 406 [EXTC Dept.] for wireless communication.

Under this academy, our faculty members were trained by D-Link to conduct this course for our students. This is an excellent opportunity for our students to get industry certification in their own campus.

14. RESEARCH AND DEVELOPMENT LAB:

R&D Department undertakes various R&D projects from industries and works on various research problems for M.E and Ph.D. students in VESIT.

Some of the successfully completed R&D Projects are:

Electronic Radioactivity Simulator in collaboration with BARC, which was funded by BRNS for an amount of 22 lakhs. Multi-channel analyzer, Nuclear Counting System – in collaboration with Nucleonix, Hyderabad.

Analysis and Monitoring of Degradation of Insulation in H V Transformers and "Nuclear Detector Characterization" in collaboration with I.I.T, Mumbai, "Sensor network for Agricultural application" in collaboration with TCS and "Low level signal generation". Signal processing circuit for Rogowski coil based integrator with low time constant in collaboration with L & T.

Research Projects currently being carried out in collaboration with industries are:

Low level sensing (from 2mA onwards) in industrial environment, DC level shift correction for sensing true AC signal, Peak detection of low level signals in frequency range of 5KHz to 25KHz, High Speed Time to Amplitude Converter and Leakage detection using Fibre Optics with L&T.

Application of Kalman Filter to Nuclear Engineering Data and Bayesian Estimation of Nuclear Engineering Data with BARC.

Our Ph.D. students are carrying out research work in the areas of:

- Low Level Signal Processing systems.
- Reconfigurable ADCs
- Optimise Nuclear spectroscopy System
- Research in New Techniques for on-line monitoring insulation degradation
- Research for changes in characteristics of optical fibres with changes in various Parameters like temperature, vibration etc.

15. COMMON INFRASTRUCTURE:

15.1 LIBRARY

Our library boasts of more than 45,000 books and journals of national and international level. Along with technical updates via latest journals, students can also keep themselves abreast with the latest happenings in the world via Newspaper, Magazine and Internet facility. The library is divided in two sections:

- 1. Reference Book: This section stock's recent editions of textbooks. Student can borrow one book at a time against their Library Identity Card. The books/journals in this section are meant for reading in the hall only. The question papers of previous examinations conducted by the University of Mumbai are also available in this section.
- 2. Lending Section: Students can borrow two textbooks for verification from this section. Generally the students can borrow books for one week. A fine will be charged if a book is not returned within the given period.

Students are required to abide by the following rules of the Library. Any violation of these rules will lead to a fine or punishment by the Principal.

- a) Every student must submit his/her valid identity at the entrance of the library and the reading room. Ex-students of the college, who are members of Alumni Association, can use the library facilities.
- b) Pencil or ink marking and writing on the pages of the library books or any other way of mutilating the books will be treated as library indiscipline. The first reader will be held responsible unless he/she points out to the Librarian at the time of issue of the book.
- c) The reference books, journals, question paper sets, dictionaries etc. will be issued for reference in the Reading Room only and on no account can they be taken out of the Reading Room.
- d) Students must refrain from disturbing other members in Library and Reading Hall.
- e) The student should inform the Librarian immediately of any loss or damage to the books borrowed by him/her on the library card.

- f) Any other matter not covered by these Rules will rest at the absolute discretion of the Principal and Librarian.
- g) Student should not bring any valuable article to the Library. If such things are lost, the Library staff cannot be held responsible for it.
- h) Students should maintain complete silence in the Reading Hall.
- i) Students should contact the Librarian if they face any problem regarding usage of library facilities. They should not argue with the staff on duty.



15.2 OTHER FACILITIES

PROJECT ROOM

Separate Project rooms are provided for each department with the laboratory facilities and computers installed. The students can carry out their project work even after the college hours.

CANTEEN

The college offers a huge canteen, with modern kitchen facility, serving fresh and hygienic food which offers a multi- cuisine variety.

CONFERENCE HALL

Seminars and Students' presentations are held in the Conference Hall, which has a seating capacity of 250 students. It is equipped with a good sound system along with video and data projectors.

VESIT LEADERSHIPACADEMY ANDRE-SEARCH CENTRE (VESLARC)

To be successful in today's world of severe competition and stress, it is necessary to have the right attitude and positive thinking. At VESIT, the students are helped by the specially appointed faculty of VESLARC to develop a strong personality with leadership qualities.

MUSIC ROOM

Music Room is well equipped with instruments such as Flute, Violin, Guitar, Tabla etc. Students can learn and practice after the college hours. Student Council wing "Bliss" (Beauty lies in Soulful Symphony) is in-charge of Music Room and conducts music competitions and workshops for the benefit of students.

SPORTS

Life of an engineer is incomplete without any extracurricular activities. For a healthy body and mind, apart from the usual workload and stress, VESIT provides its students with a sporting extravaganza SPHURTI. SPHURTI is an integral part of VESIT's history, and it has been so since the beginning of VESIT.

- Play ground: 8177 sqm
- Recreation Room: Room No. 10 B, 108.49 sqm
- Outdoor games: Football, Cricket, Volley ball, Basketball, Throw ball, Athletics, Discuss throw, Shotput.
- Indoor games: Carrom, Chess, Table Tennis.

16. TEACHING FACULTY:

Our Institute is well known for its highly qualified and skilled teacher shaving a great deal of experience. They have contributed greatly in imparting quality education at our Institute. Our teaching faculty includes 18 Ph.D. Holders, 144 Post graduates (29 Pursuing Ph.D.) and 27 Graduates (23 pursuing P.G.). Each department also takes initiative to organize "Faculty Development Programs" for helping the teachers to update their technical knowledge. These refresher courses in turn helps the teachers to deal with their subject and the projects in a professional manner. The staff members are also encouraged to acquire higher qualifications.

17. STUDENTS COMMITTEE/ACTIVITIES

Professional Societies

A unique feature of VESIT is the fact that there are four Professional societies totally managed by students under the supervision of Faculty In-Charges.

1. IEEE - Institute for Electrical and Electronics Engineers

"Excellence through Innovation" is what the IEEE VESIT stands for in VESIT society culture. IEEE-VESIT gives its members ample opportunity to develop and inculcate their technical and soft skills, be it in theory via seminar or in practice via interactive workshops.

In-house Publication "Innovations"

Mumbai-level "Short circuit"

Publication

Symposium "Melange"

Website www.ieee-vesit.org

Aptitude test, Tech Debates' and 'Technical Paper Presentation', Technical Quiz and Test, Glider making workshop, Technical Article Writing Competition, Workshop on 'Linux Operating System', Industrial visit, Level-1 Robotics, Web Designing workshop, Online Treasure Hunt, Workshop on Image Processing, Rubik Cube Workshop, Workshop on '3D Photography', Speed Maths Workshop, Robofest, Computer lab setup for an orphanage, Eagle software workshop, Python software workshop, AVR software workshop, Dream weaver software workshop, Moviemaker software workshop.

2. ISA- Instrumentation Systems and Automation

ISA is a leading global non-profit organization that caters to the needs of over 30,000 worldwide members and other professionals. ISA-VESIT student chapter is well rooted with the international organization. ISA-VESIT chapter expose its members to the upcoming competitive world and tries to give them an edge over the others when it comes to practical knowledge through various events and seminars.

In-house Publication "Envisage"

Symposium "Enguage"

Website www.isa.org

Activities during the Year

Conducts Technical Events related to Instrumentation like Mastermind, LT Spice workshop, Junkyard Wars, Technical Article writing, Survivor, Vanquisher's Quest, Technical Paper Presentation, Online Treasure Hunt.

3. CSI- Computer Society of India

The CSI VESIT stands for ingenuity, innovation and versatility. CSI-VESIT aims to give its member an added dimension to technology by keeping them abreast with the rapid advancements in the wide arena of technology. It is one of the most active student societies with the objective to accelerate student expertise in the field of technology. The CSI-VESIT student chapter has been retaining, the award of The Best Student Chapter for the past seven years.

In-house Publication "Random Thoughts" and

"Nexus"

National Board "Communications"

Annual magazine "Interface" Symposium "Impact"

Website www.csi-vesit.org

Activities during the Year

Conducts Technical Events related to software's like Code Kill, Code Breaker, .Net workshop, The Ultimate Coder, LAN gaming, Swishmax workshop, Cric-o-Mania, Open Software, Online Stock Market, Comp-o-nect, Technical Paper Presentation, Work shop on PC assembling, Work shop on Flash, Work shop on Robotics Studio, Quiz on C++/Java,

4. ISTE-Indian Society for Technical Education

VESIT-ISTE chapter's objective is to bridge the gap between the curriculum and the industry's requirements by giving students a platform to hone their technical and non-technical skills like overall personality, reasoning and presentation skills.

In-house Publication "Mirage"

Symposium "Synergy"

Website www.iste-vesit.org

Activities during the Year

Mock CAT, Mock interview, Resume writing, personal interview, Aptitude test, Mock Placement, Mathand Logic Quiz, Group Discussion, Inter SE Debates, Technical Paper Presentation, Vedic Mathematics, Article Writing Competition, Seminar on higher education, Seminar on financing further Education, Industrial visit, JA TITANS, Orientation lecture (PG), Self-evaluation test, Diagnostic CAT

PRAXIS:In order to be successful, one must have a competitive spirit and a positive attitude towards learning from mistakes. To nurture and promote this competitive spirit, VESIT organizes PRAXIS every year. PRAXIS is our annual inter-collegiate technical festival which has an extremely competitive Robotics event that has become a much awaited event among engineering students. It is gaining popularity not only in Mumbai University but in other Universities as well. PRAXIS also develops a sense of team spirit and event management skills along with a sense of belonging. Cultural Activities: In spite of all these activities, the college does not neglect cultural talent.





UTSAV: The College organizes UTSAV, a cultural festival that showcases cultural talents. It witnessed overwhelming participation from our students.

OCTAVES: A Music fest which was organized by the Music Council of VESIT. There was tremendous participation in this festival. For the first time in the college an Intradepartment music festival had an entry of 100 students and made a mark to the music club.





ANNUAL DAY is a cultural extravaganza which is a complete event management exercise for our students under the supervision of our faculty. Annual prize distribution is also held on the same day.

Teacher's day The College organizes Teacher's day function for staff members every year. Teachers completing 20 years of service are felicitated on this day.

BLISS stands for "Beauty Lies in Soulful Symphony". BLISS is the musical group of our college which is run by students passionate about music. BLISS has successfully organized many musical events since 2010. Separate music room for students is provided to practice with very well equipped instruments from classical to western.





Technology Day

On Technology day, we organize project exhibition and poster competitions for our final year and pre-final year students. Students actively participate and demonstrate their research ability as well as get to know the advancement in technology. This day is dedicated to the memory of our founder trustee "Shri. K. B. Mansharamani".

VES PREMIER LEAGUE: This is organized by a group of Football enthusiasts from our college in collaboration with other VES members. It comprises of 8 teams with 15 players in each team.

VESIT CRICKET LEAGUE: This is similar to the VESIT Premier League but for all the cricketer lovers of VESIT. It is conducted in the IPL style, where it auctions players with each captain given virtual money to participate in the auction. Over 160 students participated in the program last year.

SoRT - Social Responsibility Team

Every person in the world has a will to help improve the society, but due to personal constraints or the lack of proper platform, they might not be able to do so. The inception of "SoRT – Social Responsibility Team", was to channelize these willingness and small efforts into a larger stream. SoRT tries to spread happiness in every way it can. It endeavourers to plant the seed of social responsibility in every Vesitians' heart and hopes to touch more and more lives every year. Apart from the routine college competitions, our students eagerly wait and enthusiastically participate in other colleges/university, competitions and festivals.









SoRT WEEK:



The SoRT week was organized during 24thAugust 2015 – 1stSeptember 2015. This year the SoRT week was collaborated 6 NGO's- Balanand, NASEOH, Self Esteem Foundation, RUR, JPS and Vivekanad Youth Forum.

Products from these NGO's were brought to our Institute and sold, the entire money collection was handed over to the respective NGO's. A variety of products were put up on display provided by these NGOs which included folders, bags, handicrafts, books and writing pads which were recyclable, candles and many more. SoRT managed to help needy NGOs by selling their products in the college.

On the whole SoRT week lived up to its expectations like every year and continues to believe in the Joy of Giving.

Indeed true to its motto- "Be the wings the world needs".

ORGAN DONATION DRIVE:

SoRT-Vesit in collaboration with Shatayu organised a sign up drive for organ donation on the 22 October, 2015. The desks for the same were set up from 10:00 am to 5:00 pm.

The overwhelming response was received, with over 100 registrations on the spot. Many students and teachers took registration forms home for their family members too.



BLOOD DONATION CAMP:

On 22ndJuly 2015, SoRT VESIT organized one of its most awaited events the blood donation drive, at the college premises, on the eve of the birth anniversary of late Shri Hashu Advani.

The drive comprised of various stages such as weight check, haemoglobin count, calcium count and blood group identification. The presence of an orthopaedic surgeon from the St. George's Hospital proved to be a boon to many, as did the provision for the test for thalassemia.



The response to the event was tremendous with over 110 registrations.

Professional doctors and experts monitored the donors and made sure that the procedure went smoothly and the donors were comfortable.

Separate desks were set up to serve the donors refreshments. All the donors were awarded certificates as a token of appreciation for their good deed.

VISIT TO JOANNA E BUTHELLO SCHOOL:

The 31st January 2016 saw SoRT VESIT visited the Joanna E Buthello School in Malad. The members of SoRT left for the school at around 11:30 and were with the kids by 1:30. The SoRT team made it a point to spend quality time with all the kids there, and entertained them with various activities like dancing and singing to playing passing the parcel. The team had also taken food supplies for two days meals for the kids and also various donations as given by the students. Towards the end of the day most of them had become so attached that no one wanted to leave. We bid them farewell and came home with a promise of future visits.

CANCER WALK:

In keeping with the tradition, the SoRt team once again took to the streets to spread awareness against Cancer. In collaboration with MediGene and WeChange, the team organised a cancer awareness walk on February 4, 2016. "WE CHANGE" is an NGO started by our alumni Mr. Sridhar (2015).

The walk started from Azad Maidan at 10.00 a.m. and ended at the Gateway of India. With more than 100 students of the college walking the streets, wearing

distinct t-shirts and holding banners to display the cause, the walk was a huge success. Cancer has been a big cause of terror, and it has been so since 1600 BC! It is the largest contributor to the human death toll, having already killed more than 2 million people this year, and its only April!

SANITATION PROGRAMME:

SoRT is now moving to the new challenge of sanitary hygiene of society. Sanitation is the basic amenity and need of a healthy environment. If one has time to lean then he must have time to clean. Looking forward for a healthier society in order to raise the living standard of humans raised the need to focus on sanitation. Health is the foremost notion when SORT thinks of sanitation. In India, many villages still lack knowledge and are unaware of the importance of sanitation. Hence, SORT this year accepted the challenge of providing one such village with its basic rights for health and hygiene. Sakhare is a quaint little rural area near kalyan with the population of over 200 people at approx. It was chosen to be the part of the small step taken by SORT.SORT has planned to build at least two toilets for this village in order to setup a model of sanitation and hygiene under health priority.

SWACHH BHARAT ABHIYAN (Campaign Clean India)

Swachh Bharat Abhiyaan proves to be one of the most successful initiatives across the country as citizens are keen enough to change the opinion of cleanliness of the nation in their minds, the students of VESIT too proved to be mindful of this thought as they gathered to clean Chembur Station on the 7th October, 2015.

The clean-up drive kick started at 9:00 AM with a pledge to keep our environment clean. The pledge was taken by commoners and students alike. Students from every department were enthusiastic regarding this event and participated in huge numbers. The BMC provided students with necessary cleaning equipments. It was indeed an inspiring sight to see so many students work towards a cleaner brighter country. All departments also organized the cleanup drive in the institute. Active support was seen by students as well as teachers for the same. The cleaning provisions were provided by the Institute. Each class was allotted with one class room & laboratory.



■ शहर कचरामुक्त करण्यासाठी व्हीईएस इस्टिट्यूट ऑफ टेक्नॉलॉजीच्या विद्यार्थ्यांनी स्वच्छता मोहीम हाती घेतली. रेल्वे प्रशासन व म्युनिसिपल कॉर्पोरेशन ऑफ ग्रेटर मुंबई यांच्यातर्फे आयोजित या उपक्रमात इंजिनीअरिगच्या जवळपास २०० विद्यार्थ्यांनी सहभाग घेतला. या मोहिमेची सरुवात चेंबर रेल्वेस्थानक व सभोवतालच्या परिसरातन दाली.

टैलंटिली

- द व्हेजिटेरियन सोसायटी : 'हास्य आणि पर्याची औषध' या विषयावर किशोर कुवावाला यांचे व्याख्यान. केसी महाविद्यालय, दिनशों वाख्या रोड, चर्चगेट. संध्या ६ वा.
- भारतीय जनता पार्टी : 'आपला विश्वास' जनेरिक मेडिकल स्टोअर्स उद्घाटन समारंभ. प्रमुख पाहुणे- अन्न व नागरी पुरवठामंत्री गिरीश बापट. दामूनगर-भीमनगर, कांदिवली (पू) संध्या ६:३० वा.
- विवेकानंद एज्युकेशन सोसायटीज इन्स्टिट्यूट ऑफ टेक्नॉटॉजी : महाविद्यालयीन विद्यार्थ्यांतर्फे चेंबूट स्थानकाची स्वच्छता अभियान. चेंबूर टेल्वे स्थानक (पू) सकाळी १०वा.

RUN FOR UNITY

The Governor of Maharashtra Shri. Vidyasagar Rao flagged off 'Run for Unity' a 2 – km long run. Shri. Devendra Fadnavis, the Chief Minister designate led the run for unity from the front. Union Ministers Prakash Jawdekar, Education Minister Shri. Vinod Tawade, Piyush Goyal and Raosaheb Danve also took part in the race.

Prior to the start of the race, the Governor gave a 'Rashtriya Ekatmata Pledge' to the participants of the run. The run started from Air Inida building and culminated near the Parsi Gymkhana on the Marine Drive in Mumbai. **VESIT Non Teaching staff and students participated in "Run For Unity"**



INTERNATIONAL YOGA DAY

This year we got opportunity to learn the Yoga lessons from Shri. Ramesh Lakshman, Chartered Accountant with additional qualifications in Law and cost accounting. He runs his own firm of Chartered Accountants, he is also visiting faculty with SP Jain School of Global Business at Singapore and Dubai. He is a studious practioner of Yoga and his yogic practice has completed 24 years. Students and staff of VESIT and VESACS were very impressed with his sessions on Introduction & Importance of "Yoga" and his practice session: Aasana, Pranayama &Meditation. This session was held on Sunday 21st June 2015 at 9.30 am. Venue of the session was VESIT Amphitheatre.



RASHTRIYA EKTA DIWAS

VESIT observed 31st October 2015, Sardar Patel's Birth Anniversary as the Rashtriya Ekta Diwas.



NATIONAL EDUCATION DAY

Poster making and Essay Competition has been organized with the theme "Nation's commitment to all aspects of Education" for the occasion of National Education Day (11th November).

CONSTITUTION DAY

Constitution Day was celebrated on 26th November 2015. The Preamble of the Constitution of India in English and Marathi was displayed at the entrance lobby and Library. An exhibition of books Constitution of India and Dr. B. R. Ambedkar was organized in Library during the period 26th Nov. - 7th Dec. 2015



INTERNATIONAL WOMENS DAY CELEBRATION

To pay tribute to the indomitable spirit of women, International Women's day was celebrated in VESIT on March 08, 2016 from 1.00 pm to 3.00 pm in the Auditorium under the leadership of Women's Development Cell. On the eve of International Women's Day Special session was organized in the College. The invited guest speakers for the session were Mrs. Neelam



Bhardwaj- Certified Resource Person of SEBI and Mr. Bhavadasan – Retried Chief Engineer from Johnson & Johnson Pharma Division.

In the Presentation, Mrs. Neelam Bhardwaj provided insightful address on topic "Financial Awareness" for Women. In this, she covered Financial Freedom and Financial Sustainability and managing various events in life such as Routine, Predictable and Unpredictable events using SII (Saving Investment and Insurance). She also focused on principles to follow for assurance of availability of financial funds at various stages of life. And given financial tips for Single Women with respect to Pension Plans and Health Insurance. Moreover, she highlighted Do's and Don'ts when buying Financial SII Products.

After the Presentation, Mrs. Neelam Bhardwaj and Mr. Bhavadasan jointly conducted an interactive session and addressed queries related to National Pension Plans, PF and PPF with respect to Tax Exemption. Also, emphasized on importance of Pension for every individual and advantages of it for happy and peaceful life.



Sanitary Napkin Vending machine was inaugurated by Mrs. Neelam Bhardwaj on the occasion of International Women's Day.

CELEBRATION OF 67TH REPUBLIC DAY

India celebrated its 67th republic day with pride on 26th January 2016. VES also organized a ceremony at the VES Polytechnic ground. The members of various VES institutions assembled to celebrate the Republic Day. The Chief Guest for the occasion, Mr.Sangramsinh Pratapsinh Nishandar (Deputy Commissioner of Police, Zone-VI) hoisted the Indian Flag. Following the flag hoisting, there was a parade of the student scouts followed by the cultural programme presented by the different institutes. The ceremony concluded with national anthem and a grand salute to the national flag.

NATIONAL YOUTH DAY- IGNITING YOUNG MINDS

"Take risks in life; if you win, you may lead....if you lose, you may guide." -Swami Vivekananda

National Youth Day is celebrated in India on 12th January on the birth of Swami Vivekananda. His writings and lectures have always been a great source of inspiration.

Maintaining this legacy, a number of activities were conducted in VESIT by Vivekananda Kendra, Mumbai in January 2016.

The celebration of the day commenced with a session on "How to live successful and purposeful life" in the auditorium on 7thJan 2016 in two different slots for the First Year engineering students.

The students were addressed by Mrs. Sujata Dalvi (Head of Mumbai Branch) and Mr.Ramkrishna who briefed the students about the life and work of Swami Vivekananda which attributed to his success.

This session was followed by another on 12thJan 2016 for all the Student Council members. A short video clip on the life and work of Swami Vivekananda was showcased to enlighten the young minds about the unique ideology of this philosopher. The video briefed about the extraordinary achievements of Swami Vivekananda.

This session was delivered by Mr. Abhay Bapat (Head of Maharashtra region. Vivekananda Kendra). The event witnessed a huge crowd of about 200 students. The event ended successfully with a short questionnaire to which Mr. Bapat responded graciously.

The events concluded with an exhibition cum sale of posters and books on Life of Swami Vivekananda. It was organized in the college premises luring the young to tread the path of this great saint.

MARTY'S DAY

Every year, 30th January is mourned as Martyr's Day (Hutatma Din). On this day we pay our respects to the brave freedom fighters and all those valiant soldiers who

sacrificed their lives for the sake of the nation. This year as well the Government of India had issued a circular to all the institutions in the country to maintain two minutes of silence to pay homage to the martyrs. All the institutes including VESIT as instructed maintained the two minute silence at 11.00 a.m. and paid a heartfelt tribute to all the departed brave souls.



ENGINEER'S DAY

15thSeptember was celebrated as Engineers' Day marking the birth day of Sir Mokshagundam Visvesvarayya. Presentation on activities conducted.

"Disaster Management / Mock Evacuation Drill"

Mock Evacuation Drill was successfully conducted at VESIT on 27thFebruary 2016 by Freedom For You Foundation, NGO for Empowerment of Citizens. The drill was followed by hands on workshop for students and staff on the demonstration of Fire Safety Equipments.





Marathi Bhasha Diwas on 27th February 2016





Road Safety Week:

Road safety is very vital as road accidents amount to large number of deaths. To create awareness the Ministry of Road Transport & Highways had organized the Road



Safety Week from 11th January 2016 throughout the country to improve the safety on road and to give an opportunity to all stakeholders to contribute to the cause of road safety. During the week, awareness about various causes of road accidents

and measures to prevent them were highlighted by

organizing various activities with School / college students, drivers and all other road users. VESIT also took immense interest in celebrating this week. Poster making competition for the students to spread the



message and importance of road safety was conducted by VESLit Circle. Various banners were posted on notice boards to promote road safety.

SHRESHTHATA AWARD

The 'Shreshthata awards' is an initiative by VESLARC (VES Leadership and Research Centre) to acknowledge and encourage students who endeavor to make a difference in spite of unaccommodating circumstances. This year nine students from the VES group of colleges received this much sought award. The award ceremony was held at the VESIT auditorium on the 15th of October, the birth anniversary of the nation's beloved A.P.J. Abdul Kalam and also World Students Day. The auditorium was packed to the brim with proud parents, cheering friends and well-wishers of the awardees.

The Chief Guest for the occasion was Dr. Sunil M. Keswani. The special guests also included the Treasurer of VES, Mr. Amar Asrani, the President of VES, Mr. Mahesh Tejwani and Dadi Vaswani. The event was also attended by staff members and students from various colleges under the VES fraternity. The function began with the traditional lighting of the lamp by the honorary guests and a prayer song by one of the students. Then the hostess for the day went on to explain the importance of the day and gave a brief description of the awardees after which they were called on to stage amongst a loud round of applause.

Each one of awardees was handed out a star and a candle and was asked to take an oath, vowing to use their skills to be future change agents of India. The oath was taken not only in English but also in Hindi. These special children were then given a chance to speak about themselves. Aditya Thakur, one of the awardees, inspired the audience by telling them that self-reliance is the best way to help themselves. The other awardees too gave beautiful speeches that held the audience captivated. The hostess's timely and apt rendering of quotes in Sanskrit and English also impressed the listeners.

The chief guest, Dr. Sunil Keswani, also charmed the audience with his humorous nature and delightful anecdotes. He stated that, although he was one asked to motivate the students, it was he who was the one going back inspired. His best sentence might as well be 'You do your best and God will do the rest.' The program culminated with an interactive question and answer section. Students and all others present were encouraged to question these young inspirations, which led to some interesting dialogue. The event concluded with the National Anthem. The show might have ended there but the seeds of change were planted in the minds of the attendees.

Digital India Week

VESIT celebrated Digital India Week during 2nd to 7th July 2015.A series of lectures towards awareness of the Internet usage, Security and Cyber laws were given to students and staff.

- o Digital India Initiativeby Ms. Sunita Suralkar.
- o e-Services and m-Services by Ms. Garima Shrivastava
- o Cyber Security by Ms. Anuradha
- o Digital Literacy by Ms. Roopali
- o e-Waste by Ms. Pooja Nagdev
- o Cyber law and Digital Locker by Mr. Prashant Kanade.



Make in India Hackathon

Harsh Gupta and Nirbhay Pherwani won the first prize in the Make in India Hackathon held at IIT Bombay. Their project titled 'Subsurface irrigation model with a sensor based crop prediction' is a low cost irrigation solution for draught hit farmers









18. PRESS RELEASES

"The Free Press Journal" page no. 20on 2nd November 2015. Principal's interaction with education page editor Mrs. Shraddha Kamdar.



Skill-building academy

UK-based 225 Academy is conducting a five-day five-days including Communication skills – dove –focussing on development of skills to make the project, blog, or business, find investors, envisage think positively, identifying strengths and overco

"APTITUDE GETS YOU A JOB, ATTITUDE TAKES YOU FURTHER

Dr. J. M. Nair, Principal, Vivekanand Education Society's Institute of Technology (VESIT), Chembur, offers her message to all students through an interaction with Shraddha Kamdar

of them are arready many and the students. In fact, in the first year inself, the students.



Interaction with Manish Bharti student of VESIT, B.E last year. His dream of starting a school and educate under - privilege society through his NGO Kartavya Foundation has been highlighted.

FINANCIAL AID II Through his NGO, BTech student Manish Bharti provides monetary assistance to underprivileged students

Helping others in realising their dreams

Bharti – who is a last year BTech made it his life's mission to help students from (VESIT) – founded students in need. We completed

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हुंस् व्हावकीर मा क्या क्षेत्रका स्वेत क्षिप्र व वाल्य महुव नाम पुरस्क क्षेत्र क्षेत् ्त्र जा उक्तरणा वर्षक विद्यार्थः सहिद्यिद्यालयोव व्यामध्ये स्वित्यालयोव व्यामध्ये स्वित्यालयोव व्यामध्ये स्वित्यालयोव व्यामध्ये स्वित्यालयोव व्यामध्ये स्वत्यालयो क्रिया क्रांत अभ्याल आणि प्रमुख्याने प्रकारण अभ्याल आणि प्रमुख्याने प्रकारण अभ्याल आणि

पुम्नवामे गैनक्यात असे. करिअवकर लक्ष केंद्रित गाण विद्याले मण्यून मानेस्ता करवाये देव बहुतेकांचे ध्येय इंबरर केवाये स्वय सक्य असते. परंतु इतित सुनावा क्सावचे होते. पत्, पुरेक्षा निर्धाच्या जनवानुके ज्वला विवान ताला प्रदेश ज्वला लागन अधि नामें वृद्धे इतिमाशीस निवासे, अशा वैद्यों तरुजानी, विश्वणापासन रत्यावने नीवायता येथ दिना अस्ता, कनु नरीको वा अनुभवाना उसके, विकार महिलेल्या गरीब मुलांसाठी यह अमलेल्य आणि

वींका व वर करोति गरू करणाचे भोरतंतुमा रहेको एक राजारोवी रूपमा लागाची उत्पादका हूं जिलाहा हैता. तमे ११,००० वो नगरूरी "हुमारीमानू, पहल वींकाबोर राजी सामग्री है कर मोला वृह करणामणे प्रध्य मुख करणामणे महत्त करें वीत्रभावेत राशों बातमा है तम स्वेता कुत कामामार्थ प्रध्य मुद्द कामामार्थ प्रदान कोई स्थान किया कामाप्या महत्व अध्यापक कर्म तमार्थ के अपना के के अपना क्षेत्र कामाप्या महत्व अध्यापक कर्म तमार्थ के अध्यापक कर्म तमार्थ के अध्यापक कर्म तमार्थ के अध्यापक कर्म तमार्थ कर्म तमार्य कर्म तमार्थ कर्म तमार्थ कर्म तमार्थ कर्म तमा

मदत करणे हे जगावेगळ आहे ते मनीव भारथी या तरुणाने. शिक्षणापासून

अवृद्ध प्राले काणि नस्या मुंदर्जुरील त्यांचा वायम काणी रहाँगः." विकिय पदवी महाविद्यालयातील व्योद्धमः इनिटस्यूरमध्ये अमे १०० हर अधिक स्वयंगेयक कर्तव्य असेक तस्त्र मेरे आहेत. ज्यांच्यत काठकेतनच्या विविध उसक्रमात समाजाच्या सत्यागासकी बदल कार्यस्त आहे. आसार्यस्था नामानः यहत् आण्याची क्षम्त आहे. समाप्रानी न रहता, न बांबत, गाँव, त्यांची हो आजाने सनेपनाराया इस यह विद्यार्थना नोपत हिसमा हरणांना प्राप्त सरण्यामाठी वर्गस्त्रवं

पुनवणामही २०२४ मानायाँन जाता. समाप्र आहेत होवटी, आयुष्यत पुन करप्याचे मंत्रिये स्वन आहे. बेवळ गुण विख्वणे (हे गारेचे मंत्रिते तहीं वेजोत्या च योज्या असले तही) विंवत अध्यासाव्यक्तित

स्वाभाग वे हत जगा, विकास विहित्सा महीद मुन्ति होते हैं स्वाभाग के स्वाभाग के स्वाभाग के स्वाभाग के स्वाभाग के स्वाभाग के स्वभाग के स्वाभाग के स्वभाग के स्व

MidDay_27_Nov_2015_pg_11_VESIT

Marathi Newspaper: Navakal 30th Nov. 2015



■ शहर कचरामुक्त करण्यासाठी व्हर्डिएस इस्स्टिटबूट ऑफ टेक्नॉलॉजीच्या विद्यार्थ्यांनी स्वच्छता मोहीम हाती घेतली. रेल्वे प्रशासन व म्बुनिसिपल कॉर्पोरेशन ऑफ ग्रेटर मुंबई यांच्यातर्फे आयोजित या उपक्रमात ईंजिनीअरिगच्या जवळपास २०० विद्यार्थ्यांनी स्वस्भाग घेतला ऱ्या मोहिमेची सरुवात चेंबर रेल्वेस्थानक व सस्रोवतालच्या परिसरातन बाली.



રેલ પાસે સફ્કાઈનો ખેલ : સ્વચ્છ ભારત ઝુંબેશ હેઠળ વીઇએલ ઇન્સ્ટીટ્યુટ ઓફ ટેકનોલેજીના સેંકડો વિદ્યાર્થીઓ અને વિદ્યાર્થિનીઓએ બુધવારે વેમ્યુરસ્ટેશન પરિસરમાં ગેઠકીઅને ક્યન્ને હટાવવાનીકામગીરી જોરશોરથી પાર પાડી હતી. મહાપાલિકાના 'એમ' વોર્ડ ઓફિસની મદદથી સ્વચ્છતા ઝુંબેશ હાથ ધરવામાં આવી હતી. (તસવીર : પ્રસાદ લોકે)



▶▶ સ્વચ્છતા અભિચાન… વી. ઇ. એસ. ઇન્સ્ટિટ્યુટ ઓફ ટેકનોલોજીના સોથી પણ વધારે એન્જિનિયર વિદ્યાર્થીઓએ બીએમસી એમ વોર્ડની સહાયથી સાતમી ઑક્ટોબરના ચેમ્બુર સ્ટેશન અને તેના પરિસરમાં સ્વચ્છતા અભિયાન હાથ ધર્યું હતું. વિદ્યાર્થીઓએ સ્ટેશનની બધી દીવાલો પર રંગ કામ કર્યું હતું તેમ જ સંપૂર્ણ વિસ્તારમાં સાફસકાઇ કરી હતી. (અવ્યવસો)

टेवांटिकी

- द व्हेजिटेरियन सोसायटी : 'हास्य आणि पर्याची औषध' या विषयावर किशोर कुवावाला यांचे व्याख्यान. केसी महाविद्यालय, दिनशॉ वाच्छा रोड, चर्चगेट. सध्या ६ वा.
- भारतीय जनता पार्टी : 'आपला विश्वास' जमेरिक मेडिकल स्टोअर्स उद्घाटन समार्रभ. प्रमुख पाहुणे- अन्न व नागरी पुरवठामंत्री गिरीश बापट. वामूनगर-भीमनगर, कांदिवली (पू) संध्या ६:३० वा.

विवेकानंद एज्युकेशन सोसायटीज इन्स्टिट्यूट ऑफ टेक्नॉर्जी : महाविद्यात्मीन विद्यार्थ्यांतर्फे वेंबूर स्थानकाची स्वकता अभियान, वेंबूर रेल्वे स्थानक (पू) सकाळी १०वा.

ENGINEERING THE CHANGE



Around 200 engineering students from VES Institute of Technology, along with BMC M ward and Central Railway officials, launch a clean-up campaign at Chembur railway station on Wednesday. —Hemant Padalkar/dna

होईएस विद्यार्थ्यांची स्वच्छता मोहिम

इन्स्टिश्वट टेक्नालॉजीच्या विद्यार्थ्यांनी आज स्वच्छता मोहीम हाती घेतली. या मोहिमेची मुरुवात चेंबूर येथील रेल्वे स्थानक व सभोवतालच्या परिसरापासून विद्याध्यांनी त्यांच्या स्वच्छता करण्यासाठी वेळ शपथ देण्याची धेतली पोस्टर आणि फलक पांच्यासह, छचरामुक्त मुंबईचा संदेश देण्यात आला. त्यामध्ये शहराला धनकचन्यापासून मुक्त प्रमाच्या भर देण्याच्या करण्यावर भर दण्याच्या तातडीच्या गरजेला प्राधान्य देण्यात आले. विशाश्यक्तिया TES

विद्याध्याच्या या
उपक्रमाविषयी सांगत,
व्हीईअएसआयटीचे प्राचार्य
हॉ. (सी) जयलक्ष्मी नायर
हं. (सी) जयलक्ष्मी नायर
हं. (सी) जयलक्ष्मी नायर
हंतासाठी विविध्य
सामाजिक विषयांबावत
पुढाकार घेतला आहे.
प्रत्येक चांगते कार्य स्वतः
पासून सुरू व्हायला
हवे आणि मार्वजनिक
ठिकाणांची स्वच्छता आम
च्यासाठी नेहमीच महत्वाची
गहिती आहे.



Over 200 engineering students of Vivekanand Education Society's Institute of Technology (VESIT), in association with BMC undertook a cleanliness drive near Chembur railway station. While Dr Vijaylakshmi M, Vice-Principal, led the students in the drive, Principal Dr (Mrs) Jayalakshmi Nair said that the institute has always been in the forefront of initiating social causes. Vijay Patil, Assistant Engineer (Solid waste Management Dept), MCGM -M ward, was all praise of the students.



प्रत्येक जण आपापल्या परीने स्वच्छ भारत अभियानाला हातभार लावत आहे. व्हीईएस इन्स्टिट्यूट ऑफ टेक्नॉलॉजीच्या विद्यार्थ्यांनी मुंबई महापालिकेच्या मदतीने एम विभागात स्वच्छतेचे अभियान राबविले. अशा प्रकारे प्रत्येकाने प्रयत्न केले तर भविष्यात कथी तरी स्वच्छ भारतचं स्वप्न साकार होऊ शकेल.



UNIQUE Volunteers team up with police to solve problems faced by senior citizens

NUMBAL College students in the city are disching run of the mill activities for social change and are adopting innovative ways to help the society. Colleges are not limited to organizingblood donation drives or safety campaignsalonebutarealsotaking more proactive steps in ensuring an

on-ground change Mithibai College in Vile Parie is a prime example of this as it has recently adopted Ville Parde railboay station which is very close to the institution and used by students for commute. A group of 25 stadents visit the station every week to sprace it up and also put up messages about keeping it clean.

The management of the college has funded all the supplies and according to the principal, Dilip Karrach, this is a long term project

Another collegeto have adopted a railway station is Vivekamanda Education Society's Institute of Technology (VESIT). As a part of the college's campaign Yuva for Seva' the students have adopted Chembur railway station and have cleaning the platforms to installingdustbins. Since the beginning of the year students have been tion along with help of the Vedant Welfare Foundation, an NGO.

'Apart from cleaning up the facilities, clean bathrooms and safefood is a priority. We are showly working on getting it all fixed

OUT-OF-THE-BOX

Social work has taken up a new form ing on-ground efforts.

like blood donations camps and cleaniness drives, colleges are taking upresoonally littles of maintain-ing railway stations near their premises too. Mithibai College, Wilson College and Vivekananda Marine lines and Chembur railway

Students of Bombay veterinary college will be organising a pet adoption drive this month to help rescue stray animals. Similar other initiatives are being carried out by other

Senior officer survey by the Numbel police includes NSS students taking up the responsibility of finding out problems that plague senior citizens

A vide about a LGEY friendy IT-B camous by the resource group Saathi

begunits maintenance, rightfrom Railway stations are extremely important and used by almost Service (NSS) students across every Mumbai resident however colleges are involved in a senior no one cares about keeping it citizen survey to identify problems Institute of Technology Bombey involved in maintaining the sta-surved up" said Ament Devivedia and issues of senior citizens liv- (IIT-B) are making sure that the second year student of the college, in galance in the city The Mumbai dialogue about rights of the queer

The college base after taken up police is pioneering this survey community does not die out. the responsibility of Kings Circle and according to data collected place, other issues like medical station as well. The Yuvafor Seve. mitiative has been included in the curriculum of the management within city limits have been iden being LGBT friendly which went students of the institute as well.



Students have adopted Chembur railway station and have begun

Railway stations are extremely important and used by almost every Mumbai resident but no one cares about keeping it spruced up. We are slowly working on getting it all fixed ANANT DWWEDI, student

around 15 lakh senior citizens

Purthermore National Social the NSS volunteers to fill up forms with their details.

Agroup of students from Indian

Saathi, a society and resource from the election commission. groupforthequeer students at III B shot a video about the campus tified who will be approached by viral within two days.



इंजिनीयरिंगच्या दोनशे विद्यार्थ्यांनी चेंबूर स्थानक केले चकाचक



व्हीईएस इन्स्टिट्यूट ऑफ टेक्नॉलॉजीची मोहीम

मुंबई, दि. ७ (प्रतिनिधी) - निरोगी पर्यावरणाचे व्हीजन समोर ठेवून चेंबूरच्या विवेकानंद एज्युकेशन सोसायटीज इन्स्टिट्यूट ऑफ टेक्नॉलॉजीमधील (व्हीईएस) इंजिनीयरिंगचे तब्बल २०० विद्यार्थी बुधवारी झाडु घेऊन रस्त्यावर उतरले. चेंबुर रेल्वे स्थानक आणि आसपासच्या परिसरात या विद्यार्थ्यांनी स्वच्छता मोहीम राबवून परिसर चकाचक केला.

रेल्वे प्रशासन, महानगरपालिका यांच्या सहकार्याने हे विद्यार्थी या उपक्रमात सहभागी झाले होते. त्यांनी स्वच्छताच केली नाही, तर फलक आणि पोस्टर्सच्या सहाखाने

स्वच्छतेबाबत जनजागृतीही केली. या विद्यार्थ्यांसोबत संस्थेच्या प्राचार्या डॉ. जयलक्ष्मी नायर आणि उपप्राचार्या डॉ. विजयालक्ष्मी एम. यासुद्धा होत्या. कचरा ही वाढत जाणारी समस्या असून ती दूर करण्यासाठी प्रत्येक नागरिकाने स्वच्छतेची काळजी घ्यायला हवी, असे इंजिनीयरिंगच्या द्वितीय वर्षाचा विद्यार्थी प्रणीत कोटगिरे याने सांगितले. तर स्वच्छ व आजारमुक्त पर्यावरणाचे व्हीजन समोर ठेवले तर प्रत्येक स्थानक, सार्वजनिक ठिकाण स्वच्छ होईल, असा विश्वास विनायक शर्मा या विद्यार्थ्याने



Not wanting to rest on I Manish has stated the dream is starting a by 2020 to provide poor, deserving



19. PROHIBITION OF RAGGING

Ragging within or outside the institute is strictly prohibited. Any student who directly or indirectly participates in, abets or propagates ragging within or outside the institute shall face strict disciplinary action as per the provision of Maharashtra Prohibition of RAGGING ACT, 1999.

If a case is to be made as per law the following procedure is to be followed:-

- Investigation by squad
- Findings reported to the Principal
- Discussions with committee by Principal to decide whether: To go for FIR (within 24 hrs) with Police or Local Authorities- OR - Whether punishment to be awarded by Anti-Ragging committee of VESIT

Punishments may be in the form of:

- Suspension from attending classes and academic privileges.
- Withholding/withdrawing scholarship/fellowship and other benefits.
- Debarring from appearing in any test/ examination or other evaluation process.
- Withholding results.
- Debarring from representing the institution in any regional, national or international meet, tournament, youth festival, etc.
- Suspension/ expulsion from the hostel.
- Cancellation of admission.
- Rustication from the institution for period ranging from one to four semesters.
- Expulsion from the institution and consequent debarring from admission to any
 other institution for a specified period. Where the persons committing or
 abetting the act of ragging are not identified, the college will resort to collective
 punishment.

ADVICE TO ALL STUDENTS: All students are advised to be aware of anti-ragging measures and report any ragging incident to college authorities in person or by telephone at the earliest.

More information is available on our institute's website: http://vesit.ves.ac.in/vesit.ves.ac.in/vesit

Anti-Ragging/ Disciplinary Committee

Sr. No.	Name	Designation	Position	
1	Mr. Manoj Sabnis	Faculty, INFT	Chairperson	
2	Mrs. V. Khadilkar	Faculty, H & Sci.	Member	
3	Mrs. M. Vijayalakshmi	Faculty, MCA	Member	
4	Dr. R. K. Kulkarni	Faculty, EXTC	Member	
5	Mr. A. Nagananda	Faculty, EXTC	Member	
6	Mr. Vivek Umrikar	Faculty, H & Sci.	Member	
7	Mr. P. Birajdar	Faculty, ETRX	Member	

Students should submit an affidavit as per #ANNEXURE I and #ANNEXURE II at the time of admission. (Refer page nos. 66 and 68)

20. WOMEN WELFARE / GRIEVANCE COMMITTEE

Sr. No.	Name	Designation	Position	
1	Mrs. Shobha Krishnan	Faculty, EXTC	Chairperson	
2	Mrs. V. S. Khadilkar	Faculty, H&Sci.	Member	
3	Dr. (Mrs.) Nupur Giri	Faculty, CMPN	Member	
4	Dr.(Mrs.) Saylee Gharge	Faculty, EXTC	Member	
5	Mrs. Nishi Tikku	Faculty, MCA	Member	
6	Mrs. Navita Kant	Faculty, ETRX	Member	
7	Mrs. Amudha P.	Faculty , INST	Member	

21. ANTI SEXUAL HARASSMENT COMMITTEE

Sr. No.	Name	Designation	Position
1	Mrs. Shobha	Faculty, EXTC	Chairperson
	Krishnan		
2	Mrs. Jayshree Pandit	Co-ordinator/ Counselor,	Member (NGO)
		Sri Mukta Sanghtana,	

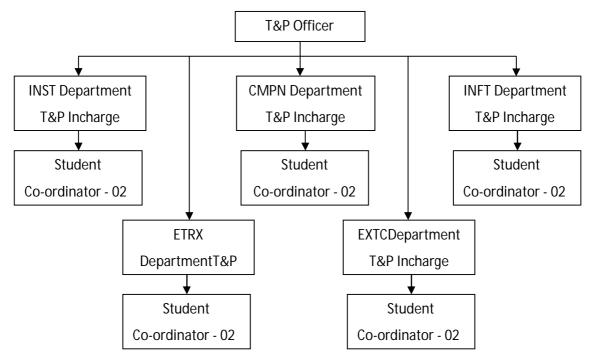
		R. C. Marg, Chembur - 71	
3	Dr. (Mrs.) Nupur Giri	Faculty, CMPN	Member
4	Mrs. V. S. Khadilkar	Faculty, H&Sci.	Member
5	Mrs. Nishi Tikku	Faculty, MCA	Member
6 Mrs. Prerana		Admin Staff, Office	Member
	Bhandari		
7	Mrs. Sonali Rahate	Admin Staff, Exam	Member
8	Student Representative	Member	
	Year		

22. WOMEN DEVELOPMENT CELL

Constitution – as per Guidelines from Mumbai University					
Sr. No.	Name	Designation	Position		
1	Mrs. M. Vijayalakshmi	Faculty, MCA	Member		
2	Mrs. Shobha Krishnan	Faculty, EXTC	Member		
3	Mrs. V.S. Khadilkar	Faculty, H&Sci.	Member		
4	Dr.(Mrs.) Saylee Gharge	Faculty, EXTC	Member		
5	Mrs. Naveeta Kant	Faculty, ETRX	Member		
6	Mrs. Nishi Tiku	Faculty, MCA	Member		
7	Dr. Nupur Giri	Faculty, CMPN	Member		
8	Mrs. Prerana Bhandari	Superintendent	Member		
9	Mrs. Sonali Rahate	Superintendent	Member		

23. CAREER GUIDANCE, TRAINING, PLACEMENT & ENTREPRENEURSHIP CELL

Training and Placement Cell



Activities:

	Activities	Resource Person	Target Audience
*	Finishing School for final year students to prepare them for placements	In-house faculty and industry experts	B. E.
*	Analytical Skill Enhancement Lectures included in regular timetable.	In-house faculty	S.E, T. E and B.E
*	Mock Group Discussions	Professional	T.E and S.E
*	Paper Presentation	Societies	
*	Aptitude Tests		
*	Training for Campus Placements	Mr. Nagananda A.,	T. E
	(Process of Decision Making and impact on	TPO	
	career, Skill Set Analysis, Resume Writing,		
	What Companies look for and filling		
	Corporate Bio data, Aptitude test and Group		
	discussion, Steering the Interview,		

Transactional Analysis)

* Placement Workshops Mr. Nagananda A.,

TPO

* Entrepreneurship development Preparing Professional for exams like GRE, GATE, GMAT, TOFEL Societies

Academic Year 2015-2016

		Selected	ETRX	CMPN	INST	EXTC	INFT	MCA	ME
*				179(11				81(11	
	Job Offered	547(26)	88)	23	124(3)	51(2))	3
*	Strength	853	144	223	79	202	77	128	12
*	Placed	521	86(2)	168	23	121	49	70	3
*	Second Job	26	0	11	0	0	2	11	-
*								101.2	
	% Job Offered	79.62	75.86	89.95	46	71.26	75	5	-
*	% Placed	75.83	74.13	84.42	46	69.54	72.05	87.5	-
*	Minimum Salary								
	(in Rs./-)		3,00, 000						
*	Maximum Salary								
	(in Rs./-)		13,50, 000						

* **No of Companies** 35(As on April 2016)

Entrepreneurship cell: The VESIT Entrepreneurship Cell (tVEC) organized 'Reverse Internship Crawl'. This event was in association with India's premier angel funding institution, The Indus Entrepreneurs (TiE). TiE is a nonprofit trade group dedicated to fostering entrepreneurship. This grand event was the first of many events that the Entrepreneurship Cell of Vivekanand Education Society's Institute of Technology (VESIT) plans to host every year



24. VESIT ALUMINI ASSOCIATION

ALUMNI MEET

Alumni Meet of this year was held on January 10, 2016. About 150 ex-students participated in this event. They were indeed very impressed with the change that has taken place in VESIT. They were also recollecting and reminiscing about the wonderful time they had spent here. The Meeting served as platform for current student council members to interact with their seniors and share experiences.





25. PARTICIPATION IN INTER-COLLEGIATE FESTIVALS

Other than in-house activities, the Institute also ensures that students are allowed to participate in various inter-collegiate events/cultural festivals if it helps the individual student to develop his/her talent without affecting his/her regular studies.

26. DISCIPLINE

Discipline plays a vital role in the institute. All the students admitted in the first year (F.E.) of the institute will have to give an undertaking regarding that they would observe general discipline and decent behaviour throughout the four years of their engineering course.

26.1 IDENTITY CARDS

Every student who has taken admission and duly confirmed, must get the I-Card issued from the Principal's office. He/she must wear this I-Card at all times. A student cannot enter the premises without his/her I-Card. The I-Card must be presented by the student as and when asked for, by the college authorities.

At the beginning of a new academic year, each student must get a new I-Card issued by the college after surrendering the old I-Card. This new I- Card will not be issued unless the old I-Card is submitted.

26.2 LOSS OF I-CARD and ISSUE OF DUPLICATE I-CARD

In case the I-Card is lost, students can obtain special permission from the Principal which is valid for only three days to enter the Institute premises without the I-Card. Thereafter, a duplicate I-Card is issued on payment of a prescribed fee. Any student using an I-Card (Original or Duplicate) without the signature of the Principal or without the Institute Rubber Stamp will be penalized.

26.3 EX-STUDENT'S I-CARD

Ex-students can obtain Ex-student's I-Card, if they desire to take advantage of the Library facility. Entry of Ex-students is permitted only during a specified time on working days.

26.4 LIBRARY and OFFICE TIMINGS

The Library will remain open on all working days, during the term, from 8.30 a.m. To7.00 p.m. The students can also use the Library facilities for extended hours and on holidays during preparatory leave and on examination days. The

office timings for students are from 11.00a.m. to 5.30 p.m., excluding lecture/Lab/Tutorial timings.

26.5 USE OF SPORTS GROUND

Students desiring to use the sports ground have to ensure that they adhere to the Rules and Regulations strictly. The students are not allowed to play while the lectures/practicals of respective classes/batches are in progress.

26.6 HOLIDAYS and VACATIONS

The list of the holidays will be displayed on the notice board. The notice regarding "Vacation " will be displayed as per the intimation from the University. Please be noted that the vacation period is liable to change from year to year and students should be prepared for such exigencies.

26.7 RAILWAY CONCESSION

Students aged up to 25 years and less can avail for concession for railway journeys between the stations near to the place of their residence in Mumbai and the College and for journeys between their place of permanent residence (where the students or his parents normally reside as declared in their application form for admission) and Mumbai during vacations. Concession forms for Local Railway tickets are issued from the Office of the Institute on working days.

The long journey concession form will be issued only for the vacation period. The students should write their local and permanent home /town address in the application form, for availing this concession. Concessions are being made within three days from the receipt of their application.

26.8 HOSTEL

Hostel accommodation is available for boys and girls separately. It will be provided on first-come-first -served basis. Preference will be given to outstation candidates. Before leaving for vacation or holidays, those students residing, in the hostel are required to take a permission letter from the class teacher/class counsellor, and submit a copy of the same to the Warden.

26.9 SAFETY PRECAUTION

Safety instructions issued from time to time by teaching faculty should be followed strictly by the students while doing their practicals. In case of a mishap, the entire responsibility shall lie on the student concerned and the Institute will no way be held responsible.

26.10 LOST AND FOUND

The Institute authorities will not be responsible for any damage to the property or possessions of the students. However, articles lost and found are deposited in the office, and are returned to the concerned student after identification.

26.11 DRESS CODE

Students are supposed to wear decent dresses and footwear suiting Indian Culture. Indecent, revealing, sleeveless, tight fitting short and gaudy dresses and T-shirts with indecent messages and pictures will not be permitted. Students should maintain proper hair style to depict a positive personality.

26.12 CLEANLINESS OF PREMISES

Students should not scribble anything on the walls, furniture etc. Strict action will be taken against the erring students and if not detected, a common fine will be levied to the entire class. Breakage of furniture will be treated in the same manner. All the students should strive for cleanliness of the premises and help in the better maintenance and upkeep of the institutional property.

26.13 INDULGING IN ANTI-SOCIAL ACTIVITIES

If a student is found in indulging in anti-social or anti-anti-national actions, he/she is liable to be expelled from the college without prior notice

26.14 CHEWING PAN/TOBACCO AND SMOKING

Chewing pan/tobacco or smoking in the institute premises is strictly prohibited.

26.15 USE OF ELECTRONIC ITEMS/GADGETS

Students are advised to use mobile phones only outside class rooms, laboratories. If they are found using mobile during lectures/ labs, the college authorities can confiscate such items and take strict action against the students concerned. Other electronic gadgets/equipment which is needed for Project purpose can be brought in with proper permission and documentation.

26.16 REFUND OF CAUTION MONEY

On completion of the course, an application for refund of caution money should be made (along with F.E. fee receipt) within the same financial year. Unclaimed caution money will be forfeited. Life membership fee for alumni will be deducted from the caution money.

26.17 LOCKER FACILITY

Locker facility for the first year students is provided on first-come-first-served basis. The student should forward an application for using this facility for which he/she has to pay a locker fee.

26.18 RETURN OF ORIGINAL CERTIFICATES

All original certificates like Mark Sheets, Passing Certificate, Caste Certificate, Domicile Certificate etc. will be returned to the students after all the admission formalities with the University /DTE are completed. Normally, these certificates are returned to first year students in the month of March/April.

26.19 NOTICE BOARDS

Students are advised to read the notice boards for various circulars/notices etc. displayed regarding payment of fees, commencement of examination and other matters of academic interest. Whenever dates have been given for payment of Tuition Fees/Exam Fees etc. student should pay the fees well in advance, before the last date. This will avoid penalty as well as inconvenience to them. Students should not go to the Office for Railway concession or any other matter while their theory classes/practicals are in progress. Further, whenever they come to the Office, they should maintain proper discipline and decorum. They shall

always follow the Queue system and shall not crowd near the counters and should not block main entrance and exit points.

26.20 GRE/TOEFL/GATE/GMAT

Students will not be allowed to stay away from college for the sake of preparation of GRE/TOEFL/GATE/GMAT or other forms of competitive examinations. It may be noted that students need to plan their time well in advance so that it does not affect their academic calendar. Students so desiring to appear, will be permitted for a maximum period of 15 days, provided their academic record and attendance in the semester is satisfactory.

26.21 CO-CURRICULAR ACTIVITIES

All co-curricular and extracurricular activities of the college will be held only after the regular college timings. No activities will affect the regular working of the college. Seminars/symposia/conferences/workshops of the student's professional societies will also be held only after the regular college hours. Students desiring to participate in local and outstation technical seminars, paper presentation contests, project exhibitions and other competitions will have to take prior permission.

26.22 PICNICS and OUTINGS

Students will not be permitted to organize/participate in private picnics/outings even on Sundays/Holidays without the written consent of the college Principal. All picnics/outings have to be authorized and accompanied by college staff. Any failure to comply with any of the rule will lead to strict action taken against them.

26.23 INTERNET USAGE

Internet facility is provided to our students at various places. Students are required to use this facility only for academic purposes and not for any other purposes like chatting. Misuse of this facility and indecent behaviour on the NET is liable for punishment and strict action.

26.24 THEFT

Students found stealing other's property or institutional property will be dealt with severely. If the institutional property/facility is damaged/stolen, the entire batch of students will be penalized and the cost will be recovered as deemed fit.

26.25 SUGGESTION BOX

Students can avail the suggestion box facility provided outside the Principal's Office, in ease of any suggestions, complaints etc.

26.26 BIRTHDAY CELEBRATIONS

Birthday celebrations of fellow students are not allowed inside the class or within the premises or in the vicinity of the college.

26.27 PRINCIPAL'S SIGNATURE

Students should come to Principal's Office for signature or for attestation during the recess time or only after their theory classes/practicals are over.

The above conditions are as per the Rules and Regulations of the Institute and all students should strictly follow them. Strict action will be taken against the student, if he/she fails to observe them.

Any matter not covered by these Rules will rest at the absolute discretion of the Principal.

Most Important Note:

- All Admissions and Cancellations will be conducted/done
 as per the DTE rules and Guidelines.
 - All available Scholarships of University, State
 Government, etc will be granted to the eligible
 candidates as per rules. (Please refer website.)

UNDERTAKING

I hereby agree that during my study tenure in this Institute, to abide by all the Rules and Regulations mentioned in prospectus. I assure that my behaviour and attitude will add to name and fame of this Institution.

If I am found as defaulter with respect to above or otherwise, I will be liable for punishment given to me by the authorities.

I have taken admission in this Institution in FIRST / SECOND Year Engineering in the Course of ______Branch, on my own, in consultation with my parents. I assure you that I will abide by all rules and regulation laid down by the Management of this Institution, Directorate of Technical Education (M.S.), University of Mumbai issued from time to time. In the event of failure of these, I shall render myself for disciplinary action including expulsion.

I shall not claim for any kind of financial assistance and help from this Institution for my education. I shall not damage any furniture, equipment, and other properties; and if damaged, I undertake to make good the loss on this account, as decided by the Management of this Institution.

The information given by me in the application is true to the best of my knowledge and belief.

I have not been debarred from appearing at any examination held by any Government constituted or Statutory Examination Authority in India.

I fully understand that the offer of a course or branch of engineering will be made to me depending on inter-se merit and availability of a seat at the time of scrutiny of my application, when I report to the admission authority according to the schedule of admission.

I understand that no other document, other than those attached to the application form before the last date of admission will be entertained for the purpose of claims/concessions etc. in connection with my admission.

I understand that no refund of fees (except Security Deposit and Caution Money Deposit) shall be made if I cancel the admission after cut-off date of admission declared by the Competent Authority (DTE).

I hereby agree to confirm to any rules, acts and laws enforced by Government including Anti-Ragging Act and I hereby undertake that so long as I am a student of the institute, I will do nothing either inside or outside the institute, which may result in disciplinary action against me under the rules, acts and laws.

I fully understand that the Principal / Director of the institute where I would be admitted, will have right to expel/rusticate me from the institute for any infringement of the Rules of conduct and discipline prescribed by the institute/university (if any)

Condition of minimum attendance: I am fully aware that, I will not be allowed to keep term and appear for the examination if I do not attend minimum 75% classes of theory, practical, tutorials etc. I am also aware that I will not be allowed to appear for the examination, if I fail to submit satisfactory all the assignment, jobs, journals, reports, etc. as specified by the university within stipulated time.

I am aware that defaulters' list will be put up on the notice board three times during each semester and it is my responsibility to note this and take corrective action and inform my parents / local guardian / guardian.

Signature of the Student

I have read the above contents carefully. I understand that all the conditions are in the interest of my ward and therefore I agree to abide by all the conditions and will respect any decision taken by the management in case of violation of any rules of the institute or disobedience by my ward.

Date:	

Signature of the Parent/Local Guardian/Guardian

V.E.S Institute of Technology

		$\mathbf{E}\mathbf{X}$		

AFFIDAVIT BY THE STUDENT (On rupees 100 stamp paper with notary)

Ι,								(full
name	of	student	with	admission/registration/enrolment	number)	s/o	-	d/o
Mr./M	rs./	Ms						

- 1) having been admitted to **V.E.S. Institute Of Technology**, **HAMC**, **Chembur**, **Mumbai-400074.**, have received a copy of the AICTE regulations on Curbing the menace of Ragging in Higher Educational Institutions, 2009, (hereinafter called the "Regulations") carefully read and fully understood the provisions contained in the said Regulations.
- 2) I have, in particular, perused clause 3 of the Regulations and am aware as to what constitutes ragging.
- 3) I have also, in particular, perused clause 7 and clause 9.1 of the Regulation and am fully aware of the penal and administrative action that is liable to be taken against me in case I am found guilty of or abetting ragging, actively or passively, or being part of a conspiracy to promote ragging.
- 4) I hereby solemnly aver and undertake that
 - a) I will not indulge in any behaviour or act that may be constituted as ragging under clause 3 of the Regulations.
 - b) I will not participate in or abet or propagate through any act of commission or omission that may be constituted as ragging under clause 3 of the Regulations.
- 5) I hereby affirm that, if found guilty of ragging, I am liable for punishment according to clause 9.1 of the Regulations, without prejudice to any other criminal action that may be taken against me under any penal law or any law for the time being in force.
- 6) I hereby declare that I have not been expelled or debarred from admission in any institution in the country on account of being found guilty of, abetting or being part of a conspiracy to promote, ragging; and further affirm that, in case the declaration is found to be untrue, I am aware that my admission is liable to be cancelled.

V.E.S Institute of Technology

Declared this day of month of year	
Declared thisday of month of year	
Signature of Deponent	
Name:	
Address:	
Telephone/Mobile No.	
VERIFICATION	
Verified that the contents of this affidavit are true to the best of my knowledge.	edge and no
part of the affidavit is false and nothing has been concealed or misstated the	nerein.
Verified at(place) on this the(day) of	(month)
(year)	
Signature of deponent	
Solemnly affirmed and signed in my presence on this the(day) of_	month
(year),	
after reading the contents of this affidavit.	
OATH COM	MISSIONER

ANNEXURE 2

AFFIDAVIT BY PARENT/GUARDIAN (On rupees 100 stamp paper with notary)

I, Mi	r./Mrs./M	S							
(full	name	of	parent/	'guardian)	fath	er /	mother/gu	ıardian	of
			(full	 name	of	student	with	admis	sion
/regis	tration/er	rolmer	nt numbe	r),					

- 1) having been admitted to V.E.S. Institute Of Technology, HAMC, Chembur, Mumbai-400074., have received a copy of the AICTE regulations on Curbing the menace of Ragging in Higher Educational Institutions, 2009, (hereinafter called the "Regulations") carefully read and fully understood the provisions contained in the said Regulations.
- 2) I have, in particular, perused clause 3 of the Regulations and am aware as to what constitutes ragging.
- 3) I have also, in particular, perused clause 7 and clause 9.1 of the Regulations and am fully aware of the penal and administrative action that is liable to be taken against my ward in case he/she is found guilty or abetting ragging, actively or passively, or being part of a conspiracy to promote ragging.
- 4) I hereby solemnly aver and undertake that
 - a) My ward will not indulge in any behavior or act that may be constituted as ragging under clause 3 of the Regulations.
 - b) My ward will not participate in or abet or propagate through any act of commission or omission that may be constituted as ragging under clause 3 of the Regulations.
- 5) I hereby affirm that, if found guilty of ragging, my word is liable for punishment according to clause 9.1 of the Regulations, without prejudice to any other criminal action that may be taken against my ward under any penal law or any law for the time being in force.
- 6) I hereby declare that my word has not been expelled or debarred from admission in any institution in the country on account of being found guilty of, abetting or being part of a conspiracy to promote, ragging; and further affirm

admission my word is liable to be cancelled.					
Declared thisday of month of year					
Signature of Deponent					
Name:					
Address:					
Telephone/Mobile No:					
VERIFICATION					
Verified that the contents of this affidavit are true to the best of my knowledge and no part of the affidavit is false and nothing has been concealed or misstated therein. Verified at (place) on this the(day) of (month) and(year) Signature of deponent Solemnly affirmed and signed in my presence on this the (day) ofmonth,(year), after reading the contents of this affidavit.					
OATH COMMISSIONER					



VIVEKANAND EDUCATION SOCIETY



V.E.S. Institute of Technology

Affiliated to Mumbai University & approved by A.I.C.T.E



PROSPECTUS

With Blessings of Our Founder

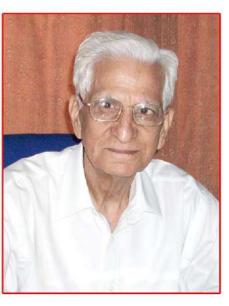


Late Shri. Hashu Advani

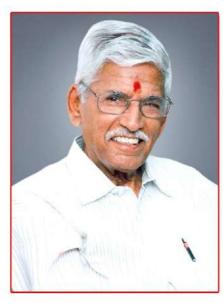
Our Ex-Presidents



Late Shri.J.T.Wadhwani



Late Shri.K.B.Mansharamani



Late Shri. Vasudev Valecha

VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY

Affiliated to Mumbai University & Approved by A.I.C.T.E



Website: http://ves.ac.in/vesit

INDEX

1.	ABOUT VIVEKANAND EDUCATION SOCIETY (V.E.S)	1
2.	GOVERNING BODY	3
	VES MANAGING COMMITTEE MEMBERS	3
	VESIT GOVERNING BODY MEMBERS	3
3.	VISION AND MISSION	4
4.	COURSES OFFERED	5
5.	FROM PRINCIPAL'S DESK	6
6.	FROM VICE-PRINCIPAL'S DESK	11
7.	SCHEME OF STUDY	12
	7.1.1 BACHELOR OF ENGINEERING (B.E) (DTE CODE EN3185)	12
	7.1.2 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME 3185)	13
	7.1.3 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME 3185)	13
	7.1.4 Ph.D. PROGRAMME	14
8.	GRADING SYSTEM OF THE UNIVERSITY OF MUMBAI	14
9.	TERM GRANT	15
10.	EXAMINATIONS	15
11.	USING UNFAIR- MEANS IN THE EXAMINATIONS	15
12.	ATTENDING LECTURES/LABS	16
13.	INFRASTRUCTURE AND SALIENT FEATURES	16
14.	FEE STRUCTURE	18
15.	ABOUT CAPITATION FEE	20
16.	SPECIAL PROGRAMMES	20
17.	MAJOR LABORATORIES	21
18.	INDUSTRY COLLABORATION	27
19.	RESEARCH AND DEVELOPMENT LAB	29
20.	COMMON INFRASTRUCTURE	30
21.	TEACHING FACULTY	33
22.	STUDENTS' COMMITTEE/ACTIVITIES	33

23.	ACHIEVEMENTS	44
24.	PRESS RELEASES	49
25.	PROHIBITION OF RAGGING	51
26.	WOMEN DEVELOPEMENT CELL	52
27.	ANTI SEXUAL HARASSMENT COMMITTEE	53
28.	INTERNAL COMPLAINT COMMITTEE	54
29.	CAREER GUIDANCE, TRAINING, PLACEMENT & ENTREPRENEURSHIP CELL.	55
30.	VESIT ALUMINI ASSOCIATION	57
31.	PARTICIPATION IN INTER-COLLEGIATE FESTIVALS	58
32.	DISCIPLINE	58
	32.1 IDENTITY CARDS	58
	32.2 LOSS OF I-CARD AND ISSUE OF DUPLICATE I-CARD	58
	32.3 EX-STUDENT'S I-CARD	58
	32.4 LIBRARY AND OFFICE TIMINGS	59
	32.5 USE OF SPORTS GROUND	59
	32.6 HOLIDAYS AND VACATIONS	59
	32.7 RAILWAY CONCESSION	59
	32.8 HOSTEL	59
	32.9 SAFETY PRECAUTION	59
	32.10 LOST AND FOUND	60
	32.11 DRESS CODE	60
	32.12 CLEANLINESS OF PREMISES	60
	32.13 INDULGING IN ANTI-SOCIAL ACTIVITIES	60
	32.14 CHEWING PAN/TOBACCO AND SMOKING	60
	32.15 USE OF ELECTRONIC ITEMS/GADGETS	60
	32.16 REFUND OF CAUTION MONEY	60
	32.17 LOCKER FACILITY	60
	32.18 RETURN OF ORIGINAL CERTIFICATES	61
	32.19 NOTICE BOARDS	61
	32.20 GRE/TOEFL/GATE/GMAT	61
	32.21 CO-CURRICULAR ACTIVITIES	61
	32.22 PICNICS AND OUTINGS	61
	32.23 INTERNET USAGE	62

32.24 THEFT	62
32.25 SUGGESTION BOX	62
32,26 BIRTHDAY CELEBRATIONS	62
32.27 PRINCIPAL'S SIGNATURE	62
ANNEXURE 1	63
ANNEXURE 2	63

2. ABOUT VIVEKANAND EDUCATION SOCIETY



Founder of Vivekanand Education Society



Shri Hashu Advani along with his 10 friends started Vivekanand Education Society (VES) as a small school, in a barrack, which has grown like a banyan tree, with a spread of 26 institutions.

Shri Hashu Advani was the Municipal Corporator, MLA Urban Development Minister and Finance Minister in the Government of Maharashtra.

Philosophy of Shri. Hashu Advani ji & Values of VES

Samarpan Total dedication and devoted to serving Humanity Samdrishti Humanitarian feeling, behavior with equality for all

Vishalta Visionary in pursuit of excellence for a value rich education

Karm Yogi A doer, Actions speaking louder than words

Siddhant Valuing humanity and the importance of education and learning, in building a better

future

Vision

- Increase the intellectual wealth of the country by providing quality education and building excellent technical skills
- Make individuals highly confident, self reliant and truly global citizens
- Provide for holistic development, in terms of Knowledge, Skill and Wisdom, leading to cultural and spiritual integration.

VES Mission – Value Based Education

Develop new generation leaders to bring a positive change in the world using modern education system emanating from India's rich cultural heritage blended with western science!

Educational Facilities Offered

- · Professional, Technical & Higher Education
- · Junior & Senior Colleges
- · Certificate Level Technical & Vocational Education
- · High School Level Education

- · Primary & Preprimary Level Education
- · Welfare Activities
- · Sports & Fine Arts





Distinctive Essentials

- · Admired for Ethics, Values, Discipline and Excellence in education
- Management board of honorary professionals in different fields. Honorary Associates and Alumni assisting in various activities of the trust.
- 26 Institution from crèche to schools to colleges upto Ph.D. level
- · More than 18,000 students studying every year and over 1200 teaching & non-teaching Staff.
- · No capitation or donation from students or parents
- There are no Management quota seats in all professional Institutions of VES.
- · All admissions are purely on merit
- 12 buildings in 3 complex's with over 395000 sq. ft of area
- · 3 Large Sports grounds and, Hostel & Staff quarters.
- Financial help to poor & needy students.

Institutions of VES

Professional, Technical & Higher Education Institutions

- V.E.S. Institute of Management Studies and Research
- V.E.S. Institute of Technology
- V.E.S. College of Pharmacy
- V.E.S. Law College
- V.E.S. College of Science, Arts & Commerce
- V.E.S. Leadership Academy & Research Centre
- V.E.S. Polytechnic

High Schools & Junior Colleges

Swami Vivekanand Jr. College, English Medium

Swami Vivekanand Kanishta Mahavidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, English Medium, Chembur

Swami Vivekanand Vidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, Marathi Medium, Kurla

Certificate Level Technical & Vocational Institutions

V.E.S. Tulsi Technical Institute,

Sindhi Society, Chembur

V.E.S. Catering Craft Centre

V.E.S. Nari Shala

Sports & Fine Arts Institutions

V.E.S. Vivekanand Sports Academy

V.E.S. Vivekini

Other Institutions

V.E.S. Creche & Day Care Centre

V.E.S. Vocational Guidance Bureau

V.E.S. Welfare Section

V.E.S. Vivek Sindhi Kendra

Primary & Preprimary Schools

Swami Vivekanand Preprimary & Primary School English Medium, Sindhi Society, Chembur

Swami Vivekanand DBC Pre-Primary & Primary School English Medium, Collectors Colony, Chembur

Swami Vivekanand Prathmik & Purva Prathmik Shala Hindi Medium, Kurla

Swami Vivekanand Pre-Primary & Primary School English Medium, Kurla (E)

Swami Vivekanand Prathmik & Purva Prathamic Shala, Marathi Medium, Kurla

Swami Vivekanand Nursery & Play School, Chembur

2. GOVERNING BODY

VES MANAGING COMMITTEE MEMBERS

1. Shri. Mahesh Tejwani (President) 2. Shri. Dinesh Tahiliani (Secretary) 3. Shri, Amar Asrani (Treasurer) Shri. B. L. Boolani 4. (Member Trustee) Shri. Bansi Wadhwa 5. (Member Trustee) Adv. Shri. Laxman Kanal 6. (Member Trustee) 7. Shri, Suresh Bharwani (Member Trustee) 8. Shri. Prakash Lulla (Member Trustee) 9. Shri. Suresh Malkani (Member Trustee) Smt. P. Vasvani 10. (Member Trustee) 11. Shri, Vishu Lala (Member Trustee)

VESIT GOVERNING BODY MEMBERS

1. Shri. Mahesh Tejwani Chairman 2. Shri. Dinesh Tahiliani Member 3. Shri. Amar Asrani Member 4 Shri. B. L. Boolani Member 5. Shri Sunil Khanna Member Industry Expert 6. Shri. R.N.Mukhija Member Industry Expert 7. Dr. R. Unnikrishanan Regional Officer, AICTE Nominee 8. Dr. M. D. Desai **AICTE Nominee** 9. Dr. N. R. Gilke Mumbai University Nominee Director DTE. DTE Nominee 10. Dr. S. K. Mahajan State Government Nominee 11. Shri. P. K. Krishnamurthy 12 Dr. (Mrs.) J. M. Nair Principal /Secretary 13 Vice Principal Dr. (Mrs.) M. Vijayalakshmi 14 Dr. P. P. Vaidya Staff Representative 15 Mrs. Gresha Bhatia Staff Representative 16 Shri. K. D. Ahuja Special Invitee

3. VISION AND MISSION

Vision

"To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of socially conscious application of technology".

Mission

- To inculcate culture of value based education.
- To enthuse students to develop in an ambient environment of caring and of sharing information.
- To enable students to work towards excellence in their chosen fields with professional bent of mind.

4. COURSES OFFERED

National Board of Accreditation (NBA) has approved the accreditation of Under-Graduate Programmes. Department of Computer Engineering and Electronics Engineering are accredited for a period of 2 years and Department of Electronics and Telecommunication Engineering, Instrumentation Engineering and Information Technology accredited for period of 3 years, after the visit of Expert Committee w.e.f. 01/07/2016.

Starting Year	Course/Programme/Intake
1984 - 1985	BE Electronics Engineering – 60
	BE Computer Engineering – 60
	BE Instrumentation Engineering – 60
1992 – 1993	BE Electronics and Telecommunication Engineering – 60
1999 - 2000	BE Information Technology – 60
2002 - 2003	ME Electronics and Telecommunication Engineering – 18
2002 - 2003	ME Information Technology – 18
2003 - 2004	ME Instrumentation and Control – 18
2003 - 2004	Master of Computer Applications – 60
	Increase in intake of the existing programmes
2011 - 2012	BE Computer Engineering – 60
2012 - 2013	BE Electronics and Telecommunication Engineering – 60
	BE Electronics Engineering – 60
	BE Computer Engineering – 60 (Regular Second Shift)
	Master of Computer Applications – 60 (Regular Second Shift)
	Recognized as Ph.D. Centre for Electronics
	Direct Second Year BE
2013-2014	Electronics and Telecommunication Engineering – 60(Regular Second Shift)

5. FROM PRINCIPAL'S DESK

The year 2017 will be remembered as a year when India's space programme rocketed into record books by successfully deploying 104 satellites into the orbit – a feat not accomplished by any country so far. It has been recorded as the highest number of satellites sent in a single mission, overtaking Russia's feat of launching 37. It is really a proud moment of every Indian, not only for space scientific community, but also, for the whole nation.



Every engineer can also be proud of "Laugh Star"- 3D printed representation of sound waves generated from human laughter-created aboard the International Space Station (ISS) which has become the first ever piece of art to be produced outside the Earth. The artwork was printed using Additive Manufacturing Faculty, a gravity independent 3D Printer which supports the daily operations of ISS by producing parts, tools and assemblies.

Academic year 2016-17 has also been a year of achievement and recognition for VESIT. National Board of Accreditation granted three years of accreditation to Departments of Electronics & Telecommunication, Information Technology and Instrumentation and two years of accreditation to Departments of Computer and Electronics from academic year 2016-17.

Another recognition was the award of Platinum Ranking in the AICTE-CII Industry-linked Technical Institute Survey 2016 under the category of Best Industry Linked Engineering Institute (Degree). It is worth mentioning that only 18% of the institutes that participated in this survey secured platinum rank. To add another feather to the cap: VESIT has been awarded "The most Promising Engineering College with Excellent Faculty in Maharashtra" at the Asia Education Summit and Awards 2017.

We are proud of our third year Electronic Engineering student Rohit Kashyap, whose project "Semi-Autonomous Pipe Line Exploration Robot" was selected for the finals of NI Yantra 2016 competition, out of 950 teams, which was conducted by National Instruments, Bangalore.

The Final year students of Electronics & Telecommunication Engineering successfully presented project titled 'Road Divider Based Power Generation and Street Light Illumination' which won the 2nd position in University Research Convention of AVISHKAR 2016-17. They represented the University of Mumbai in the final Round. The proposed model demonstrates the generation of electricity with the help of wind energy from the vehicular movements on the Highways. Our Ph.D. student Mrs. Shilpa Joshi also presented her paper titled 'Novel filter designing for speckle noise removal from medical images.' and represented our institute at district level, in the same Inter-Collegiate Research Convention.

Three teams of students, two from computer department and one from Electronics & Telecommunication department, secured first and third position respectively, in the 8th Indian Youth Science Congress held at University of Mumbai from 16 to 18 February 2017. The projects were Lung Cancer Detection using Image data Analysis and machine Learning, Drought Prediction and Management system using Big Data Analytics and Next Generation PCB Fault Detection and Rectification respectively.

The academic results of first batch of credit based system for academic year 2015-16 of MCA was 100%, followed by Electronics & Telecommunication department with 99.5%, Computer and Instrumentation Department 99%, and Electronics with 90%.

Our Degree Distribution Ceremony was conducted on 18 February, 2017. Dr. B.K.Dutta, Dean HBN, Shri Britto, Technical Director, RCF and Shri. Pankaj Doke, Senior Scientist, Tata Consultancy Services addressed our students during this occasion.

The placement scenario has been promising with 72.27% compared to the last year's 63.83%. This year 11 new companies visited our campus, thanks to the special efforts of our efficient Training and Placement team. Accenture remained the top recruiter this year also, with placement offered for 260 students across all branches, followed by Cappemini, which placed 92 students. The highest salary offered was 16 lakhs by Morgan Stanley, which placed two of our students.

We congratulate our students Tarleen Dand, Anant Gupta and Mayur Kolwani for receiving Shresthta Award for conquering adversities while excelling in various domains and continued to maintain good academic grades.

Our social responsibility team (SoRT) collaborated with five NGO's namely National Association for Equal Opportunities for Handicapped (NASEOH, Chembur), Are You

Recycling? (RUR, Matunga), Bal Anand (Chembur), Self Esteem (Chembur) and Karmayog by volunteering to sell their handmade products during SoRT week 2016.

SoRT in association with Marrow Donor Registry (India) held a Bone Marrow Registration drive on the 12 August 2016 in our campus. They also had a blood donation drive, Thalassemia test and eye checkup in association with Hashu Advani Memorial Trust and in collaboration with doctors from JJ Hospital.

An inter-class debate competition was held on 20 October 2016, which was sponsored by the UNION BANK OF INDIA. It was conducted to inspire the young minds to think deeply on socially relevant problems. The topics for the final round included, 'RTI is an effective tool to tackle corruption', 'Corruption can never be eliminated in a democratic country' and 'Men are more susceptible to corruption than women'. An inter class One Act English play competition was also conducted for first and second year students and was judged by experts from Tata Institute of Social Sciences.

Illusion'17, an intra-collegiate competition, conducted on league format, was held during the second week of January where six teams, comprising of students from all the five branches of engineering as well as MCA, competed with each other in six portfolios viz. dance, drama, sports, aesthetics, music and visual arts.

Visual Arts portfolio which was introduced newly in this year tested the video editing and photography skills of participating teams.

Our budding engineers excel in extracurricular activities also. Our students secured 1st place in Inter-collegiate Drama competition in Manzhar conducted by ICT, 2nd place in Talash conducted by Jai Hind College, 3rd place in Mithibai College and Xavier's College. We are also proud of our student Utkarsh Pathak who won Bronze medal in National Kudo tournament and Uska Asian Karate Championship 2017 and Miss. Darshana Jain, third year Electronics Engineering student who secured 3rd place in Mumbai University Boxing Tournament under 75Kg middle weight category.

This year's alumni meet was celebrated on 7^{th} January, 2017 and three of our alumni Ms. Tanushree Bagrodia

(Computer-2001), Chief Finance officer and Vice President-IT of NRB Bearings Ltd. (turnover USD 100m and Market Cap USD 200m), Mr. Anubhav Sonthalia (Computer-2002) Co-founder & COO, Sokrati, Mr. Neeraj Vashi (Computer-1990-91) Founder and Director of Nivalink were V.E.S. INSTITUTE OF TECHNOLOGY 8

presented the Distinguished Alumni Award. We are also proud to inform that our alumnus Dilip Kumar Khandelwal, Managing Director, SAP Labs India and Executive VP & Global Head of Suite Engineering is selected as one of the India's Hottest 40 Young Business Leaders 2016, under forty.

VESIT celebrated Vigilance Awareness Week from 31 October to 5 November 2016 where all the students had taken integrity pledge in the class and all staff took online integrity pledge. Constitution day was also celebrated on 26 November 2016. We were delighted to welcome Shri. Chandrakant Patil, Minister of Revenue and Public Works, Government of Maharashtra who visited our campus this year, for the celebration of "Nishkam Seva Divas", birth anniversary of late Shri Hashu Advani, our founder trustee.

We are very much proud of our staff members of Electronics Engineering Department, whose research proposal got a grant of 3 lakhs from ISRO under Mars Orbiter Mission – An Opportunity (MOM-AO). The proposed research work by the team is related to surface geology, composition and morphology of Mars, by utilizing the data captured by the on-board sensors. Also, eleven of our faculty members got Minor Research Grant from Mumbai University for this academic year.

Faculty Development Programs are regularly conducted in our institute to keep teachers updated about the latest technologies. One week ISTE approved Industry Oriented Workshop on "IT Innovations", in collaboration with Cappemini, SAP Labs and Foslipy from 5 January to 11 January 2017. The workshop was organized by the Departments of Computer Engineering, Information Technology and Master of Computer Applications. An industrial visit to Cappemini and practical session on Python Data Analytics were also organized. Cloud Computing, Service Oriented Architecture, Internet of Things, Agile Methodology and SDLC Concept and User Interface Design were the topics covered during this workshop. The faculties of EXTC Department visited POLICE COMMISSIONERATE OFFICE, Navi Mumbai, Belapur on 5 January 2017 to understand VSAT facility used by Maharashtra Police. A visit to Solar Power Station of RCF was also organized for Instrumentation, Electronics and Electronics & Telecommunication departments on 27 January 2017.

A one week Special Faculty Development Programme "Aarohan" was organized from 4 July to 9 July 2016 by VESIT Renaissance cell. Mahindra & Mahindra sponsored a two day workshop which provided a wonderful opportunity to faculty members for augmenting and elevating their effective conversation skills and team building skills. A session on Multiple Intelligence was delivered by Mr. Y. Dattatreya, Leadership Consultant, on 7 July 2016 followed by a Yoga V.E.S. INSTITUTE OF TECHNOLOGY

session by Ami Yoga Solutions Private Limited and an expert talk by Dr. Nair, a very renowned Orthopedic Surgeon from Zen Hospital, on prevention of osteoarthritis & Osteoporosis.

As a part of our expansion plans, the Bhoomi poojan for extension building was conducted on 6 March 2017. We are very much indebted to our management, VES President Shri Mahesh Tejwani, Secretary Shri Dinesh Tahiliani and Treasurer Shri Amar Asrani for sanctioning our proposal for extension wings of our building.

Marching ahead, VESIT is planning to apply for academic autonomy this year, which will provide us flexibility to modify existing academic structure and also to include innovative ideas which will help us to enhance academic excellence. We sincerely invite suggestions, feedback and guidance from stakeholders in this regard. This will help us to make suitable changes and modification which will go a long way in making our teaching-learning process more effective and relevant with times.

"Arise! Awake! And Stop Not Till The Goal is Reached!" - Swami Vivekananda.

6. FROM VICE-PRINCIPAL'S DESK

VESIT has always taken pride in imparting holistic education to its students. Our vision "To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of socially conscious application of technology", indicates our dream of reaching for the beyond. I am proud to say that this year too, we have been able to deliver this promise. The Platinum status conferred upon us by AICTE in association with CII and winning the "The most promising Engineering College with excellent faculty in Maharashtra"



award at the Seventh Asia Education Summit and Awards, 2017 organized by World Wide Achievers on 9 March 2017 at New Delhi goes on to prove our commitment and achievement.

We are constantly striving to improve and bring innovative techniques in our teaching-learning practices. POGIL, Project Based Learning, Flipped classrooms, using NPTEL, e-Yantra and Virtual labs initiatives from MHRD, continuous collaboration with industry experts to deliver current trends to our students are some of the efforts taken by us as we strive towards our goal.

VESIT boasts of a well-equipped library, state of art laboratories with industry caliber hardware and software. Our students run professional societies, industry collaboration labs which help us to create and nurture a vibrant knowledge oriented environment, ideal for creating next generation engineers ready to take our nation into a nation of job creators as versus job seekers.

Commitment to society is at the core of VESIT culture. Our SoRT team continues to associate itself with several social organizations to participate in activities that benefit the society. Blood donation drive, Bone Marrow Donation Registration, crowd management during Gokulashtami festival, helping citizens waiting in long bank queues during demonetization are some of the activities taken up by them during this academic year.

Team work and cooperation by staff and students and the special endeavors by the faculty has enabled us to maintain our standards and quality. We thus remain in the forefront of engineering colleges in Mumbai.

"See for the highest, aim at the highest and you shall reach the highest"

- Swami Vivekananda

7. SCHEME OF STUDY

7.1.1 BACHELOR OF ENGINEERING (B.E) (DTE CODE EN3185)

The Bachelor of Engineering is a four year course, which is divided into eight semesters. The first year (F.E.) course spreads over two semesters dealing with general engineering, common to all branches of engineering. The basic objective is to give elementary foundation needed in different aspects of engineering.

During the second year (S.E.) and third year (T.E.), students gain basic knowledge in their chosen branches. The final year i.e. fourth year (B.E.) is designed to strengthen the specialized skills which help the students to apply the concepts studied earlier by participating in "project work". The subjects and the syllabus are as prescribed by the University of Mumbai.

Approved Intake for Academic Year 2017-18

Regular First Shift	<u>Intake</u>
B.E Electronics Engineering	120
B.E. - Computer Engineering	120
B.E Instrumentation Engineering	60
B.EElectronics and Telecommunication	120
B.E Information Technology	60
Regular Second Shift	<u>Intake</u>
B.E Computer Engineering	60
B.E. – Direct Second Year (Elect.and Tele.)	60

Eligibility: - As per DTE, Mumbai University and State Government Rules.

<u>Note:</u> -51% Minority quota admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

7.1.2 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME 3185)

The M.E. Program comprises of four semesters with duration of two years. The courses are designed/prescribed by the University of Mumbai.

The numbers of seats offered are as mentioned below:

Approved Intake for Academic Year 2016-17

Regular First Shift	<u>Intake</u>
M.E Electronics and Telecommunication.	18
M.E Instrumentation and Control	18
M.E Information Technology	18

Eligibility: - As per DTE, Mumbai University and State Government Rules.

<u>Note: -</u> 51% Minority quota admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

7.1.3 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME 3185)

Master of Computer Application (M.C.A) is a full time three year course (six semesters). The course is conducted as per the syllabus laid down by the Mumbai University. Examinations are conducted by the University for all the six semesters and the degree is also awarded by the University of Mumbai.

Approved Intake for Academic Year 2016-17	Intake
MCA - Regular First Shift	60
MCA - Regular Second Shift)	60

Eligibility: - As per DTE, Mumbai University and State Government Rules.

Note: - 51% Minority quota admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

7.1.4 Ph.D. PROGRAMME

VESIT has always given importance to the experimental way of teaching and learning of engineering concepts which can only be perfected through the best research and development practices. VESIT offers Ph.D. course in Electronics Engineering. VESIT has well equipped R&D Laboratory which is utilized by P.G. and Ph.D. students.

8. GRADING SYSTEM OF THE UNIVERSITY OF MUMBAI

Mumbai University introduced Choice Based System from the academic year 2016-17. All information about pattern and syllabus is available on Mumbai University's official web site www.mu.ac.in

Salient Features:

- ✓ Final grade for B.E. Degree will be based as the average of all eight semesters. So each semester/each subject is important.
- ✓ 75% attendance is compulsory.
- ✓ Journal submission must be done on or before every term end.
- ✓ Two tests will be conducted according to the given model by the University for Internal Assessment of each subject. The average of the two scores of internal assessment will be considered as the final grade in the subject and will be printed in the final mark sheet. No retest shall be held.
- ✓ Failing in eight heads in a year is the maximum limit for allowed in the ATKT system.

 (Maximum five theory exams and remaining Prac./Oral/Internal etc.)
- ✓ Failure to submit the journal in any subject (even for a single subject) will result in detention (i.e. loss of one year). Admission for the next academic year is to be taken by repayment of fees.

9. TERM GRANT

Term Sanctioning (allowing the students to appear for the University examination) entirely depends upon the following:-

- Minimum of 75% attendance in all subjects.
- Satisfactory completion of all term work
- Practical and Assignments as specified in Syllabus or as instructed within prescribed periods
- General behavior of students, both inside and outside the classroom.

10. EXAMINATIONS

The University of Mumbai conducts examination in every semester and two class tests are conducted by the college in every subject. The performance in these tests is considered for grade calculations as per the MU circular. The submission of term work in each subject should be taken very seriously. If a student fails to submit his/her Term work or Sessional work before the prescribed date, he/she will not be allowed to sit for the concerned examination.

11. USING UNFAIR- MEANS IN THE EXAMINATIONS

During an examination, if a student is found either possessing any material (written or printed) with the intention of copying or actually copying, he/she is liable to be severely punished according to the rules laid out by the University. In such circumstances, a student may either be debarred from appearing for the entire examination or expelled from the University of Mumbai for a minimum period of three years as per the decision of "Unfair - Means Committee ".

12. ATTENDING LECTURES/LABS

- 1. Students coming late to the Institute will not be allowed to enter the premises.
- 2. Bunking classes is strictly forbidden. If the student misses even one period, he/she may be marked absent for the whole day.
- 3. In order to monitor the academic progress as well as the overall development of our students, we nominate one staff member as faculty advisor to whom a student can approach for guidance. For day to day functioning, class teacher's and class counsellors are associated with each class. The students, as well as their parents, are welcome to contact either the class teacher/class counsellor or the faculty advisor any time during the session.
- 4. While the lecture or the lab is in progress, a student is supposed to keep his/her mobile phone switched off, failing which severe punishment can be given.
- 5. After their classes are over, the students should leave the premises in a proper disciplined manner, without making any noise or crowding in the corridors.

13. INFRASTRUCTURE AND SALIENT FEATURES

The VESIT campus strikes a balance between modern facilities and natural surroundings to create an atmosphere that is ideal for students to learn, explore, and grow.

- ✓ Sprawling Complex
- ✓ Equipped Library having more than 45000 technical books
- ✓ Professional Societies
- ✓ Active Placement Cell
- ✓ Strong Alumni Association with Alumni Web Portal
- ✓ Conference Hall
- √ Seminar Halls
- ✓ Auditorium
- ✓ Music Room
- √ Huge Canteen
- ✓ Inviting Playground

- √ Amphi Theatre
- ✓ Recreation Room

Labs set up in collaboration with industry

- ✓ Automation Industry Association Lab
- ✓ Cloudera Big Data Analytics Lab
- ✓ National Instruments Labview Academy
- ✓ D Link Academy
- ✓ e-Yantra-MHRD-IITB initiative

14. FEE STRUCTURE

FIRST YEAR - BACHELOR OF ENGINEERING

Maharashtra State Board Candidate			
1. Open Category Students		1. Open Category Students	
Particular of Fees	Amount	Particular of Fees	Amount
Tuition Fees	94,545	Tuition Fees	94,545
Development Fees	9,455	Development Fees	9,455
University Charges	546	University Charges	546
Caution Money	1,000	Caution Money	1,000
Enrollment Fees	200	Enrollment Fees	200
Yuvaraksha Insurance	100	Yuvaraksha Insurance	100
		Eligibility Fees	300
Total	1,05,846	Total	1,06,146

FIRST YEAR - MASTER OF ENGINEERING

Mumbai University Candidate		Other than Mumbai University <u>Candidate</u>	
1. Open Category Students		1. Open Category Students	
Particular of Fees	Amount	Particular of Fees	Amount
Tuition Fees	66,364	Tuition Fees	66,364
Development Fees	6,636	Development Fees	6,636
University Charges	5,46	University Charges	546
Caution Money	1,000	Caution Money	1,000
PG Registration	825	PG Registration	825
Yuvaraksha Insurance	100	Yuvaraksha Insurance	100
		Enrollment Fees	200
		Eligibility Fees *@	200
Total	75,471	Total	75,871

FIRST YEAR - MASTER OF COMPUTER APPLICATION

Mumbai University Candidate		Other than Mumbai University <u>Candidate</u>	
1. Open Category Students		1. Open Category Students	
Particular of Fees	Amount	Particular of Fees	Amount
Tuition Fees	72,727	Tuition Fees	72,727
Development Fees	7,273	Development Fees	7,273
University Charges	546	University Charges	546
Caution Money	1,000	Caution Money	1,000
PG Registration	825	PG Registration	825
Yuvaraksha Insurance	100	Yuvaraksha Insurance	100
		Enrollment Fees	200
		Eligibility Fees *	200
Total	82,471	Total	82,871

15. ABOUT CAPITATION FEE

INSTITUTE / MANAGEMENT <u>DOES NOT</u> ACCEPT ANY TYPE OF CAPITATION.

Refer to Anti Capitation Fee Act available on our college website: http://vesit.ves.ac.in or http://vesit.ves.ac.in or http://vesit.ves.ac.in or

16. SPECIAL PROGRAMMES

Exclusive Scholar Programme

Exclusive Scholar Programme launched in January 2013, is a program which is envisaged for academically bright students, who are interested in any extra practical/ study project beyond the curriculum. These students, if needed, may be given exemption in either theory or practical classes, depending on the evaluation of the student and the requirement of the extra project work by the mentor /subject teacher.

Looking Beyond Syllabus

Looking beyond syllabus was launched in the year 2011, with an aim to expose the students to emerging technologies and opportunities and to work as teams on multidisciplinary projects under the mentorship of a faculty member assigned to them, in their area of interest/passion that may not necessarily be his stream of study.

The main objective of **looking beyond syllabus** of technical education is to infuse prospective technocrats with the ability to apply basic science and experimentation skills, to understand problems in the industry, academia and humanity at large and also be able to develop practical solutions. It also aims to model them into wholesome professionals in the field of Technology. Looking beyond syllabus also prepares the students to use their skills with a strong base for higher education and acquisition of knowledge to sustain a life-long career in related areas.

Awakening the Scientist

Awakening the Scientist is an opportunity given to the First year students to take the first step in this transformation of a student to a professional. It is a group activity. The objective is to identify the science, engineering and technology associated with gadgets, equipment's and tools used in day to day life and also relate the past, present and future of such gadgets, equipment's and tools used in day to day life.

Skill Enhancement Lectures

VESIT believes in giving its students an invigorating academic environment. The regular syllabus of Mumbai University is conducted by our faculty very effectively. An extra edge is given to our students by inviting experts from the industry and research institutions .This endeavour is termed as 'Skill Enhancement Lectures'.

17. MAJOR LABORATORIES

Department Of Electronics Engineering

Basic Electrical Engineering Lab

Laboratory courses conducted: - Basic Electrical and Electronics Engineering, Electrical Workshop, Electronic and Electrical Measuring Instruments and Machine. Lab is equipped with 1-phase and 3-phase Motors, different types of Transformers, A.C. and D.C. Generators, Motors, Trainer kits for network theorems, analog and digital measuring instruments etc.

VLSI Design Lab

Laboratory courses conducted: - Basics of VLSI Design, IC Technology and CMOS VLSI Design. The department is a member of Cadence University Program; subscribed to cadence VLSI design suite, running on 1- Intel Xeon processor,19 clients with 20 cadence licenses and FPGA boards.

Electric Circuit Design and Analysis Lab

Laboratory courses conducted: - Basic of Electronic Circuits, Electrical Network Analysis and Synthesis, Electronic Circuit Analysis and Design. This lab is equipped with sufficient number of CROs, Function Generator, DMM etc. Software's required for design and simulation of electronic circuits are also available.

Digital Logic Design Lab

Laboratory courses conducted: Digital System Design I and II, Linear Integrated Circuits and Design. Lab is equipped with software's for digital and analog circuit simulation, FPGA and CPLD training kits, Digital measuring instruments etc. This Lab also extends support to the final year students for their Project Work.

Advanced Microprocessor Lab

The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. All electronic testing equipment's are available. Digital Trainer Kits and Demonstration kits (RAM and ROM), Universal Programmers, cards for ISA, PCI and different connectors helps in better understanding of computer architecture.

Power Electronics Lab

Laboratory courses on Industrial and Power Electronics are conducted in this lab. The Lab is adequately equipped with Test equipment's for Power Electronic Devices and circuits. Various demonstration kits and measuring instruments are also available in this lab.

Electronics Workshop-PCB Lab

Laboratory courses conducted: - Electronics Workshop - I and II, PCB Workshop. Lab Facilities - PCB Exposer, Film Exposer, Dip Coat Machine, Etching Tank, Drilling Machine etc. Students perform their Electronic Workshop project designing, testing and implementation in this lab.

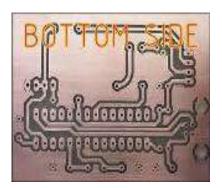












Department Of Electronics and Telecommunications Engineering







Analog and Digital Communication

Laboratory courses conducted: - Principle of Communication Engineering, Digital Communication, Mobile Communication Systems and other analog and digital communication related subjects. Lab is equipped with DSO-(Agilent, Aplab, Tektronics, Rigon), Spectrum Analyzer-3GHz (Agilent, Hameg), Signal Generators-AM/FM, Computers with latest configuration etc. COMMSIM:- Network and Communication Simulation Software, Digital Communication Trainer kits like 16-QAM, QPSK, PSK, TDMD etc.

Microwave and Advanced Communication

Laboratory courses conducted: - RF Circuit Design, Microwave Devices and Circuits, Computer Communication Networks, Electromagnetic wave Theory etc. Lab is equipped with DSO-(Agilent, Aplab, Tektronics, Rigon), Spectrum Analyzer-3GHz (Agilent, Hameg), Standard Signal Generators for RF-range, Network Analyzer (Agilent), Transmission Line Trainer, Computers with



latest configuration etc. Softwares like MATLAB, Ansoft, and PCADD are used for simulation.

Network and Wireless Communication

Laboratory courses conducted :- Discrete Time Signal Processing, Computer Communication Networks, Image Processing, Wireless Communication, ICE etc. This LAB consists of PCs with latest configuration, server, Wireless LAN, PDAs and Mobile phones for experimental purpose. All computers are installed with necessary simulation software's.

Optical Fiber Communication

Laboratory courses conducted: - Optical Fiber Communication, MOC. This Lab also extends support to the final year students for their Project Work. The lab consists of latest equipment's/software's like OTDR, Optiwave, Trainer kits for calculation of IV Characteristics, WDM, Chromatic dispersion analog and digital signal transmission, 1550nm and 1310nm optical sources etc.



T.V. and Sstellite Communication

This lab supports conducting practical and some project work for SE to BE students. The practical's conducted for the subjects like TV and Video Engineering, Satellite Communication, Antenna and Wave Propagation, RADAR etc., equipment's like different TV models, B/W and colour pattern generators, Satellite communication module, and complete setup of cable- TV and CCTV etc.

Department Of Computer / Information Technology / MCA

All the computer laboratory are spatial and well equipped with the latest configuration and well networked Machines.

Programming Lab

Programming Lab supports students in learning courses conducted in Programing Concepts and C++, Programming and Data Structure, Computer Graphics, Object Oriented Concepts through Java, Web Applications etc.



Network and Security lab

Aims to provide an environment that aids in effective learning of Networking & Mobile Computing concepts. Systems on the Linux Platform and several development toolkits for mobile computing are some of the features. It consists of Packet Tracer, NetSim Academic, Sun Java Wireless Toolkit, Visual Paradigm. The courses conducted are Computer Networks, Cryptography and System security, Networking Programming Laboratory.

Specialized Lab – Machine Learning & Big Data Analytics Lab

This specialized lab is for final year Elective courses Big data analytics and machine learning. It is equipped with latest high end computers. Cloudera distribution for Apache Hadoop version 4.4.1 which comprises of Hadoop, Hive, Pig, Hbase is used along with other open source tools like R, Mahout.

Database Management Systems

A well-equipped lab comprising of latest computers installed with various Database tools & Servers like Microsoft SQL Server 2005/08 & MS Access (under dreamsparx licence), Oracle 11g, Visual Studio 08/10.,Netbeans etc. The courses conducted are Distributed databases, Operating Systems and Systems Designing.



Cloud Computing Lab

Caters to the final year course and has open source software's.

Project Lab

A well-equipped lab catering to final year students for their projects and courses.

Department Of Instrumentation Engineering

Process Automation Lab

The Process Automation Lab aims at training the students in the field of Process Instrumentation, control and optimization. PLC and Emerson DELTAV DCS controlled systems are also used to train the students.



Process Instrumentation Lab

The Process Instrumentation Labs where the study of some major process control instruments /equipment's is done. This lab covers mainly the digital and analog I/O PLCs of Allen Bradley and Technimount companies, PC based flow and level process control loop, Ellipse SCADA based Ratio and Cascade controls and pilot plants setups, Differential Pressure Transducer (DPT) Calibration set up, I/P Converter, Control Valve Positioner, Control Valve Sizing set ups and Hydraulic and Pneumatic trainers.

Transducer Lab

This lab is equipped with all latest transducers used in the industry.

Biomedical / Analytical / Nuclear Instrumentation Lab

This Laboratory has been established to familiarize the students with capturing, analysing and processing different bio signals. Biopac is an integrated instrument which students use for such experiments. Physiology Lab kits and digital temperature feedback system are also used for training the students in the area of Biomedical Instrumentation.

Analog and Digital Lab

Laboratory courses on Analog & Digital Electronics are conducted in this lab. The Lab is adequately equipped with Test equipment's for Electronic Devices and circuits. Various demonstration kits and measuring instruments are also available in this lab.

Control Systems and Digital Signal Processing Lab





The lab is equipped with Pentium-IV (IBM) and HCL computers with latest softwares such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. Digital Trainer Kits and Demonstration kits (RAM and ROM), Universal Programmer, cards for ISA, PCI and different connectors in the lab aids in better understanding of computer Architecture.

18. INDUSTRY COLLABORATION

1. AIA (AUTOMATION INDUSTRY ASSOCIATION):

VESIT is the only technical institution in Mumbai which is authorized by AIA (Automation Industry Association) to be a 'Competency Development Centre' for conducting course on "Industry Automation" as a part of its Campus Connect Initiative. The newly developed Integrated Automation Lab was inaugurated by Padmashri Dr. G. D. Yadav, Director, Institute of Chemical Technology, Mumbai on 2 March 2016.



2. NI LabVIEW Academy:

V.E.S. Institute of Technology has started NI LabVIEW Academy in collaboration with National Instruments (USA), **Ben**galuru on 9 July, 2013. This is the first of its kind in Mumbai region.

LabVIEW is a graphical programming software which provides comprehensive tools that are required for any type of test, measurement or control application in dramatically less time. After completing this course, the participants will be able to appear for CLAD (Certified LabVIEW Associate Developer) examination, conducted by National Instruments (NI), USA. The CLAD distinguishes basic proficiency in using NI LabVIEW to develop applications. The certification and course work conducted by academy is useful for developing expertise in the LabVIEW environment for career advancement and employment opportunities. NI LabVIEW Academy will provide strong platform for interaction with industries and R & D centres to undertake research projects. Under this academy, training programmes are conducted for UG and PG Engineering staff and students.

3. e-Yantra Lab Setup Initiative (eLSI):

V.E.S. Institute of Technology is equipped with state of art facility for Robotics, e-Yantra Robotic

Lab under eLSI (e-Yantra Lab Setup Initiative).

e-Yantra Lab Setup Initiative (eLSI) is an initiative by the IIT-Bombay under which colleges are encouraged to set up robotics labs. It is designed as a scalable and sustainable approach that addresses infrastructure creation and teacher training to create an eco-system at the colleges to impart effective engineering education. This facility is availed to the students, so that they can focus on embedded system, robotics projects.



e-Yantra Lab is equipped with Firebird V, Spark V, Qu-Bot robots, various hardware, software platforms required to build develop embedded projects. Every year 20-25 teams of students participate in Robotic competition.

4. Accenture Lab:

"VESIT-Accenture Innovation Centre" is a specialized lab developed in accordance with VESIT's MoU with Accenture. This lab is used by Accenture to conduct their Head Start Foundation Program (HSFP) for students selected through campus placement, faculty development program and other similar activities.



5. Cloudera Lab:



The Big Data Analytics lab in collaboration with Cloudera provides a high end distributed environment for Big Data processing, through which training is provided in specialized area 'Hadoop' and projects are undertaken to apply the concepts using Cloudera tools. A six week course is regularly conducted for the final year students.

6. VESIT D-link Academy Lab:

D-Link academy at VESIT started on 1 April 2015. This academy trains students in batches which will equip them for better placement opportunities. As a part of this venture, a specialized laboratory is set up in EXTC Department for wireless communication.

Under this academy, our faculty members were trained by D-Link to conduct this course for our students. This is an excellent opportunity for our students to get industry certification in their own campus.

19. RESEARCH AND DEVELOPMENT LAB

R&D Department undertakes various R&D projects from industries and works on various research problems for M.E and Ph.D. students in VESIT.

Some of the successfully completed R&D Projects are:

Electronic Radioactivity Simulator in collaboration with BARC, which was funded by BRNS.

- ➤ Multi-channel analyzer, Nuclear Counting System in collaboration with Nucleonix, Hyderabad.
- ➤ Analysis and Monitoring of Degradation of Insulation in H V Transformers and "Nuclear Detector Characterization" in collaboration with I.I.T, Mumbai.
- ➤ "Sensor network for Agricultural application" in collaboration with TCS and "Low level signal generation" signal processing circuit for Rogowski coil based integrator with low time constant in collaboration with L & T.

Research Projects currently being carried out in collaboration with industries are:

- ➤ Low level sensing (from 2mA onwards) in industrial environment, DC level shift correction for sensing true AC signal, Peak detection of low level signals in frequency range of 5KHz to 25KHz, High Speed Time to Amplitude Converter and Leakage detection using Fibre Optics with L&T.
- Application of Kalman Filter to Nuclear Engineering Data and Bayesian Estimation of Nuclear Engineering Data with BARC.

Our Ph.D. students are carrying out research work in the areas of:

- ➤ Low Level Signal Processing systems.
- ➤ Reconfigurable ADCs
- Optimize Nuclear Spectroscopy System
- Research in New Techniques for on-line monitoring insulation degradation
- ➤ Research for changes in characteristics of optical fibres with changes in various Parameters like temperature, vibration etc.

20. COMMON INFRASTRUCTURE

LIBRARY

Our library boasts of more than 47,000 books and journals of national and international level. The number of standard journals / magazines: Journals – 30, Magazines-60, e-journals - 747. Library also provides:

➤ Internet and all e-journal access through college campus as well as off campus V.E.S. INSTITUTE OF TECHNOLOGY 30

- > Open access facility for all
- ➤ IIT Library Membership
- Membership with NDL
- ➤ Best User facility for student
- > NPTEL lecture facilities
- > Best user get additional book facility

Along with technical updates via latest journals, students can also keep themselves abreast with the latest happenings in the world via Newspaper, Magazine and Internet facility. The library is divided into two sections:

Reference Books: - This section stocks recent editions of textbooks. Student can borrow one book at a time against their Library Identity Card. The books/journals in this section are meant for reading in the hall only. The question papers of previous examinations conducted by the University of Mumbai are also available in this section.

Lending Section: - Students can borrow two textbooks for verification from this section. Generally the students can borrow books for one week. A fine will be charged if a book is not returned within the given period.

Students are required to abide by the following rules of the Library. Any violation of these rules will lead to a fine or punishment by the Principal.

- a. Every student must submit his/her valid identity at the entrance of the library and the reading room. Ex-students of the college, who are members of Alumni Association, can use the library facilities.
- b. Pencil or ink marking and writing on the pages of the library books or any other way of mutilating the books will be treated as library indiscipline. The first reader will be held responsible unless he/she points out to the Librarian at the time of issue of the book.
- c. The reference books, journals, question paper sets, dictionaries etc. will be issued for reference in the Reading Room only and on no account can they be taken out of the Reading Room.
- d. Students must refrain from disturbing other members in Library and Reading Hall.
- e. The student should inform the Librarian immediately of any loss or damage to the books borrowed by him/her on the library card.

- f. Any other matter not covered by these Rules will rest at the absolute discretion of the Principal and Librarian.
- g. Student should not bring any valuable article to the Library. If such things are lost, the Library staff cannot be held responsible for it.
- h. Students should maintain complete silence in the Reading Hall.
- i. Students should contact the Librarian if they face any problem regarding usage of library facilities. They should not argue with the staff on duty.



OTHER FACILITIES:

PROJECT ROOM

Separate Project rooms are provided for each department with the laboratory facilities and computers installed. The students can carry out their project work even after the college hours.

CANTEEN

The college offers a huge canteen, with modern kitchen facility, serving fresh and hygienic food which offers a multi-cuisine variety.

CONFERENCE HALL

Seminars and Students' presentations are held in the Conference Hall, which has a seating capacity of 250 students. It is equipped with a good sound system along with video and data projectors.

MUSIC ROOM

Music Room is well equipped with instruments such as Flute, Violin, Guitar, Tabla etc. Students can learn and practice after the college hours. Student Council wing "Bliss" (Beauty lies in Soulful Symphony) is in-charge of Music Room and conducts music competitions and workshops for the benefit of students.

SPORTS

Life of an engineer is incomplete without any extracurricular activities. For a healthy body and mind, apart from the usual workload and stress, VESIT provides its students with a sporting extravaganza SPHURTI. SPHURTI is an integral part of VESIT's history and it has been so since the beginning of VESIT.

- ➤ **Play ground:** 8177 sqm
- ➤ Outdoor games: Football, Cricket, Volley ball, Basketball, Throw ball, Athletics, Discuss throw, Shotput.
- ➤ **Indoor games:** Carom, Chess, Table Tennis.
- > Recreation Room

21. TEACHING FACULTY

Our Institute is well known for its highly qualified and skilled teachers, sharing a great deal of experience. They have contributed greatly in imparting quality education at our Institute. Our teaching faculty includes 20 Ph.D. Holders, 151 post graduates (29 pursuing Ph.D.), 02 Graduates and 15 other post graduates. Each department also takes initiative to organize "Faculty Development Programs" for helping the teachers to update their technical knowledge. These refresher courses in turn helps the teachers to deal with their subject and the projects in a professional manner. The staff members are also encouraged to acquire higher qualifications.

22. STUDENTS' COMMITTEE/ACTIVITIES

Professional Societies

A unique feature of VESIT is the fact that there are four Professional societies totally managed by students under the supervision of Faculty In-Charges.

1. IEEE – Institute for Electrical and Electronics Engineers

"Excellence through Innovation" is what the IEEE-VESIT stands for in VESIT society culture. IEEE-VESIT gives its members ample opportunity to develop and inculcate their technical and soft skills, be it in theory via seminar or in practice via interactive workshops.

In-house Publication "Innovations"

Symposium "Melange"

Website <u>www.ieee-vesit.org</u>

Activities during the Year:

LabVIEW and Matlab workshop, Quizard, Android WS, Bluffmaster, TROTA - e Rise of e Alliances was the insidious mind game of politics and war, based on worldly events in historical chronology, Treasure Hunt, Online treasure hunt, FPGA, FVPL, among the other events on the platter are TPP, Bewitch, WIE Workshop and e Deep Web workshop.

2. ISA- Instrumentation Systems and Automation

ISA is a leading global non-profit organization that caters to the needs of over 30,000 worldwide members and other professionals. ISA-VESIT student chapter is well rooted with the international organization. ISA-VESIT chapter expose its members to the upcoming competitive world and tries to give them an edge over the others when it comes to practical knowledge through various events and seminars.

In-house Publication "Envisage"

Symposium "Enguage"

Website www.isa.org

Activities during the Year:

Strategy based games like e Oblivion Conundrum, FE fun event, PCB Workshop, PLC Workshop, Analog Electronics Workshop, Arduino Workshop, Lab Awareness Sessions, Project Exhibition, IoT Workshop, and Intercollege Events Manthan 2017.

3. CSI- Computer Society of India

The CSI VESIT stands for ingenuity, innovation and versatility. CSI-VESIT aims to give its member an added dimension to technology by keeping them abreast with the rapid advancements in the wide arena of technology. It is one of the most active student societies with the objective to accelerate student expertise in the field of technology. The CSI-VESIT student chapter has been retaining the award of The Best Student Chapter for the past seven years.

In-house Publication "Random Thoughts" and

"Nexus"

National Board "Communications"

Annual magazine "Interface"

Symposium "Impact"

Website www.csi-vesit.org

Activities during the Year:

Conducts Technical Events like Hidden Cipher, Wordpress workshop, Code Breaker, Java fx workshop, Business Quiz, Python Workshop, Virtual Stock Market, Article Writing and Non - Technical events like Cash In, LAN Gaming, Cric-o-Mania, and Idiot Box.

4. ISTE-Indian Society for Technical Education

VESIT-ISTE chapter's objective is to bridge the gap between the curriculum and the industry's requirements by giving students a platform to hone their technical and non-technical skills like overall personality, reasoning and presentation skills.

In-house Publication "Mirage"

Symposium "Synergy"

Website <u>www.iste-vesit.org</u>

Activities during the Year

MoAround e World, Techtrix, Maximus, Placement Week, Technical Paper Presentation, Gangsta Blues which would be the second mega event for this semester and is known for attracting a huge amount of crowd, D-deal which is an online event, Article Writing in the Technical and Non-Technical sections and the SE Coord Event designed by the super talents SE coordinators of the society for its SE members.

E-Cell (tVEC)

Entrepreneurship is the capacity and willingness to develop, organize and manage a business venture along with any of its risks in order to make it a success. Innovation is the specific instrument of entrepreneurship...the act that endows resources with a new capacity to create wealth.

But entrepreneurship is neither an art nor science; it's pure practice. It takes years of self-improvement, determination and perseverance to achieve what is seen as overnight success. Hence to bestow upon this practice of entrepreneurship to its students from the very beginning, the VESIT Entrepreneurship Cell (tVEC) was founded in 2014.

tVEC comes with many innovative events throughout the year to imbibe the entrepreneurial attitude in students, because entrepreneurship is not about dreaming but doing. tVEC events like hackathon compelled the competitors to rack their brain cells and brainstorm to come up with feasible solutions to real life problems. Internship mela made the participants interview-ready for their near future. Several talks and panel discussions are held by elite entrepreneurs and industrialists to clear any ambiguities amongst the students about Entrepreneurship. Bizcafe trains students to be a complete entrepreneur inspiring them to turn their ideas into business model.

VES LEADERSHIP ACADEMY AND RESEARCH CENTRE (VESLARC)

To be successful in today's world of severe competition and stress, it is necessary to have the right attitude and positive thinking. At VESIT, the students are helped by the specially appointed faculty of VESLARC to develop a strong personality with leadership qualities.



PRAXIS

In order to be successful, one must have a competitive spirit and a positive attitude towards learning from mistakes. To nurture and promote this competitive spirit, VESIT organizes PRAXIS every year. PRAXIS is our annual inter-collegiate technical festival which has an extremely competitive Robotics event that has become a much awaited event among engineering students. It

is gaining popularity not only in the Mumbai University but in other Universities as well. PRAXIS also develops a sense of team spirit and event management skills along with a sense of belonging.





Cultural Activities

- In spite of all these activities, the college does not neglect cultural talent.
- > UTSAV: The College organizes UTSAV, a cultural festival that showcases cultural talents. It witnessed overwhelming participation from our students.

OCTAVES: Octaves is a music fest which was organized by the Music Council of VESIT. There was tremendous participation in this festival. For the first time in the college, an Intra-department music festival had an entry of 100 students and made a mark to the music club.

ILLUSION SEE THE UNSEEN: Illusion is an intra-collegiate competition wherein six teams comprising of students from all the five branches of engineering, as well as M.C.A., compete in various portfolios like Dance, Drama, Sports, Aesthetics and Music.

ANNUAL DAY is a cultural extravaganza which is a complete event management exercise for our students under the supervision of our faculty. Annual prize distribution is also held on the same day.

Teachers' day The College organizes Teachers' day function for staff members every year. Teachers completing 20 years of service are felicitated on this day.

BLISS stands for "Beauty Lies in Soulful Symphony". BLISS is the musical group of our college which is run by students passionate about music. BLISS has successfully organized many musical events since 2010.



Separate music room with very well equipped instruments from classical to western is provided for students to practice.

Technology Day: On Technology day, we organize project exhibition and poster competitions for our final year and pre-final year students. Students actively participate and demonstrate their research ability as well as get to know the advancement in technology. This day is celebrated in the memory of our founder trustee "Shri. K. B. Mansharamani".

VES Premier League: This is organized by a group of Football enthusiasts from our college in collaboration with other VES members. VESIT's premier football tournament, the VESIT Premier League re-turned with its fourth season this academic year. The league comprises of eight teams, divided into two groups and is played in the League and Knockout fashion. The captains are chosen

from amongst a group of prominent footballers of the third year, while the second year students fill out the vice-captain spots. The captains actually 'buy' their teams from amongst the set of more than 200 registered players.

Tree Plantation Program (1st July 2016)

As a part of its support to environmental conservation measures and reiterating its commitment to increasing the green cover in the state, Vivekanand Education Society's Institute of Technology (VESIT) undertook a tree plantation drive on its sprawling campus on 1 July 2016. Students



Vivekanand Education Society's

Institute Of Technology



and faculty of the institute whole-heartedly participated in the drive which saw the planting of more than 50 trees. The tree plantation exercise was undertaken to lend strength to the 'Vanamahotsav 2016-17' initiative of the Maharashtra government under which three crore plants are planned to be planted across the state, started in July 2016.

VESIT Cricket League: This is similar to the VESIT Premier League but for all the cricket lovers of VESIT. VESIT Cricket League returned this academic year with its third installment. The league comprised of eight teams and all captains were from the third year. The captains initially battled it out in a scintillating auction round. The teams were named after iconic cricketers.

Social Responsibility Team (SoRT)

Together, we can make a difference. With this amazing thought inspiring theme, e Social Responsibility team grows stronger each year, depicting on how to make a difference actually does pay off. The major objective was reaching out to the needy, collaborating with NGOs- by volunteering to sell their products (handmade) in the college. This year, SoRT collaborated with 5 NGO's in all, namely, National Association For Equal Opportunities for Handicapped (NASEOH, Chembur), Are You Recycling? (RUR, Matunga), Bal Anand(Chembur), Self Esteem (Chembur) and Karmayog.

List of activities for the year 2016-2017.

1. Blood Donation Drive, 22 July 2016

SoRT in association with Hashu Advani Memorial Trust and with the collaboration of doctors from JJ Hospital organised- a blood donation camp, Thalassemia test and an Eye checkup. An overwhelming count of 140 people for blood donation, 200 for Thalassemia test and 150 for eye checkup turned up.

2. Bone Marrow Donation Registration, 12 August 2016



In association with the Marrow Donor Registry (India), a Bone Marrow Registration drive with a total count of 168 registrations were made by the students and the staff, all under the proper guidance and supervision of doctors from Fortis/Raheja Hospital. e sample collection of cheek epithelial cells was done.



3. Collaboration with Chembur Welfare Brigade-Gokulashtami, 25 August 2016

Recognizing the need for citizen participation in assisting the police to maintain decorum and reduce instances of unruly behavior, the local self-help group Chembur Welfare Brigade approached SoRT.

SoRT organised a team of 20 boys to go out onto the roads and assist the policemen to maintain public discipline on the streets.

4. SoRT Week, 29 July 2016 - 3 August 2016

The major objective was reaching out to the needy, collaborating with NGOs- by volunteering to sell their products (handmade) in the college. This year, SoRT collaborated with 5 NGO's in all, namely, National Association For Equal Opportunities for Handicapped (NASEOH, Chembur), Are You Recycling? (RUR, Matunga), Bal Anand (Chembur), Self Esteem (Chembur) and Karmayog. This year, a Virtual Reality workshop was also included in association with People for the Ethical Treatment of Animals i.e., PETA, which helped us spread awareness about cruelty against animals in our campus.

5. Helping Banks with the process of Demonetization, 14 November 2016

Due to the chaos due to demonetization, long queues outside banks and ATMs were a common sight. SoRT recognized the need to help people through this process and decided to go out to the banks and help everyone out. The team went to the branches of SBI, Janakalyan and HDFC banks. Students of our college were seen volunteering in large numbers. The team helped the people standing in scorch sunlight by



providing everyone with water and helping people fill out the forms.

6. Lakshya Foundation on Knowing your Army, January 27th, 2017

Lakshya Foundation, with their main objective to bridge the gap between civilians and the armed forces, came to VESIT on the 27th of January with two Majors from the armed forces to have an interactive session with our students and to let us know more about our brothers and sisters at the border.

SoRT VESIT wished to take this crucial step to form a bond with those who sacrifice their lives for us.



Vigilance Awareness Week

Vigilance Awareness Week was observed in the institute during 31 October 2016 to 5 November 2016. Students had taken Integrity Pledge in the class. All Staff had taken online Integrity Pledge available on Central Vigilance Commission's web site http://pledge.cvc.nic.in/

Vachan Prerana Divas

The 15th October 2016 was celebrated as Vachan Prerana Divas to commemorate the birth anniversary of former president, late Dr. A. P. J. Abdul Kalam. On this day, there were stalls on the ground floor of the college, having a display of various inspirational books, religious books, biographies as well as works of literature. Some books were "Wings of Fire" by Dr. Kalam, "Gita" by C. Rajagopalachari, the Quoran, "Gitanjali" by Rabindranath Tagore and "Playing it my way", Sachin Tendulkar's autobiography. Banner published on 15 October 2016:



Constitution Day - 26th November



Celebration of 68th Republic Day

India celebrated her 68th Republic Day with pride on 26 January 2017. VES also organized a ceremony at the VES Polytechnic ground. The members of various VES institutions assembled to celebrate the Republic Day. Following the flag hoisting, there was a parade of the student scouts followed by the cultural programme presented by the different institutes. The ceremony concluded with national anthem and a grand salute to the national flag.

Martyr's Day

Every year, 30th January is mourned as Martyr's Day (Hutatma Din). On this day, we pay our respects to the brave freedom fighters and all those valiant soldiers who sacrificed their lives for the sake of the nation. This year as well the Government of India had issued a circular to all the institutions in the country to maintain two minutes of silence to pay homage to the martyrs. All the

institutes including VESIT, as instructed maintained the two minute silence at 11.00 a.m. and paid a heartfelt tribute to all the departed brave souls.

Engineers' Day

15th September was celebrated as Engineers' Day marking the birth day of Sir Mokshagundam Visvesvarayya.

Disaster Management / Mock Evacuation Drill

Mock Evacuation Drill was successfully conducted at VESIT on 27 February 2017 by Freedom For You Foundation, NGO for Empowerment of Citizens. The drill was followed by hands on workshop for students and staff on the demonstration of Fire Safety Equipments.



23. ACHIEVEMENTS

VESIT enters the Platinum Club





VESIT placed in Platinum Category

Dr. J. M. Nair receiving the Platinum award

All India Council of Technical Education (AICTE) in association with Confederation of Indian Industries (CII) conducted an Industry-linked technical institute survey during 4 May 2016 to 31 August 2016 and the result of the survey was declared in the recently concluded UK-India tech summit.

Out of the total 12610 eligible institutes in the category of Architecture, Pharmacy, Management and Engineering, 890 institutions successfully submitted the application for the survey. These institutions were then placed in three categories viz. Platinum, Gold and Silver according to the scores acquired by them. It is a proud moment for all us with VESIT bagging the platinum category.

VESIT the Most Promising Engineering College

Vivekanand Education Society's Institute of Technology VESIT) won the "Most Promising Engineering College with Excellent Faculty in Maharashtra" award at the 7th Asia Education Summit and Awards, 2017 by Worldwide Achievers.

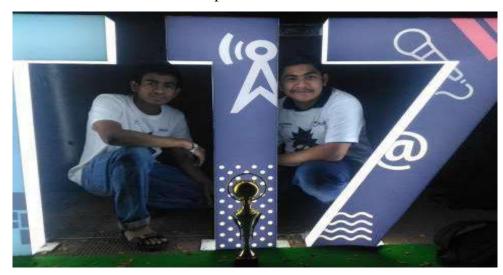


V.E.S. INSTITUTE OF TECHNOLOGY

VESITians win the Hackathon '<I-Hack>'

Learning is the only thing the mind never exhausts, never fears and never regrets" - Leonardo Da Vinci.

A chance to learn is a chance to grow. Hackathons gives opportunity to learn and hone skills of the students. '<I-Hack>' was a Hackathon organised by the IIT-Bombay as a part of the Entrepreneurship Summit from 27-29 January 2017. The event was sponsored by Facebook, Cimpress, Texas Instruments and other companies.



Shreshthata Award

The 'Shreshthata award' is an initiative by VESLARC (VES Leadership Academy and Research Centre) to acknowledge and encourage students who endeavor to make a difference in spite of unaccommodating circumstances.

On 15 October 2016, Mayur Kalwani of INST D18 and Tarleen Dand of EXTC D19C were awarded the Shresthta Award for conquering adversities while excelling in various domains and continued to maintain academic grades.

Tinkerers' Lab: Students' Secret to Creativity

"It's not about ideas. It's about making ideas happen." - Scott Belsky

The Tinkerer's Lab at VESIT was started with an aim to bring together the students who wanted to learn and the students who wanted to teach. It also aimed at using the available resources in the college optimally and to eliminate the hassle of starting a project from the scratch and working on redundant tasks. It is currently operational in three departments viz. Department of Electronics, Department of Electronics and Telecommunication and Department of Instrumentation. The aim of Tinkerer's Lab is to make lab resources available to the students and also e-waste of various labs. Students from all years, that is, from first year to final year, were encouraged to work together. This increased the technical interaction between seniors and juniors. Also, the juniors found immense help in refining or coming up with newer ideas. It can be proudly said that some of the notable projects were a result of these numerous ideas and tinkering done by the students in the lab, which has also won laurels for our institute.

Rohit Kashyap and his team developed a semi Autonomous Pipeline Exploration Robot-SAPER and were **runners-up in NIYantra 2016.** SAPER is an in-pipe exploration robot that can help local corporations identify the problems in the existing network of ducts and pipelines without excavating and before they lead to disasters like floods, contaminated drinking water, health hazards, etc.



This is a cell comprising of staff members from all branches including MCA that suggests and implements innovative schemes to afflux VESIT for achieving academic excellence. It helps in increasing the use of technology in teaching learning process, to create knowledge sharing

facilities and helps in enhancing overall skills. Various events for staff were conducted during the Academic year 2016-2017.

VRC conducted college level eliminations for the Smart India Hackathon on 18 January 2017, where participants were eliminated based on their project presentation.

SMART INDIA Hackathon

VESIT was a part of and was one of the winners in the very first edition of the Smart India Hackathon, which was held on the 1 & 2 April 2017. The Smart India Hackathon 2017 was a national level initiative by the Ministry of HRD. It is the world's biggest digital movement. In the final round, there were about 1266 teams providing solutions to 29 Ministries.





Two teams from the college were shortlisted for the final 36 hours intensive coding round from the seven teams that participated, with one team securing the Second Runners-up position. The team from the MCA department had secured the Second Runners-up position, receiving a cash prize of Rs. 50,000 for their solution to a problem statement by the All India Council for Technical Education (AICTE). The team from IT department of Engineering provided a solution for a problem by the University Grants Commission.

Summer & Winter School Training

Training is arranged for the students who wish to get some academic edge over their peers during winter and summer vacations. Different departments organize training programs on recent technologies or industry relevant topics; where experts of the domain deliver highest quality of input to the participants. The training schools offer students thoughtful inquiry, robust skill development and insightful discourse. The participation certificates received by the student after successful completion of the program offers a high value and helps in their future triumph. The training is conducted usually for a span of one week.

Education Word Private Engineering Institutes Rakings

Education World is India's first education-focused magazine. Education World conducted a survey of India's Top 100 Universities, Engineering Colleges and Top 50 B-Schools. Education world also has complied national and state rankings.

Education World India Private Engineering Institutes Rankings 2017-18 survey was conducted by the Delhi-based Centre for Forecasting and Research Pvt. Ltd (C fore), one of the country's premier market research and public opinion polling companies.

To compile the national and state rankings, C fore field researchers interviewed faculty and final year students of engineering institutes countrywide and industry representatives, asking them to rate engineering institutes on following nine parameters

- 1. Leadership / Governance Quality.
- 2. Faculty Welfare and Development.
- 3. Competency of Faculty.
- 4. Curriculum and pedagogy.
- 5. Industry Interface.
- 6. Research and Innovation.
- 7. Infrastructure and Facilities.
- 8. Placements.
- 9. Value for Money.

Further, the interviewees were also asked to assign weightage to each parameter. The scores awarded by respondents under each parameter were totalled for the purpose of final ratings and ranking.

V.E. S. Institute of Technology (VESIT) has earned the **FIRST RANK in Maharashtra State** and among the Top 100 Engineering Colleges in India, V.E. S. Institute of Technology (VESIT) has secured the 26th Rank.

On Behalf of VESIT, Nagananda. A Training and Placement officer received the award on 10th June 2017.



24. PRESS RELEASES







25. PROHIBITION OF RAGGING

Ragging within or outside the institute is strictly prohibited. Any student who directly or indirectly participates in, abets or propagates ragging within or outside the institute shall face strict disciplinary action as per the provision of Maharashtra Prohibition of RAGGING ACT, 1999.

If a case is to be made as per law the following procedure is to be followed:-

- > Investigation by squad
- Findings reported to the Principal
- ➤ Discussions with committee by Principal to decide whether: To go for FIR (within 24 hrs) with Police or Local Authorities- OR Whether punishment to be awarded by Anti-Ragging committee of VESIT

Punishments may be in the form of:

- Suspension from attending classes and academic privileges.
- ➤ Withholding/ withdrawing scholarship/ fellowship and other benefits.
- ➤ Debarring from appearing in any test/ examination or other evaluation process.
- > Withholding results.
- ➤ Debarring from representing the institution in any regional, national or international meet, tournament, youth festival, etc.
- > Suspension/ expulsion from the hostel.
- > Cancellation of admission.
- Rustication from the institution for period ranging from one to four semesters.
- > Expulsion from the institution and consequent debarring from admission to any other institution for a specified period. Where the persons committing or abetting the act of ragging are not identified, the college will resort to collective punishment.

ADVICE TO ALL STUDENTS:

All students are advised to be aware of anti-ragging measures and report any ragging incident to college authorities in person or by telephone at the earliest.

More information is available on our institute's website: http://vesit.ves.ac.inor http://vesit.ves.ac.inor

Anti-Ragging/Disciplinary Committee

Sr. No.	Name	Designation	Position	
1	Mr. Manoj Sabnis	Faculty, INFT	Chairperson	
2	Mrs. V. Khadilkar	Faculty, H & Sci.	Member	
3	Mrs. M. Vijayalakshmi	Faculty, MCA	Member	
4	Dr. R. K. Kulkarni	Faculty, EXTC	Member	
5	Mr. A. Nagananda	Faculty, EXTC	Member	
6	Mr. Vivek Umrikar	Faculty, H & Sci.	Member	
7	Mr. P. Birajdar	Faculty, ETRX	Member	

Students should submit an affidavit as per #ANNEXURE I and #ANNEXURE II at the time of admission. (Refer page nos. 59 and 60)

26. WOMEN DEVELOPEMENT CELL

	Constitution – as per Guidelines from Mumbai University			
Sr. No.	Name Designation I		Position	
1	Mrs. M. Vijayalakshmi	Faculty, MCA	Member	
2	Mrs. Shobha Krishnan	Faculty, EXTC	Member	
3	Mrs. V.S. Khadilkar	Faculty, H & Sci.	Member	
4	Dr.(Mrs.) Saylee Gharge	Faculty, EXTC	Member	
5	Mrs. Naveeta Kant	Faculty, ETRX	Member	
6	Mrs. Nishi Tiku	Faculty, MCA	Member	
7	Dr. Nupur Giri	Faculty, CMPN	Member	
8	Mrs. Sonali Rahate	Superintendent	Member	

27. ANTI SEXUAL HARASSMENT COMMITTEE

Sr. No.	Name	Designation	Position
1	Mrs. Shobha Krishnan	Faculty, EXTC	Chairperson
2	Mrs. Jayshree Pandit	Co-ordinator/ Counselor, Sri Mukta Sanghtana, R. C. Marg, Chembur - 71	Member (NGO)
3	Dr. (Mrs.) Nupur Giri	Faculty, CMPN	Member
4	Mrs. V. S. Khadilkar	Faculty, H&Sci.	Member
5	Mrs. Nishi Tikku	Faculty, MCA	Member
6	Mrs. Sonali Rahate	Admin Staff, Exam	Member
7	Student Representative from cu	Member	

The Government of India has recently promulgated The Sexual Harassment of Women at Workplace (Prevention, Prohibition & Redressal) Act, 2013 (Act) and the Sexual Harassment of Women at Workplace (Prevention, Prohibition & Redressal) Rules, with effect from 9th December, 2013. The act was laid down to ensure that women in particular are protected against sexual harassment at all workplaces, be it in public or private. It has now become mandatory to have an anti-sexual harassment policy in place as well as for an Internal Complaint Committee to be formed in addition to reports being filed with the government for the same.

28. INTERNAL COMPLAINT COMMITTEE

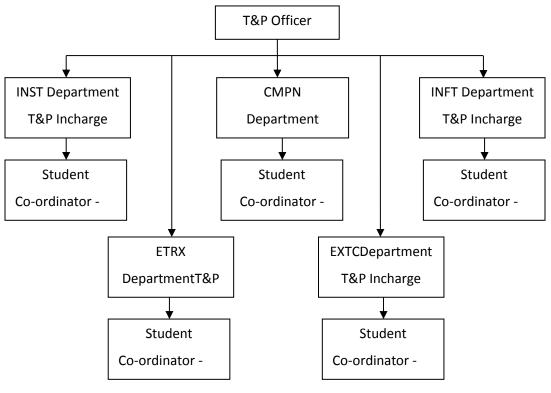
It is compulsory for all institutes/colleges under AICTE / UGC to Constitute "Internal Complaints Committee (regulation has come into force since 1st July 2016)

The ICC committee of VESIT was constituted on $23^{\rm rd}$ Feb 2017 having members (nominated /elected).

Sr. No.	ICC Posts	Criteria	Name of the Member		
1	Non - Teaching	2- male/female	Mrs. Sonali Rahate		
	Employee		Mr. Vikas Page		
2	Faculty	2- male/female	Prof. Sujata Khedkar		
	Member		Prof. Nandini Ammanagi		
3	External Member	1-familiar with	Mrs. Jaishree Pandit		
	(NGO)	women cause			
		2. LIC/DC/D	Kautubh Dhake (UG- SORT Scrtry)		
4	Students	3- UG/PG/Research scholar (elected)	Mrs. Abha Tiwari (PG)		
			Mrs. Asma Praveen (PhD)		

29. CAREER GUIDANCE, TRAINING, PLACEMENT & ENTREPRENEURSHIP CELL

Academic Year 2016-2017 Placement Statistics as on 3 rd June 2017							
Strength	844	153	213	79	205	77	117
Eligible for placements	696	121	193	64	161	64	93
Placed	521	80	161	31	115	52	82
Second Job	54	03	15	01	15	02	18
% Job Offered (Eligible)	82.61	68.59	91.19	50	80.74	84.37	107.53
% Placed (Eligible)	74.85	66.11	83.41	48.43	71.42	81.25	88.17
No. Of Companies Visited	58						
Minimum Salary (Lakhs / Annum)	1.44						
Maximum Salary (Lakhs / Annum)	16.00						
Average Salary (Lakhs / Annum)	5.3						
We have Two Job Policy. Figure in bracket () indicate second Job							



Training and Placement Cell Activities

Year	Activities
I YEAR	Inputs during Induction
II YEAR	Eligibility, Employability, impact of CGPI Contents of Resume and Building your Resume
III YEAR	Mock tests and Placement Readiness Workshops, Interaction with Final year students
IV YEAR	Interaction with alumni / companies. Online mock aptitude test

30. VESIT ALUMINI ASSOCIATION

Alumni Meet of this year was held on January 7, 2017. About 150 ex-students participated in this event. They were indeed very impressed with the change that has taken place in VESIT. They were also recollecting and reminiscing about the wonderful time they had spent here. The Meeting served as platform for current student council members to interact with their seniors and share experiences.







31. PARTICIPATION IN INTER-COLLEGIATE FESTIVALS

Other than in-house activities, the Institute also ensures that students are allowed to participate in various inter-collegiate events/cultural festivals if it helps the individual student to develop his/her talent without affecting his/her regular studies.

32. DISCIPLINE

Discipline plays a vital role in the institute. All the students admitted in the first year (F.E.) of the institute will have to give an undertaking that they would observe general discipline and decent behavior throughout the four years of their engineering course.

32.1 IDENTITY CARDS

Every student who has taken admission and duly confirmed, must get the I-Card issued from the Principal's office. He/she must wear this I-Card at all times. A student cannot enter the premises without his/her I-Card. The I-Card must be presented by the student as and when asked for, by the college authorities.

At the beginning of a new academic year, each student must get a new I-Card issued by the college after surrendering the old I-Card. This new I- Card will not be issued unless the old I-Card is submitted.

32.2 LOSS OF I-CARD AND ISSUE OF DUPLICATE I-CARD

In case the I-Card is lost, students can obtain special permission from the Principal which is valid for only three days to enter the Institute premises without the I-Card. Thereafter, a duplicate I-Card is issued on payment of a prescribed fee. Any student using an I-Card (Original or Duplicate) without the signature of the Principal or without the Institute Rubber Stamp will be penalized.

32.3 EX-STUDENT'S I-CARD

Ex-students can obtain Ex-student's I-Card, if they desire to take advantage of the Library facility. Entry of Ex-students is permitted only during a specified time on working days.

32.4 LIBRARY AND OFFICE TIMINGS

The Library will remain open on all working days, during the term, from 8.30 a.m. to 7.00 p.m. The students can also use the Library facilities for extended hours and on holidays during preparatory leave and on examination days. The office timings for students are from 11.00a.m. to 5.30 p.m., excluding lecture/Lab/Tutorial timings.

32.5 USE OF SPORTS GROUND

Students desiring to use the sports ground have to ensure that they adhere to the Rules and Regulations strictly. The students are not allowed to play while the lectures/practical of respective classes/batches are in progress.

32.6 HOLIDAYS AND VACATIONS

The list of the holidays will be displayed on the notice board. The notice regarding "Vacation "will be displayed as per the intimation from the University. Please be noted that the vacation period is liable to change from year to year and students should be prepared for such exigencies.

32.7 RAILWAY CONCESSION

Students aged up to 25 years and less can avail for concession for railway journeys between the stations near to the place of their residence in Mumbai and the College and for journeys between their place of permanent residence (where the students or his parents normally reside as declared in their application form for admission) and Mumbai during vacations. Concession forms for Local Railway tickets are issued from the Office of the Institute on working days.

The long journey concession form will be issued only for the vacation period. The students should write their local and permanent home /town address in the application form, for availing this concession. Concessions are being made within three days from the receipt of their application.

32.8 HOSTEL

Hostel accommodation is available for boys and girls separately. It will be provided on first-comefirst -served basis. Preference will be given to outstation candidates. Before leaving for vacation or holidays, those students residing in the hostel are required to take a permission letter from the class teacher/class counsellor and submit a copy of the same to the Warden.

32.9 SAFETY PRECAUTION

Safety instructions issued from time to time by teaching faculty should be followed strictly by the students while doing their practicals. In case of a mishap, the entire responsibility shall lie on the student concerned and the Institute will no way be held responsible.

32.10 LOST AND FOUND

The Institute authorities will not be responsible for any damage to the property or possessions of the students. However, articles lost and found are deposited in the office and are returned to the concerned student after identification.

32.11 DRESS CODE

Students are supposed to wear decent dresses and footwear suiting Indian Culture. **Indecent, revealing, sleeveless, tight fitting short and gaudy dresses and T-shirts with indecent messages and pictures will not be permitted.** Students should maintain proper hair style to depict a positive personality.

32.12 CLEANLINESS OF PREMISES

Students should not scribble anything on the walls, furniture etc. Strict action will be taken against the erring students and if not detected, a common fine will be levied to the entire class. Breakage of furniture will be treated in the same manner. All the students should strive for cleanliness of the premises and help in the better maintenance and upkeep of the institutional property.

32.13 INDULGING IN ANTI-SOCIAL ACTIVITIES

If a student is found in indulging in anti-social or anti-national actions, he/she is liable to be expelled from the college without prior notice

32.14 CHEWING PAN/TOBACCO AND SMOKING

Chewing pan/tobacco or smoking in the institute premises is strictly prohibited.

32.15 USE OF ELECTRONIC ITEMS/GADGETS

Students are advised to use mobile phones only outside class rooms/laboratories. If they are found using mobile during lectures/ labs, the college authorities can confiscate such items and take strict action against the students concerned. Other electronic gadgets/equipment which is needed for Project purpose can be brought in with proper permission and documentation.

32.16 REFUND OF CAUTION MONEY

On completion of the course, an application for refund of caution money should be made (along with F.E. fee receipt) within the same financial year. Unclaimed caution money will be forfeited. Lifetime membership fee for alumni will be deducted from the caution money.

32.17 LOCKER FACILITY

Locker facility for the first year students is provided on first-come-first-served basis. The student should forward an application for using this facility for which he/she has to pay a locker fee.

32.18 RETURN OF ORIGINAL CERTIFICATES

All original certificates like Mark Sheets, Passing Certificate, Caste Certificate, Domicile Certificate etc. will be returned to the students after all the admission formalities with the University /DTE are completed. Normally, these certificates are returned to first year students in the month of March/April.

32.19 NOTICE BOARDS

Students are advised to read the notice boards for various circulars/notices displayed regarding payment of fees, commencement of examination and other matters of academic interest. Whenever dates have been given for payment of Tuition Fees/Exam Fees etc., student should pay the fees well in advance, before the last date. This will avoid penalty as well as inconvenience to them. Students should not go to the office for Railway concession or any other matter while their theory classes /practicals are in progress. Further, whenever they come to the office, they should maintain proper discipline and decorum. They shall always follow the Queue system and shall not crowd near the counters and should not block main entrance and exit points.

32.20 GRE/TOEFL/GATE/GMAT

Students will not be allowed to stay away from college for the sake of preparation of GRE/TOEFL/GATE/GMAT or other forms of competitive examinations. It may be noted that students need to plan their time well in advance so that it does not affect their academic calendar. Students so desiring to appear, will be permitted for a maximum period of 15 days, provided their academic record and attendance in the semester is satisfactory.

32.21 CO-CURRICULAR ACTIVITIES

All co-curricular and extracurricular activities of the college will be held only after the regular college timings. No activities will affect the regular working of the college. Seminars/symposia/conferences/workshops of the student's professional societies will also be held only after the regular college hours. Students desiring to participate in local and outstation technical seminars, paper presentation contests, project exhibitions and other competitions will have to take prior permission.

32.22 PICNICS AND OUTINGS

Students will not be permitted to organize/participate in private picnics/outings even on Sundays/Holidays without the written consent of the college Principal. All picnics/outings have to be authorized and accompanied by college staff. Any failure to comply with any of the rule will lead to strict action taken against them.

32.23 INTERNET USAGE

Internet facility is provided to our students at various places. Students are required to use this facility only for academic purposes and not for any other purposes like chatting. Misuse of this facility and indecent behavior on the NET is liable for punishment and strict action.

32.24 THEFT

Students found stealing other's property or institutional property will be dealt with severely. If the institutional property/facility is damaged/stolen, the entire batch of students will be penalized and the cost will be recovered as deemed fit.

32.25 SUGGESTION BOX

Students can avail the suggestion box facility provided outside the Principal's Office, in ease of any suggestions, complaints etc.

32.26 BIRTHDAY CELEBRATIONS

Birthday celebrations of fellow students are not allowed inside the class or within the premises or in the vicinity of the college.

32.27 PRINCIPAL'S SIGNATURE

Students should come to Principal's Office for signature or for attestation during the recess time or only after their theory classes/practicals are over.

The above conditions are as per the Rules and Regulations of the Institute and all students should strictly follow them. Strict action will be taken against the student, if he/she fails to observe them.

Any matter not covered by these Rules will rest at the absolute discretion of the Principal.

*** Most Important Note ***

- All Admissions and Cancellations will be conducted/done as per the DTE rules and Guidelines.
- * All available Scholarships of University, State Government, etc will be granted to the eligible candidates as per rules. (Please refer website.)

ANNEXURE 1

AFFIDAVIT BY THE STUDENT (On rupees 100 stamp paper with notary)

I,	_(full	name	of	student	with	admission/registration/enrollment
number) s/o - d/o Mr./Mrs./Ms_						

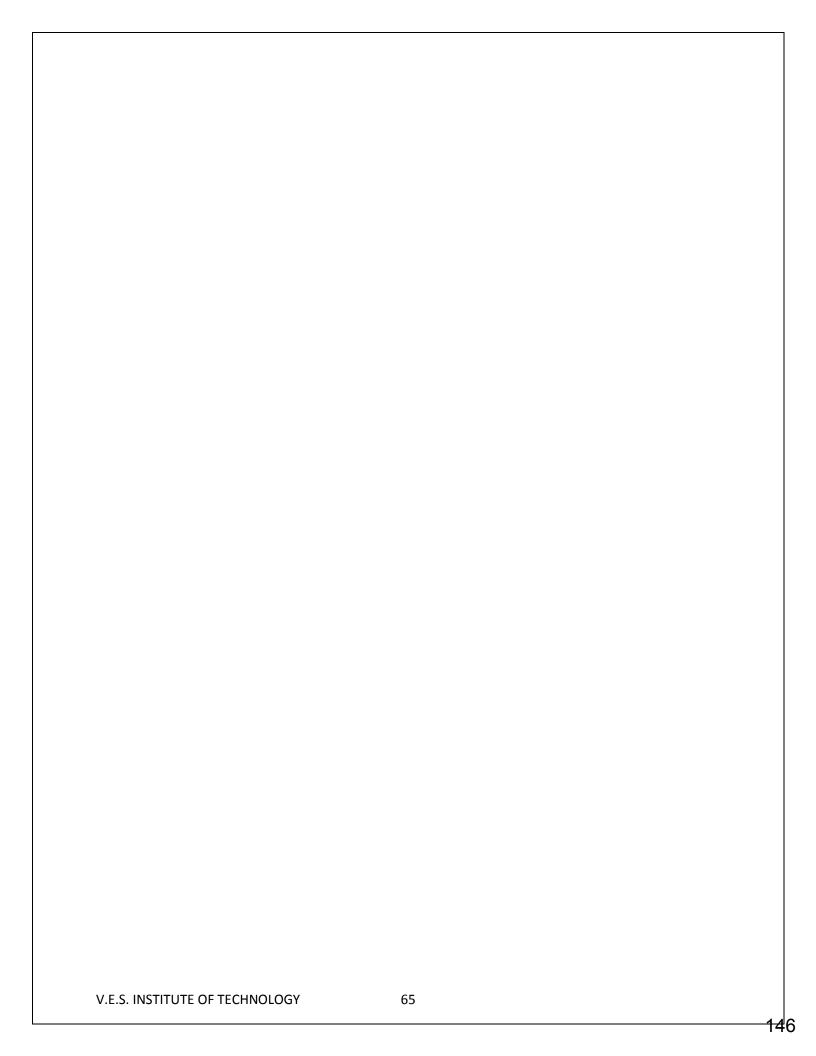
- 1. having been admitted to **V.E.S. Institute Of Technology, HAMC, Chembur, Mumbai-400074.**, have received a copy of the AICTE regulations on Curbing the menace of Ragging in Higher Educational Institutions, 2009, (hereinafter called the "Regulations") carefully read and fully understood the provisions contained in the said Regulations.
- 2. I have, in particular, perused clause 3 of the Regulations and am aware as to what constitutes ragging.
- 3. I have also, in particular, perused clause 7 and clause 9.1 of the Regulation and am fully aware of the penal and administrative action that is liable to be taken against me in case I am found guilty of or abetting ragging, actively or passively, or being part of a conspiracy to promote ragging.
- 4. I hereby solemnly aver and undertake that
- a) I will not indulge in any behaviour or act that may be constituted as ragging under clause 3 of the Regulations.
- b) I will not participate in or abet or propagate through any act of commission or omission that may be constituted as ragging under clause 3 of the Regulations.
- 5. I hereby affirm that, if found guilty of ragging, I am liable for punishment according to clause 9.1 of the Regulations, without prejudice to any other criminal action that may be taken against me under any penal law or any law for the time being in force.
- 6. I hereby declare that I have not been expelled or debarred from admission in any institution in the country on account of being found guilty of, abetting or being part of a conspiracy to promote, ragging; and further affirm that, in case the declaration is found to be untrue, I am aware that my admission is liable to be canceled.

Declared this	day of	month of	year	
Name & Signature	of Deponent			
Address & Telephor	ne/Mobile No. :			

ANNEXURE 2

AFFIDAVIT BY PARENT/GUARDIAN (On rupees 100 stamp paper with notary)

I, Mr./Mrs./Ms.	(ft	ull name of parent/gu	ardian) father / mother/guardian of
	(full name of	student with admission	on /registration/enrolment number),
1. having been admitted	d to V.E.S. Institut	e Of Technology, Ha	AMC, Chembur, Mumbai-400074.,
have received a copy	of the AICTE regi	ulations on Curbing	the menace of Ragging in Higher
Educational Institution	is, 2009, (hereinaf	eter called the "Regu	ulations") carefully read and fully
understood the provision	ons contained in the	e said Regulations.	
2. I have, in particular,	perused clause 3	of the Regulations ar	nd am aware as to what constitutes
ragging.			
3. I have also, in particu	ılar, perused clause	e 7 and clause 9.1 of	the Regulations and am fully aware
of the penal and admin	istrative action tha	at is liable to be taker	n against my ward in case he/she is
found guilty or abettin	g ragging, actively	or passively, or being	ng part of a conspiracy to promote
ragging.			
4. I hereby solemnly av	er and undertake t	hat	
a) My ward will not	indulge in any be	chavior or act that m	ay be constituted as ragging under
clause 3 of the Regulat	ions.		
b) My ward will not pa	articipate in or abet	or propagate through	any act of commission or omission
that may be constituted	as ragging under	clause 3 of the Regul	ations.
5. I hereby affirm that,	if found guilty of	ragging, my word is	liable for punishment according to
clause 9.1 of the Regu	lations, without pr	rejudice to any other	criminal action that may be taken
against my ward under	any penal law or a	any law for the time b	being in force.
6. I hereby declare that	at my word has n	ot been expelled or	debarred from admission in any
institution in the countr	y on account of bei	ing found guilty of, at	petting or being part of a conspiracy
to promote, ragging; a	nd further affirm t	hat, in case the decla	aration is found to be untrue, I am
aware that my admission	on my word is liabl	le to be canceled.	
Declared this	day of	month of	year
Name & Signature of	Deponent:		
Address & Telephone	/Mobile No:		
V.E.S. INSTITUTE OF TECH	HNOLOGY	64	





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Most Promising Engineering College with "Excellent Faculty in Maharashtra" award at the 7th Asia Education Summit and Awards 2017 by Worldwide Achievers.



Ist Rank in Maharashtra State and has secured the 26th Rank among the Top 100 Engineering Colleges in India,



VESIT placed in Platinum Category



We need education that forges our life, generates the ideal man, imbibes noble thoughts and develops the abilities in us.

-Swami Vivekanand.



VIVEKANAND EDUCATION SOCIETY



V.E.S. Institute of Technology

Affiliated to Mumbai University & approved by A.I.C.T.E



PROSPECTUS

VESIT SALIENT FEATURES

- **❖** Ph.D. Centre of University of Mumbai (Electronics Engg.)
- **❖** Library having more than 47000 books
- **Campus Connect with Automation Industries Association**
- Professional Societies of Students
- **LabView Academy with National Instruments**
- **❖** Big Data Analytics Lab with Cloudera, USA

- **State of Art R & D Centre**
- ***** Active Placement Cell
- **E** Yantra Lab with IIT-B
- Innovation Lab with Accenture
- **❖ D-Link Academy**
- Strong Alumni Association
- ➤ All Eligible UG courses received NBA Accreditation Twice in 10 Years
- Qualified Faculty Strength which includes Ph.D. Holders: 23 & pursuing: 27
- Results: 100% in MCA and more than 95 % in BE Final Exams for the last 8 years
- ➤ Placements : More than 75% Placements for the last 8 years
- EducationWorld Private Engineering Institutes Rankings 2018-19: 1st in Maharashtra
 State
- ➤ AICTE-CII Industry-linked Technical Institute Survey 2016: Platinum Category
- 'Most Promising Engineering College With Excellent Faculty in Maharashtra' award in Asia Education Summit & Awards 2017
- > Smart India Hackathon 2018 : First, Second & Inspiration Award



VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY

Affiliated to Mumbai University & Approved by A.I.C.T.E



Website: http://vesit.ves.ac.in or http://ves.ac.in/vesit

INDEX

1.	ABOUT VIVEKANAND EDUCATION SOCIETY (V.E.S)	1
2.	GOVERNING BODY	3
3.	VISION AND MISSION	3
4.	COURSES OFFERED	4
5.	FROM PRINCIPAL'S DESK	5
6.	FROM VICE-PRINCIPAL'S DESK	10
7.	SCHEME OF STUDY	11
	7.1.1 BACHELOR OF ENGINEERING (B.E) (DTE CODE EN3185)	11
	7.1.2 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME 3185)	12
	7.1.3 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME 3185)	12
	7.1.4 Ph.D. PROGRAMME	12
8.	GRADING SYSTEM OF THE UNIVERSITY OF MUMBAI	13
9.	TERM GRANT	13
10.	EXAMINATIONS	13
11.	USING UNFAIR MEANS IN THE EXAMINATIONS	14
12.	ATTENDING LECTURES/LABS	14
13.	INFRASTRUCTURE AND SALIENT FEATURES	14
14.	FEE STRUCTURE	16
15.	ABOUT CAPITATION FEE	17
16.	SPECIAL PROGRAMMES	17
17.	MAJOR LABORATORIES	18
18.	INDUSTRY COLLABORATION	25
19.	RESEARCH AND DEVELOPMENT LAB	26
20.	COMMON INFRASTRUCTURE	27
21.	TEACHING FACULTY	29
22.	STUDENTS' COMMITTEE/ACTIVITIES	29
23.	ACHIEVEMENTS	38
24.	PRESS RELEASES	43
25.	PROHIBITION OF RAGGING	44
26.	WOMEN DEVELOPEMENT CELL	45
27.	ANTI SEXUAL HARASSMENT COMMITTEE	45
28.	INTERNAL COMPLAINT COMMITTEE	46
29.	GRIEVANCE REDRESSAL COMMITTEE	50

30.	COMMITTEE FOR SCST	47
31.	ADMISSION GRIEVANCE COMMITTEE	48
32.	CAREER GUIDANCE, TRAINING, PLACEMENT & ENTREPRENEURSHIP CELL	50
33.	VESIT ALUMNI ASSOCIATION	51
34.	PARTICIPATION IN INTER-COLLEGIATE FESTIVALS	51
35.	DISCIPLINE	51
	35.1 IDENTITY CARDS	51
	35.2 LOSS OF I-CARD AND ISSUE OF DUPLICATE I-CARD	51
	35.3 EX-STUDENT'S I-CARD	51
	35.4 LIBRARY AND OFFICE TIMINGS	52
	35.5 USE OF SPORTS GROUND	52
	35.6 HOLIDAYS AND VACATIONS	52
	35.7 RAILWAY CONCESSION	52
	35.8 HOSTEL	52
	35.9 SAFETY PRECAUTIONS	
	35.10 LOST AND FOUND	52
	35.11 DRESS CODE	53
	35.12 CLEANLINESS OF PREMISES	53
	35.13 INDULGING IN ANTI-SOCIAL ACTIVITIES	53
	35.14 CHEWING PAN/TOBACCO AND SMOKING	53
	35.15 USE OF ELECTRONIC ITEMS/GADGETS	53
	35.16 REFUND OF CAUTION MONEY	53
	35.17 LOCKER FACILITY	53
	35.18 RETURN OF ORIGINAL CERTIFICATES	53
	35.19 NOTICE BOARDS	54
	35.20 GRE/TOEFL/GATE/GMAT	54
	35.21 CO-CURRICULAR ACTIVITIES	54
	35.22 PICNICS AND OUTINGS	54
	35.23 INTERNET USAGE	54
	35.24 THEFT	54
	35.25 SUGGESTION BOX	54
	35.26 BIRTHDAY CELEBRATIONS	55
	35.27 PRINCIPAL'S SIGNATURE	55
ANI	NEXURE 1	56
ΔNI	NEXURE 2	56

1. ABOUT VIVEKANAND EDUCATION SOCIETY



Founder of Vivekanand Education Society



Shri Hashu Advani along with his 10 friends started Vivekanand Education Society (VES) as a small school, in a barrack, which has grown like a banyan tree, with a spread of 26 institutions.

Shri Hashu Advani was the Municipal Corporator, MLA Urban Development Minister and Finance Minister in the Government of Maharashtra.

Philosophy of Shri. Hashu Advani ji & Values of VES

Samarpan Total dedication and devoted to serving Humanity Samdrishti Humanitarian feeling, behavior with equality for all

Vishalta Visionary in pursuit of excellence for a value rich education

Karm Yogi A doer, Actions speaking louder than words

Siddhant Valuing humanity and the importance of education and learning, in building a better

future

Vision

- Increase the intellectual wealth of the country by providing quality education and building excellent technical skills
- Make individuals highly confident, self reliant and truly global citizens
- Provide for holistic development, in terms of Knowledge, Skill and Wisdom, leading to cultural and spiritual integration.

VES Mission – Value Based Education

Develop new generation leaders to bring a positive change in the world using modern education system emanating from India's rich cultural heritage blended with western science!

Educational Facilities Offered

- · Professional, Technical & Higher Education
- · Junior & Senior Colleges
- Certificate Level Technical & Vocational Education
- · High School Level Education

- · Primary & Preprimary Level Education
- · Welfare Activities
- · Sports & Fine Arts





Distinctive Essentials

- · Admired for Ethics, Values, Discipline and Excellence in education
- Management board of honorary professionals in different fields. Honorary Associates and Alumni assisting in various activities of the trust.
- 26 Institution from crèche to schools to colleges upto Ph.D. level
- · More than 18,000 students studying every year and over 1200 teaching & non-teaching Staff.
- · No capitation or donation from students or parents
- There are no Management quota seats in all professional Institutions of VES.
- · All admissions are purely on merit
- 12 buildings in 3 complex's with over 395000 sq. ft of area
- · 3 Large Sports grounds and, Hostel & Staff quarters.
- Financial help to poor & needy students.

Institutions of VES

Professional, Technical & Higher Education Institutions

- V.E.S. Institute of Management Studies and Research
- V.E.S. Institute of Technology
- V.E.S. College of Pharmacy
- V.E.S. Law College
- V.E.S. College of Science, Arts & Commerce
- V.E.S. Leadership Academy & Research Centre
- V.E.S. Polytechnic

High Schools & Junior Colleges

Swami Vivekanand Jr. College, English Medium

Swami Vivekanand Kanishta Mahavidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, English Medium, Chembur

Swami Vivekanand Vidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, Marathi Medium, Kurla

Certificate Level Technical & Vocational Institutions

V.E.S. Tulsi Technical Institute,

Sindhi Society, Chembur

V.E.S. Catering Craft Centre

V.E.S. Nari Shala

Sports & Fine Arts Institutions

V.E.S. Vivekanand Sports Academy

V.E.S. Vivekini

Other Institutions

V.E.S. Creche & Day Care Centre

V.E.S. Vocational Guidance Bureau

V.E.S. Welfare Section

V.E.S. Vivek Sindhi Kendra

Primary & Preprimary Schools

Swami Vivekanand Preprimary & Primary School English Medium, Sindhi Society, Chembur

Swami Vivekanand DBC Pre-Primary & Primary School English Medium, Collectors Colony, Chembur

Swami Vivekanand Prathmik & Purva Prathmik Shala Hindi Medium, Kurla

Swami Vivekanand Pre-Primary & Primary School English Medium, Kurla (E)

Swami Vivekanand Prathmik & Purva Prathamic Shala, Marathi Medium, Kurla

Swami Vivekanand Nursery & Play School, Chembur

2. GOVERNING BODY

1	Classia Mada and Trainness:	(D
1.	Shri. Mahesh Tejwani	(President)
2.	Shri. Amar T. Asrani	(Secretary)
3.	Shri. Suresh Malkani	(Treasurer)
4.	Shri. B. L. Boolani	(Member Trustee)
5.	Smt. P.U. Vasvani	(Member Trustee)
6.	Shri. R.K. Bhagia	(Member Trustee)
7.	Adv. (Shri) Laxman Kanal	(Member Trustee)
8.	Dr. (Shri) Prakash Lulla	(Member Trustee)
9.	Shri. Bansi Wadhwa	(Member Trustee)
10.	Shri. Vishu Lala	(Member Trustee)

<u>VES MANAGING COMMITTEE MEMBERS</u> <u>VESIT GOVERNING BODY MEMBERS</u>

1.	Shri. Mahesh Tejwani	Chairman
2.	Shri. Amar T. Asrani	Member
3.	Shri. Suresh Malkani	Member
4.	Shri. B. L. Boolani	Member
5.	Shri Sunil Khanna	Member
6.	Shri. R.N. Mukhija	Member
7.	Dr. R. Unnikrishanan	Member
8.	Shri. K.D. Ahuja	Spl. Invitee
9.	Dr. M.D. Desai	Member
10.	Dr. N.R. Gilke	Member
11.	Shri. P. K. Krishnamurthy	Member
12	Dr. S.K. Mahajan	Member
13	Dr. (Mrs.) J.M. Nair	Member
14	Mrs. Vijaylaksmi M.	Member
15	Dr. P. P. Vaidya	Member
16	Mrs. Gresha Bhatia	Member

VESIT VISION AND MISSION

Vision

"To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of socially conscious application of technology".

Mission

- To inculcate culture of value based education.
- To enthuse students to develop in an ambient environment of caring and of sharing information.
- To enable students to work towards excellence in their chosen fields with professional bent of mind.

4. COURSES OFFERED

National Board of Accreditation (NBA) has approved the accreditation of Under-Graduate Programmes. Department of Computer Engineering and Electronics Engineering are accredited for a period of 2 years and Department of Electronics and Telecommunication Engineering, Instrumentation Engineering and Information Technology are accredited for a period of 3 years, after the visit of Expert Committee w.e.f. 01/07/2016.

Starting Year	Course /Programme /Intake
1984 - 1985	BE Electronics Engineering – 60
	BE Computer Engineering – 60
	BE Instrumentation Engineering – 60
1992 - 1993	BE Electronics and Telecommunication Engineering – 60
1999 - 2000	BE Information Technology – 60
2002 - 2003	ME Electronics and Telecommunication Engineering -18
2002 - 2003	ME Information Technology – 18
2003 - 2004	ME Instrumentation and Control – 18
2003 - 2004	Master of Computer Applications – 60
	Increase in intake of the existing programmes
2011 - 2012	BE Computer Engineering – 60
2012 - 2013	BE Electronics and Telecommunication Engineering – 60
	BE Electronics Engineering – 60
	BE Computer Engineering – 60
	Master of Computer Applications – 60
	Recognized as Ph.D. Centre for Electronics
	Direct Second Year BE
2013-2014	$Electronics\ and\ Telecommunication\ Engineering-60 (Regular\ Second\ Shift)$

5. FROM PRINCIPAL'S DESK

Reinforcement Learning is a new approach of Artificial Intelligence where computers figure out by experimenting on how to do things that no programmer could teach them. As per MIT Technology Review Report, this technology can get a robot to grasp objects, improve performance of self-driven cars and can figure out the optimal configuration of equipment in a data center. The basic purpose of Reinforcement Learning is to replicate the simple principles of nature. This technique can also be combined with Deep Learning, a technique used to recognize patterns

in data.



To make our students aware of such latest trends in technology, VESIT has been conducting "Skill Enhancement Lectures" where Industry Experts are invited to conduct sessions for our students. Our institute has a team of dedicated faculty members, who are always ready to go the extra mile in order to provide all assistance and mentorship required to ensure a productive career for our students. The focused 'training for interview' preparations conducted by Major Sandeep Tiwari for Staff Service Board (SSB) of Defence Recruitment for the interested students, which was duly coordinated by Training and Placement Cell of the Institute, is one such latest initiative.

All the concerted extra efforts, put in by the Institute finally got recognition, when our Institute was ranked *FIRST in Maharashtra State*, *2018-2019*. This survey was conducted by Education World, India's first education-focused magazine along with Delhi-based Centre for Forecasting and Research Pvt. Ltd (C-fore), one of the country's premier market research and public opinion polling companies. To compile the national and state rankings, C-fore field researchers had interviewed faculty and final year students of engineering institutes, countrywide, as well as industry representatives, asking them to rate engineering institutes on following parameters: Leadership, Governance Quality, Faculty Welfare and Development, Competency of Faculty, Curriculum and Pedagogy, Industry Interface, Research and Innovation, Infrastructure and Facilities, Placements and Value for Money.

We are proud to inform you that VESIT has proved itself in a number of areas this year, showcasing the competency and capability of its staff and students. Our students won first, second, prizes and an inspiration award at the *Smart India Hackathon2018* (a national level initiative by the Ministry of HRD), which was held on the 30th and 31st March 2018.

Pravas Sarthi– A Convenience, kiosk based system for people to communicate in their own local language

at the airport was designed by VESIT students under the mentorship of Dr. Gresha Bhatia. It was selected from 16 regional finalists out of 350 teams all across India and went on to win the 'Close to Market Category' award in E-Yantra Ideas competition held at the Indian Institute of Technology, Mumbai (IIT-B) on 7-8 April 2017.

Four final year students Ashni Damaria, Laveena Dulani, Preksha Goyal and Rahul Goswami have gone beyond textbooks to develop a *prototype of the remote patient health monitoring system* that will help doctors monitor rural patients' vital signs to facilitate better diagnosis. What started out as a final year project may now turn into something that could boost rural healthcare in rural region of our country.

Ramanathan Choodamani, Shivesh Hiranandani, Shubham Koyande, Ketan Vaidya, guided by Mrs. Ashwini Sawant, successfully presented a project titled 'Road Divider Based Power Generation and Street Light Illumination' which won the 2nd position in University Final Round of *Avishkar 2016-17* - A Research Convention conducted by Mumbai University. The proposed model demonstrates the generation of electricity with the help of wind energy from the vehicular movement on the highways. A project on Hybrid & Economical System Design for early Detection of Abnormality and Prediction of Malignancy by Ph.D. student, Shilpa Joshi, was selected to represent Mumbai University at state level for *Avishkar 2018*. An *Inter-college Elocution Competition* was organized by MLDC ALUMNI ASSOCIATION on 27th January, 2018 at M.L. Dahanukar College of Commerce, Mumbai where seven students from VESIT participated in the event. Two of them; Rakshit Ganvir (D9) and Poorvi Parmar (D14) won the First prize and the Third prize respectively. At *SAMVAAD'18*, the inter-college Debate Competition held at Fr. C. Rodrigues Institute of Technology on 3rd March, 2018, our students Rakshit Ganvir (D9A) and Harish Muthalagan (D9A) won the second position. Rakshit Ganvir was also declared as the '*Most Dynamic Speaker*' for the event.

VESIT Dance Crew, won the first position at two Inter-collegiate *Dance Competition*; one held at K. J. Somaiya Medical College, Sion, on 1st March 2018 and the other at HBT Medical College, Juhu held on 4th March 2018. Our Drama Team, too, have been victorious by winning the *first positions* both, at Spandan and at Verve, the Inter-College Drama Competitions held by *St. Xavier Institute of Engineering* and by *Shah & Anchor Kutchhi Engineering College* on 4th February and 9th Mach 2018 respectively.

The **results** of MCA for the academic year 2016-17 was 100%, followed by Computer with 99.53%, Information Technology with 98.70%, Electronics & Telecommunication with 97.54%, Electronics with 95.42 and Instrumentation with 88.61%.

The placement scenario has shown improvement with 73.12%, as compared to the last year's 72.27%. Thanks to the special efforts of our efficient Training and Placement team, this year we saw eight *new companies visiting our campus*, out of which, four companies have 'dream' status. Accenture continued to remain the top recruiter this year too, as it crossed the 300 mark, having offered placement for 362 students across all branches. It is followed by L&T Infotech, which offered placement for 68 students. The

highest salary offered was 16 lakhs by Morgan Stanley, which placed one of our students.

The Techfest "*Praxis 2017*" was organized on 22nd and 23rd September 2017. Since its inception 15 years ago, the festival has been receiving an overwhelming response from students. This year has seen the unveiling of innovative gadgets and gizmos, conceived and designed by students, where they transferred their theoretical knowledge into practical application. Ankit Panvalkar and his team designed a self-steering car which could be guided by a GPS tracking system. The vehicle has also been equipped with an IR system for obstacle detection and works on a virtual interface.

Tinkerer's Lab conducted the Embedded Systems & IoT Workshop during 15th - 21st September 2017, which saw students from ETRX, EXTC and CMPN department participate in this five- day workshop. Tinkerers Lab EXTC, VESIT, organized their first event of this semester with 'Activity on Embedded systems with Arduino'. It was a two-day activity, held on 26th & 28th September 2017, where it covered the basic concepts of Arduino.

A *Project Skill Development Workshop* was organized from 10th to 15th July 2017. The aim of the workshop was to enhance the project based skill of third year students. Thirty-three students enthusiastically participated in this workshop. The students were introduced to a new circuit simulation software "SEQUEL" and were given hands on experience by simulation.

The Industry Interaction Program "*Edifice 2018*" organized by Vertivco was conducted on 17th January. Mr. Sunil Khanna, President, Vertivco, Mr. Nitin Tahib, Business Consultant- IT, Sales and Business Development, Mr. Ninad Karpe, Chairman- Western Region, CII, Mr. C.K.Asher, Director at Ex-Buzz Fire & Security Systems Pvt. Ltd., Mr. Navneet Mohan, Head- Technical Solutions at Aveva Software, Mr. Lalit Bangera, BDM- Life Science APAC at Eurotherm by Schneider, Mr. Santosh Kulkarni, Country manager at Vertivco Mr. Prashant Mohite, Senior Manager Design at Tata Steel and Mr. Upendra Rai, Director at Milionminds were the speakers. The program, helped the students to know about the latest technologies.

Electronics and Telecommunication department organized *Short Term Training Program* on "WAVELETS AND MACHINE LEARNING" from 3 - 8 July 2017. Dr. Manesh Kokare (IIT, Kharagpur), S.G.G.S Institute of Technology, Nanded, was the guest lecturer for all 6 days. Dr. Kokare developed three new algorithms in 'Diabetic Retinopathy' during his Post-Doctoral research from University of California. The *D-Link Specialist Level program* was conducted from 20 to 24 March 2017 by Mr. Nilesh Vartak from the D-Link Academy at Department of Electronics and Telecommunication, V.E.S. Institute of Technology. The program was for students who needed detailed study of wireless networks and networking devices. The program mainly involved "Study of Wireless networks".

'Awakening the Scientist' is an opportunity given to the First year students to take the first step in their transformation from a student to a professional. In this event, the First year students formed groups after which each group had to identify the basic engineering and technology used in any equipment, gadget or

tool used in day to day life. The group had to relate to the past, present and future of the technology used in the gadget/ tool or equipment. This year eight groups with total number of 33 students registered for this event.

To keep teachers updated on the latest technologies, *Faculty Development Programs* are regularly conducted in our Institute. A Two-day hands-on training on *CADENCE* software was organized on 7 and 8 September 2017. "Getting started *SEQUEL SIMULATOR*" was organized by Mrs. Dipti Karani, which is useful for teaching Engineering courses like BEEE, ECD, Digital Electronics, and Analog Electronics. We are proud to note that Instrumentation Department of VESIT conducted a *Graduate Engineer Training Program* for *Larsen and Toubro* from 20 July to 9 August 2017 in our State of Art Process Instrumentation Lab. A One-day workshop on "*Funding Channels for Academic Entrepreneurs, Business Aspects for Technopreneurs and Success Stories*" was organized by BIRAC Regional Innovation Centre (BRIC) in collaboration with IKP Hyderabad and Vivekanand Education Society on 2nd December 2017.

The Spoken Tutorial project, an initiative of the 'Talk to a Teacher' activity by National Mission on Education through Information and Communication Technology (ICT), was launched by the Ministry of Human Resources and Development, Government of India and conducted by IIT Bombay. The students of our college have actively participated in it. "Born to Blossom" seminar was organized by the prestigious Swami Vivekananda Kendra, at Vivekanand Education Society's Institute of Technology on the 29th of July, Saturday for the second and third year MCA students and the faculty.

Our *Social Responsibility Team* (SoRT) has also been very active this year. They collaborated with Six NGO's namely, National Association for Equal Opportunities for Handicapped (NASEOH, CHEMBUR), Are you Recycling? RUR(Matunga), Aseema(Bandra), Snehalaya and We Change(Thane) during UMEED'17 – the SoRT Week from 11 – 18 September 2017. SoRT also organized a Blood Donation Camp along with Thalassemia test and eye checkup in our college on 22 July2017, where the Council collaborated with a dedicated team of doctors and nurses from J.J. Hospital, Mumbai. In association with Marrow Donor Registry (India), SoRT also held a Bone Marrow registration camp where 120 registrations were made by students and staff, who actively donated sample of cheek epithelial cells. During the festival of Ganeshotsav, the SoRT team assisted Chembur Welfare Brigade and Mumbai Police for Traffic Monitoring.

To reinforce the principle of Zero Tolerance for Sexual Harassment of students and Women in colleges and Universities, VESIT conducted a session on 27 February 2017 to sensitize students about the topic of Sexual Harassment in universities and colleges. VESIT has also constituted an Internal Complaint Committee for Prevention, Prohibition and Redressal on the same issue.

On the occasion of Pandit Deendayal Upadhyay Century Celebration and 125 year of Swami Vivekanand's Chicago Address, the live telecast of "*Young India, New India – A Resurgent Nation: from Sankalp to Siddhi*" speech by our Hon'ble Prime Minister Shri Narendra Modi was screened to our First Year students

on 11 September 2017.

"No voters to be left behind" a special drive for registration of eligible electors by the Election Commission of India was launched, where newly eligible voters are encouraged to fill Form 6 (new voter registration form). VESIT contributed to this campaign by arranging a desk at the VESIT Main Entrance during 13th to 31st July 2017 for VESIT Students and Staff.

The VESLit Circle of VESIT in association with *The Times of India* conducted an *Intra-Collegiate Debate Competition* on the 12 September 2017. The judges for the competition were; Mr. Manoj Dwivedi, Associate General Manager and Mr. Rakesh Pandey, Senior Manager from The Times of India.

On 13th October 2017, to commemorate the birth anniversary of our former president Dr. A.P.J. Abdul Kalam, VESIT celebrated "*Vaachan Prerna Diwas*", where a session was arranged for all the teaching and non-teaching staff. Students were selected based on their interest in study beyond the curriculum which was reflected in their books usage pattern of the institute library and were awarded with certificates.

"My Vision – Corruption Free India" a Short Film Making Competition was conducted by Union Bank of India during The *Vigilance Awareness Week* to raise awareness about the problem and evils of corruption in our country. On 14 October 2017, VESIT students showcased a series of seven films which exhibited their visions of an ideal and corruption free society which was *covered by press news*.

The *Alumni Meet* was held on 16th December 2017, this year. The Distinguished Alumni award was received by Dr. Deven Shah (ME IT-2005), who has four patents to his credit and has also authored and co-authored a number of books. Alumni of 1992 batch (which completed 25 years) were also felicitated, on this day.

VESIT was granted the *permanent affiliation for the post graduate* courses in engineering this year, now all UG/PG courses are now permanently affiliated with Mumbai University. VESIT has applied UGC 2(f) &12(b) status and for Academic Autonomy this year, which will provide us flexibility to modify existing academic structure, implement innovative ideas and policies which in turn, will help to enhance the academic excellence of our students. This transition may require a reexamination of traditional methods of operation and innovation to improve the processes and services of the institute. We hope to achieve this institutional improvement with systematic planning, commitment and continued support from the management and our dedicated staff members.

[&]quot;Just because something doesn't do what you planned it to do doesn't mean it's useless".

⁻Thomas Edison

6. FROM VICE-PRINCIPAL'S DESK

We live in an age of rapid transformation—one that has witnessed disruptive innovations, rising expectations and reduced shelf life of technology. Organizations have to embrace new emerging technologies such as the Internet of Things (IoT), Artificial Intelligence (AI), Robotics, Cloud, Blockchain, Augmented Reality (AR) And Virtual Reality (VR).

Alongside large firms, start-ups too have mushroomed and contributed significantly towards



the growth and adoption of emerging technologies in India. We are attempting to turn from Job Seekers to Job Creators.

Further initiatives of the government, aimed at economic galvanization and inclusion, such as Make in India, Digital India and Skill India, have begun to look at and leverage the above emerging technologies to create efficiencies and increase reach across to provide societal benefits across all sections of society.

We at VESIT can proudly state that our team of faculty and students are at the forefront of such endeavors. Value added course like *AIA*, *Dlink* and *Cloudera* help add skill sets to the students to improve employability. Further *Summer and Winter schools* in current technology areas keeps the students abreast with significant trends in technology.

A plethora of *guest lectures* from industries further helps industry and our students in gaining useful employability skills.

We are proud to state that our flagship event *Praxis* was recreated with a new Avatar giving students ample opportunities to showcase their skills.

All in all we at VESIT are a harbinger of Digital Transformation making the magazine *Vishwa karma* an E-Magazine is another step in this direction.

Wishing you all the very best. "May You live in Technology Enriching Times"

"One machine can do the work of fifty ordinary men. No machine can do the work of one extraordinary man".

-Elbert Hubbard

7. SCHEME OF STUDY

7.1. BACHELOR OF ENGINEERING (B.E) (DTE CODE EN 3185)

The Bachelor of Engineering is a four year course, which is divided into eight semesters. The first year (F.E.) course spreads over two semesters dealing with general engineering, common to all branches of engineering. The basic objective is to give elementary foundation needed in different aspects of engineering.

During the second year (S.E.) and third year (T.E.), students gain basic knowledge in their chosen branches. The final year i.e. fourth year (B.E.) is designed to strengthen the specialized skills which help the students to apply the concepts studied earlier by participating in project work. The subjects and the syllabus are as prescribed by the University of Mumbai.

Approved Intake for Academic Year 2018-19

Regular First Shift	<u>Intake</u>
B.E Electronics Engineering	120
B.E Computer Engineering	120
B.E Instrumentation Engineering	60
B.EElectronics and Telecommunication	120
B.E Information Technology	60
Regular Second Shift	<u>Intake</u>
B.E Computer Engineering	60
B.E. –Direct Second Year (Elect.and Tele.)	60

Eligibility: - As per DTE, Mumbai University and State Government Rules.

Note: -100% admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

7.2. MASTER OF ENGINEERING (M.E.) (DTE CODE: ME 3185)

The M.E. Program comprises of four semesters with duration of two years. The courses are designed/prescribed by the University of Mumbai.

The numbers of seats offered are as mentioned below:

Approved Intake for Academic Year 2018-19

Regular First Shift	<u>Intake</u>
M.E Electronics and Telecommunication.	18
M.E Instrumentation and Control	18
M.E Information Technology	18

Eligibility: - As per DTE, Mumbai University and State Government Rules.

<u>Note: -</u> 100% admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

7.3. MASTER OF COMPUTER ENGINEERING (M.C.A) (DTE CODE: MCA 3185)

Master of Computer Application (M.C.A) is a full time three year course (six semesters). The course is conducted as per the syllabus laid down by the Mumbai University. Examinations are conducted by the University for all the six semesters and the degree is also awarded by the University of Mumbai.

Approved Intake for Academic Year 2018-19 Intake

MCA - Regular First Shift	60
MCA - Regular Second Shift	60

Eligibility: - As per DTE, Mumbai University and State Government Rules.

<u>Note:</u> - 51% Minority quota admissions will be done through CAP (Centralized Admission Process) by DTE (Directorate of Technical Education).

7.4. Ph.D. PROGRAMME

VESIT has always given importance to the experimental way of teaching and learning of engineering concepts which can only be perfected through the best research and development practices. VESIT offers Ph.D. course in Electronics Engineering. VESIT has well equipped R&D Laboratory which is utilized by P.G. and Ph.D. students.

8. GRADING SYSTEM OF THE UNIVERSITY OF MUMBAI

Mumbai University introduced Choice Based System from the academic year 2016-17. All information about pattern and syllabus is available on Mumbai University's official web site www.mu.ac.in
Salient Features:

- ✓ Final grade for B.E. Degree will be based as the average of all eight semesters. So each semester/each subject is important.
- ✓ 75% attendance is compulsory.
- ✓ Journal submission must be done on or before every term end.
- ✓ Two tests will be conducted according to the given model by the University for Internal Assessment of each subject. The average of the two scores of internal assessment will be considered as the final grade in the subject and will be printed in the final mark sheet. No re-test shall be held.
- ✓ Failing in eight heads in a year is the maximum limit for allowed in the ATKT system. (Maximum five theory exams and remaining Prac./Oral/Internal etc.)
- ✓ Failure to submit the journal in any subject (even for a single subject) will result in detention (i.e. loss of one year). Admission for the next academic year is to be taken by repayment of fees.

9. TERM GRANT

Term Sanctioning (allowing the students to appear for the University examination) entirely depends upon the following: -

- Minimum of 75% attendance in all subjects.
- Satisfactory completion of all term work.
- Practical and Assignments as specified in Syllabus or as instructed within prescribed periods.
- General behavior of students, both inside and outside the classroom.

10. EXAMINATIONS

Every semester, the University of Mumbai conducts end semester examinations and two internal class tests are conducted by the college in every subject. The performance in these tests is considered for grade calculations as per the MU circular. The submission of term work in each subject should be taken very seriously. If a student fails to submit his/her Term or Sessional Work before the prescribed date, he/she will not be allowed to sit for the concerned examination.

11. USING UNFAIR MEANS IN THE EXAMINATIONS

During an examination, if a student is found either possessing any material (written or printed) with the intention of copying or actually copying, he/she is liable to be severely punished according to the rules laid out by the University. In such circumstances, a student may either be debarred from appearing for the entire examination or expelled from the University of Mumbai for a minimum period of three years as per the decision of "Unfair - Means Committee".

12. ATTENDING LECTURES/LABS

- 1. Students coming late to the Institute will not be allowed to enter the premises.
- 2. Bunking classes is strictly forbidden. If the student misses even one period, he/she may be marked absent for the whole day.
- 3. In order to monitor the academic progress as well as the overall development of our students, we nominate one staff member as faculty advisor to whom a student can approach for guidance. For day to day functioning, class teachers and class counsellors are associated with each class. The students as well as their parents are welcome to contact either the class teacher/class counsellor or the faculty advisor any time during the session.
- 4. While the lecture or the lab is in progress, a student is supposed to keep his/her mobile phone switched off, failing to which severe punishment can be given.
- 5. After the classes are over, the students should leave the college premises in a proper disciplined manner, without making any noise or crowding in the corridors.

13. INFRASTRUCTURE AND SALIENT FEATURES

The VESIT campus strikes a balance between modern facilities and natural surroundings to create an atmosphere that is ideal for students to learn, explore, and grow.

- √ Sprawling Complex
- ✓ Equipped Library having more than 47000 technical books
- √ Professional Societies
- ✓ Active Placement Cell
- √ Strong Alumni Association with Alumni Web Portal
- √ Conference Hall
- √ Seminar Halls
- √ Auditorium
- ✓ Music Room

- √ Huge Canteen
- ✓ Inviting Playground
- √ Amphi Theatre
- ✓ Recreation Room

Labs set up in collaboration with industry

- ✓ Automation Industry Association Lab
- ✓ Cloudera Big Data Analytics Lab,U.S.A
- ✓ National Instruments Labview Academy, U.S.A
- ✓ D Link Academy, National Instruments (U.S.A), Bangalore.
- ✓ e-Yantra-MHRD-IITB initiative

14. FEE STRUCTURE

V E S INSTITUTE OF TECHNOLOGY Fees Structures for Academic Year 2018 - 19 for - FIRST YEAR - BACHELOR OF ENGINEERING

	General	Quota	OB	C	SC/ST/VJ/D	T/NT/SBC	TFV	VS	J&K	
Particulars of Fees	Maharashtra Board	Other University / Boards	/ GOI	J&KPMSSS						
	0.45.45	04545	47272	47272						04545
Tuition Fees	94545	94545	47273	47273	0	0	0	0		94545
Development Fees	9455	9455	9455	9455	9455	9455	9455	9455	24000	9455
Caution Money	1000	1000	1000	1000	1000	1000	1000	1000	0	1000
Gymkhana / Other University Fees	546	546	546	546	546	546	546	546	546	546
Enrollment Fees	200	200		200					200	
Yuvaraksha Insurance	100	100	100	100	100	100	100	100	100	100
Eligibility Fees	0	300	0	300	0	300	0	300	300	300
Grand Total	105846	106146	58574	58874	11301	11601	11301	11601	25146	106146

Fees Structures for Academic Year 2018 - 19 for - FIRST YEAR - MASTER OF COMPUTER APPLICATION

Particulars of Fees	General - Maharashtra		OBC - Maharashtra		SC/ST/VJ/DT/NT/SBC Maharashtra		General	
	Mumbai University	Other University / Boards	Mumbai University	Other University / Boards	Mumbai University	Other University / Boards	Other than Maharashtra	J & K / GOI and NEUT
Tuition Fees	72727	72727	36364	36364	0	0	72727	
Developme nt Fees	7273	7273	7273	7273	7273	723	7273	24000
Caution Money	1000	1000	1000	1000	1000	1000	1000	0
Gymkhana / Other University	546	546	546	546	546	546	546	546
Fees	546	546	546	546	546	546	546	546
Yuvaraksha Insurance	100	100	100	100	100	100	100	100
Enrollment Fees	0	200	0	200	0	200	200	200
Eligibility Fees	0	200	0	200	0	200	300	300
PG								
Registration	825	825	825	825	825	825	825	825
Grand Total	82471	82871	46108	46508	9744	10144	82971	25971

Fees Structures for Academic Year 2018 - 19 for - FIRST YEAR - MASTER OF ENGINEERING

	General - N	// Jaharashtra	General	SC - Maharashtra	
Particulars of Fees	Mumbai University	Other University / Boards	Other than Maharashtra	Mumbai University	Other University / Boards
Tuition Fees	66364	66364	66364	0	0
Development Fees	6636	6636	6636	6636	6636
Caution Money	1000	1000	1000	1000	1000
Gymkhan / Other University Fees	546	546	546	546	546
Yuvaraksha Insurance	100	100	100	100	100
Enrollment Fees	0	200	200	0	200
Eligibility Fees	0	200	300	0	200
PG Registration	825	825	825	825	825
Grand Total	75471	75871	75971	9107	9507

15. ABOUT CAPITATION FEE

INSTITUTE DO NOT ACCEPT ANY TYPE OF CAPITATION.

Refer to Anti Capitation Fee Act available on our college website: http://vesit.ves.ac.in_or http://vesit.ves.ac.in_or

16. SPECIAL PROGRAMMES

Looking Beyond Syllabus

Looking beyond syllabus was launched in the year 2011, with an aim to expose the students to evolving technologies and opportunities and to work as teams on multidisciplinary projects under the mentorship of a faculty member assigned to them, in their area of interest/passion that may not necessarily be their stream of study. The main objective of looking beyond syllabus of technical education is to infuse prospective technocrats with the ability to apply basic science and experimentation skills, to understand problems in the industry, academia and humanity at large and also be able to develop practical solutions. It also aims to model them into wholesome professionals in the field of Technology. Looking beyond syllabus also prepares the students to use their skills with a strong base for higher education and acquisition of knowledge to sustain a life-long career in related areas.

Awakening the Scientist

Awakening the Scientist is an opportunity given to the First year students to take the first step in the transformation of a student into a professional. It is a group activity. The objective is to identify the science, engineering and technology concomitant with gadgets, equipment and tools used in day to day life and also to relate the past, present and future of them.

Skill Enhancement Lectures

VESIT believes in giving its students an invigorating and vitalising academic environment. The syllabus of Mumbai University is taught very efficaciously by our faculty. An added edge is given to our students by inviting connoisseurs from the industry and research institutions. This endeavor is termed as 'Skill Enhancement Lectures'.

17. MAJOR LABORATORIES

Department of Electronics Engineering

Basic Electrical Engineering Lab

Laboratory courses conducted: - Basic Electrical and Electronics Engineering, Electrical Workshop, Electronic and Electrical Measuring Instruments and Machine. Lab is equipped with 1-phase and 3-phase Motors, different types of Transformers, A.C. and D.C. Generators, Motors, Trainer kits for network theorems, analog and digital measuring instruments etc.

VLSI Design Lab

Laboratory courses conducted:- Basics of VLSI, Elements of Microelectronics, VLSI Design. The department is a member of Cadence University Program; subscribed to cadence VLSI design suite, running on SOLARIS-8, on Sun V240 servers & Sun-ray 1g thin clients. This lab has been set-up with contributions from Sun Microsystems under matching grants program, for educational institutes.







Electronic Circuit Design & Analysis Lab

Laboratory courses conducted:- Basic of Electronic Circuits, Electrical Network Analysis and Synthesis, Electronic Circuit Analysis and Design. This lab is equipped with sufficient number CRO, Function Generator, DMM etc. Softwares required for design and simulation of electronic circuits are also available.

Digital Logic Design Lab

Laboratory courses conducted:- Digital System Design I & II, Linear Integrated Circuits and Design. Lab is equipped with softwares for digital and analog circuit simulation, FPGA & CPLD training kits, Digital measuring instruments etc. This Lab also extends support to the final year students for their Project Work.

Advanced Microprocessor Lab

The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. All the electronic testing equipments are available, Digital Trainer Kits and Demonstration kits (RAM & ROM), Universal Programmer, Cards for ISA, PCI and different connectors are kept for better understanding of computer architecture.

Power Electronics Lab

Laboratory courses on Industrial and Power Electronics are conducted in this lab. The Lab is adequately equipped with Test equipment for Power Electronic Devices & circuits, Various demonstration kits & measuring instruments are available in this lab.

Electronics Workshop – PCB lab

Laboratory courses conducted: - Electronics Workshop – I & II, PCB Workshop. Lab Facilities – PCB Exposer, Film Exposer, Dip Coat Machine, Etching Tank, Drilling Machine etc. Students perform their Electronic Workshop project designing, testing and implementation in this lab.

Department of Electronics and Telecommunications Engineering

Analog & Digital Communication

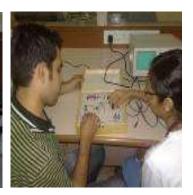
Laboratory courses conducted: - Principle of Communication Engineering, Digital Communication, Mobile Communication Systems & other analog & digital communication related subjects. Lab is equipped with DSO-(Agilent, Aplab, Tektronics, Rigon), Spectrum Analyzer-3GHz (Agilent, Hameg), Signal Generators-AM/FM, Computers with latest configuration etc. COMMSIM:- Network & Communication Simulation Software, Digital Communication Trainer kits like 16-QAM, QPSK, PSK, TDMD, Error correcting codes etc.

Microwave & Advanced Communication

Laboratory courses conducted: - RF Circuit Design, Microwave Devices and Circuits, Computer Communication Networks, Electromagnetic wave Theory etc. Lab is equipped with DSO-(Agilent, Aplab, Tektronics, Rigon), Spectrum Analyzer-3GHz (Agilent, Hameg), Standard Signal Generators for RF-range, Network Analyzer (Agilent), Transmission Line Trainer, Computers with latest configuration etc. Software like MATLAB, Ansoft,







PCADD are used simulation.

Network & Wireless Communication

Laboratory courses conducted :- Discrete Time Signal Processing, Computer Communication Networks, Image Processing, Wireless Communication, ICE etc. This LAB consist of 25-PCs with latest configuration, server, Wirless LAN, PDA's & Mobile phones for experimental purpose. All computers are installed with necessary simulation softwares.

Optical Fiber Communication

Laboratory courses conducted: Optical Fiber Communication, MOC. This Lab also extends support to the final year students for their Project Work. The lab consists of the latest equipments/ softwares like OTDR, Optiwave, Trainer kits for calculation of IV Characteristics, WDM, Chromatic dispersion analog & digital signal transmission, 1550nm & 1310nm optical sources etc.

T.V. and Satellite Communication

This lab supports conducting practical's & some project work for SE to BE students. The practical's conducted for the subjects like TV and Video Engineering, Satellite Communication, Antenna and Wave Propagation, RADAR etc. equipments like different TV models, B/W & color pattern generators, Satellite communication module, and complete setup of cable- TV & CCTV etc.

Department of Computer Engineering

All the computer laboratory are spatial and well equipped with the latest configuration and well networked Machines.

Database Management Systems

A well-equipped lab comprising of latest computers installed with various Database tools & Servers like Microsoft SQL Server 2005/08 & MS Access (under dreamsparx licence), Oracle 11g, Visual Studio 08/10.,Netbeans etc. The courses conducted are Distributed databases,Operating Systems and Systems Designing.

Web Engineering & Multimedia

A dedicated lab that caters to requirements of Web Engineering & Multimedia with latest softwares like Maya

9.0, Adobe Macromedia Studio, Macromedia Flash, Dreamweaver, Autodesk 8.0 & Fireworks. The Lab typically caters to second year and third year computer students.

Under (MODROBs grant – Ref No 12/AICTE/RIFD/MOD(Policy4)Pvt109/201213)

This specialized lab is an extension of the Web Engineering and Multimedia Lab which is high end lab and includes 3-Apple Machines, 2 Aakash Tablets,1 iPad and 1 Interactive kiosk under the AICTE MODROB Grant.

Microprocessor and Robotics







This lab is equipped with the Trainer kits of Microprocessor 8085 & Advanced 8086 Trainer Kits. Several interfacing kits that aid Robotics and i-Bot trainer kits are provided. Also houses computers with tools such as TASM & MASM. The courses conducted are Microprocessor, Microcontroller and Artificial Intelligence.

Networking

Aims to provide an environment that aids in effective learning of Networking & Mobile Computing concepts. Systems on the Linux Platform and several development toolkits for mobile computing are some of the features. It consists of Packet Tracer, NetSim Academic, Sun Java Wireless Toolkit, Visual Paradigm. The courses conducted are Computer Networks, Cryptography and System security, Networking Programming Laboratory.

Object Oriented and Programming

To aid in proper understanding of System Design and programming Concepts, this lab houses computers with several system design tools like Visual Paradigm, Rational Rose, MS access, MS SQL Server 2005 and Eclipse Ganymede Arch Studio are some of the several software estimation tools. The courses conducted are Object Oriented Programming Methodology and Web Technology.

Specialized Lab – Cloud Computing Lab

This new Lab caters to the final year course and has opensource softwares like Open stack, OwnCloud, XEN and KVM also VirtualBox. Also the resources are used for various projects.

Specialized Lab – Machine Learning & Big Data Analytics Lab

This specialized lab is for final year Elective courses Big data analytics and machine learning. It is equipped with latest high end computers. Cloudera distribution for Apache Hadoop version 4.4.1 which comprises of Hadoop, Hive ,Pig, Hbase is used along with other opensource tools like R, Mahout.

Project Lab

This lab has been specially provided for the students to perform their project work with several advanced computers with latest softwares like MATLAB, Visual Studio 2010.

Department of Instrumentation Engineering

Process Automation Lab

Process Automation Laboratory is Equipped with Latest Technology Control systems including PLC'S with simulators along with SCADA software & DELTA V Distributed Control system from well-known industry brands like Allen Bradley & Emersion Process Management. This Laboratory facilitates students to learn Basic PLC Programming, PLC Panels and simulators, DCS Features, DCS based Control systems etc.

Process Instrumentation Lab

Process Instrumentation Laboratory is a state of the Art Experimental Facility made up of 14 pilot plant set ups. This was designed & developed, which enable students to identify the instruments & Control systems as well as to provide demonstration of working instruments to the students. This laboratory is utilised to study and understand various types of Processes and Role of Instrumentation involved in the same.







It also creates an Industrial Environment for the students to grow them with the Instrumentation utilization. Students can perform various supporting Instrumentation activities Like Calibration, Tuning, mounting, Trouble shooting, Loop Testing, checking which provide them Hands on Exposure to make them comfortable and answerable for their campus preparation. Laboratory consists of various Field Instruments Like SMART Transmitters, Flow meters & Control Valves along with accessories. It is also equipped with Single Loop PID Controllers & Variable Frequency Drives.

Transducers & Sensors Lab

This is one of the Fundamental Labs which facilitates students to learn various sensors & Transducers used in Process & Factory Environment. Students can learn Testing & Performance characteristics using Calibrators. It also provides Hydraulic set up that allows studying Flow Measurement Experiments, Level Monitoring set up using various Instruments Like Rota meter, Orifice / venture meters & ultrasonic Level Transmitter. Lab also has Dead weight Testers & Master Pressure Gauge to provide Testing of Pressure Gauges.

Biomedical /Analytical /Nuclear Instrumentation Lab

This Laboratory has been established to familiarize the students with capturing, analyzing and processing different

bio signals. Biopac is an integrated instrument which the students use for such experiments. Physiology Lab kits and digital temperature feed back system are also used for training the students in the area of Biomedical Instrumentation.

Control Systems & Digital Signal Processing Lab

The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. All the electronic testing equipments are available, Digital Trainer Kits and Demonstration kits (RAM & ROM), Universal Programmer, Cards for ISA, PCI and different connectors are kept for better understanding of computer architecture.

Analog & Digital Lab

Laboratory courses on Analog 7 Digital Electronics are conducted in this lab. The Lab is adequately equipped with Test equipments for Electronic Devices & circuits, various demonstration kits & measuring instruments are available in this lab.

Department of Information Technology

Department has seven well equipped and spacious Computer Laboratories with more than 250 networked

computers and supports equipment like Servers, Printers etc.

The laboratories are installed with all the supporting software required to full fill university curriculum including Free and Open Source Software and other proprietary softwares such as MSDN Dream Spark, SPSS, Microsoft Windows Compute Cluster Server 2003, Microsoft Windows Compute Cluster ED, Adobe CS3 Design Premium, IBM Rational Rose, Qualnet Software, IBM Cognos BI, Matlab, MS-Office Professional.



These lab cater to the following category of subjects: Computer Programming, Database and Big Data



Management, Web Programming, Software Engineering and Project Management, App Development, Networking, Linux – Open source OS, System Security, Artificial intelligence, IOT Technology and many more.

One of the labs are exclusively reserved for final year students with high end machines to work on their projects.

The department also supports labs for post graduate students which including High Performance Computing.

Department of Master of Computer Applications

Programming Lab

Laboratory courses conducted: – Programing Concepts and C++, Programming and Data Structure, Computer Graphics, Object Oriented Concepts through Java, Web Applications through Servlets, JSP,EJB etc. Lab is equipped with thirty machines with Software like, Turbo C++, Java, Developer C++ etc.

Database Lab

Laboratory courses conducted: - Database Managements Systems with SQL and database Programming with PL/SQL, Parallel and Distributed database, Data warehousing and Mining Concepts, Probability and Statistics lab etc. Lab is equipped with thirty Machines with i3 and i5 configuration etc. Software Oracle11g, SPSS11 (statistical packeage for the Social Sciences), Weka7 etc.

Web Technology Lab

Laboratory courses conducted: - Computer Network Lab, Wireless and Mobile Computing etc. This lab consists of computers installed with Cisco packet Tracer, Visual Studio 2010, PhoneGap etc.

Operating System & Project Lab

Laboratory courses conducted: - Software Engineering, Software Testing Unified Modelling Language lab and Summer project. The lab consists of Machines with i5 configuration the latest soft wares like Cisco packet Tracer, Visual Studio 2010, MS Project 2007, Visual Paradigm, QTP, Fedora etc.





18. INDUSTRY COLLABORATION

1. AIA (Automation Industry Association): VESIT is the only technical institution in Mumbai which is authorized by AIA (Automation Industry Association) to be a 'Competency Development Centre' for conducting course on "Industry Automation" as a part of its Campus Connect Initiative. The newly developed Integrated Automation Lab was inaugurated by Padmashri Dr. G. D. Yadav, Director, Institute of Chemical Technology, Mumbai on 2 March 2016.



2. NI LabVIEW Academy: V.E.S. Institute of Technology has started NI LabVIEW Academy in collaboration with National Instruments (USA), Bengaluru on 9 July, 2013. This is the first of its kind in Mumbai region. LabVIEW is a graphical programming software which provides comprehensive tools that are required for any type of test, measurement or control application in dramatically less time. After completing this course, the participants will be able to appear for CLAD (Certified LabVIEW Associate Developer) examination, conducted by National Instruments (NI), USA. The CLAD distinguishes basic proficiency in using NI LabVIEW to develop applications. The certification and course work conducted by academy is useful for developing expertise in the LabVIEW environment for career advancement and employment opportunities. NI LabVIEW Academy will provide strong platform for interaction with industries and R & D centres to undertake research projects. Under this academy, training programmes are conducted for UG and PG Engineering staff and students.

3. e-Yantra Lab Setup Initiative (eLSI): V.E.S. Institute of Technology is equipped with state of art facility for Robotics, e-Yantra Robotic Lab under eLSI (e-Yantra Lab Setup Initiative). e-Yantra Lab Setup Initiative (eLSI) is an initiative by the IIT-Bombay under which colleges are encouraged to set up robotics labs. It is designed as a scalable and sustainable approach that addresses infrastructure creation and teacher training to create an eco-system at the colleges to impart effective engineering education. This facility is availed to the students, so that they can focus on embedded system, robotics projects.



e-Yantra Lab is equipped with Firebird V , Spark V , Qu-Bot robots, various hardware, software platforms required to build develop embedded projects. Every year 20-25 teams of students participate in Robotic competition.

- **4. Accenture Lab:** "VESIT-Accenture Innovation Centre" is a specialized lab developed in accordance with VESIT's MoU with Accenture. This lab is used by Accenture to conduct their Head Start Foundation Program
- (HSFP) for students selected through campus placement, faculty development program and other similar activities.
- **5. Cloudera Lab:** The Big Data Analytics lab in collaboration with Cloudera provides a high end distributed environment for Big Data processing, through which training is provided in specialized area 'Hadoop' and projects are undertaken to apply the concepts using Cloudera tools. A six week course is regularly conducted for the final year students.



- **6. VESIT D-link Academy Lab:** D-Link academy at VESIT started on 1 April 2015. This academy trains students in batches which will equip them for better placement opportunities. As a part of this venture, a specialized laboratory is set up in EXTC Department for wireless communication. Under this academy, our faculty members were trained by D-Link to conduct this course for our students. This is an excellent opportunity for our students to get industry certification in their own campus.
- 7. Microsoft Certification: VESIT has recently signed up an MOU with ATS learning solutions for facilitating Microsoft certifications and arranging short term skill development workshops on Cross Platform Mobile Application Development, Internet of Things, and Cyber Security.

19. RESEARCH AND DEVELOPMENT LAB

R&D Department undertakes various R&D projects from industries and works on various research problems for M.E and Ph.D. students in VESIT.

Some of the successfully completed R&D Projects are:

- ➤ "Electronic Radioactivity Simulator" in collaboration with BARC, which was funded by BRNS.
- > "Multi-channel analyser", "Nuclear Counting System" in collaboration with Nucleonix, Hyderabad.
- "Analysis and Monitoring of Degradation of Insulation in H V Transformers" in collaboration with L&T
- "Nuclear Detector Characterization" in collaboration with I.I.T, Mumbai.
- ➤ "Sensor network for Agricultural application" in collaboration with TCS and "Low level signal generation" signal processing circuit for Rogowski coil based integrator with low time constant in collaboration with L&T.
- Low level sensing (from 2mA onwards) in industrial environment, DC level shift correction for sensing true AC signal, Peak detection of low level signals in frequency range of 5KHz to 25KHz, High Speed Time to Amplitude Converter and Leakage detection using Fibre Optics with L&T.
- ➤ Simulation of shielding characteristics using GEANT4 (Software for simulation of nuclear data).

Our students are carrying out research work in the areas of:

- ➤ Low Level Signal Processing systems.
- ➤ Reconfigurable ADCs
- Optimized Nuclear Spectroscopy System
- > Research in New Techniques for on-line monitoring insulation degradation
- Research for changes in characteristics of optical fibres with changes in various Parameters like temperature, vibration etc.
- Universal Research Grade Biomedical Signal processing system
- > Timing spectroscopy systems
- Application of Kalman Filter to Nuclear Engineering Data and Bayesian Estimation of Nuclear Engineering Data
- Nuclear data evaluation for 241Am using Kalman Filter Technique
- > Design and construction of precision and sliding pulse generator

20. COMMON INFRASTRUCTURE

LIBRARY

Our library has more than 47,000 books and journals of national and international level. The number of standard journals / magazines: Journals – 30, Magazines-60, e-journals - 1121. Library also provides:

- Internet and all e-journal access through college campus as well as off campus
- > Open access facility for all
- ➤ IIT Library Membership
- Membership with NDL
- > Best User facility for student
- > NPTEL lecture facilities
- Best user get additional book facility

Along with technical updates via latest journals, students can also keep themselves abreast with the latest happenings in the world via newspaper, magazines and internet facility. The library is divided into two sections:

Reference Books: - This section stocks recent editions of textbooks. Student can borrow one book at a time against their Library Identity Card. The books/journals in this section are meant for reading in the hall only. The previous year question papers of Mumbai University are also available in this section.

Lending Section: - Students can borrow two textbooks for one week from this section. A fine will be charged if the book is not returned within the given period.

Students are required to abide by the following rules of the Library. Any violation of these rules will lead to a fine or punishment by the Principal.

- a. Every student must submit his/her valid identity at the entrance of the library and reading room. Exstudents of the college, who are members of Alumni Association, can also use the library facilities.
- b. Pencil or ink marking, writing on the pages or any other way of mutilating the books will be treated as library indiscipline. The reader will be held responsible until or unless he/she points out to the Librarian at the time of issue of the book.
- c. The reference books, journals, question paper sets, dictionaries etc. will be issued for reference in the Reading Room only and not to be taken out of the Reading Room.
- d. Students must refrain from disturbing other members in Library and Reading Hall.
- e. The student should inform the Librarian immediately of any loss or damage to the books borrowed by him/her on the library card.
- f. Any other matter not covered by these Rules will rest at the absolute discretion of the Principal and Librarian.
- g. Student should not bring any valuable article to the Library. If such things are lost, the Library staff cannot be held responsible for it.
- h. Students should maintain complete silence in the Reading Hall.
- i. Students should contact the Librarian if they face any problem regarding usage of library facilities. They should not argue with the staff on duty.





OTHER FACILITIES:

PROJECT ROOM

Separate Project Rooms are provided for each department with the laboratory facilities and computers installed so that the students can carry out their project work even after the college hours.

CANTEEN

The college has a huge canteen, with modern kitchen facility, serving fresh and hygienic food which offers a multi-cuisine variety.

CONFERENCE HALL

Seminars and Students' presentations are held in the Conference Hall, which has a seating capacity of 250 students. It is equipped with a good sound system along with video and data projectors.

MUSIC ROOM

Music Room is well equipped with instruments such as Flute, Violin, Guitar, Tabla etc. Students can learn and practice after the college hours. Student Council wing "Bliss" (Beauty lies in Soulful Symphony) is in-charge of Music Room and conducts music competitions and workshops for the benefit of students.

SPORTS

Life of an engineer is incomplete without any extracurricular activities. For a healthy body and mind, apart from the usual workload and stress, VESIT provides its students with a sporting extravaganza SPHURTI.

SPHURTI is an integral part of VESIT's history and it has been so since the beginning of VESIT.

- **Play ground:** 8177 sqm
- > Outdoor games: Football, Cricket, Volley ball, Basketball, Throw ball, Athletics, Discuss throw, Shotput.
- > Indoor games: Carom, Chess, Table Tennis.
- Recreation Room

21. TEACHING FACULTY

VESIT believes that the faculty of an institution determines its success in delivering quality education. The college is well known for its highly knowledgeable and immensely proficient teachers who conduct classes with full zeal and robustness. They have contributed greatly in imparting quality education at our Institute. Our teaching faculty includes 23 Ph.D. holders and 27 faculty pursuing Ph. D. Each department also takes initiative to organize "Faculty Development Programs" for helping the teachers to apprise their technical knowledge which in turn helps them to deal with their subject and the projects in a professional manner. The staff members are also encouraged to acquire higher qualifications.

22. STUDENTS' COMMITTEE / ACTIVITIES

Professional Societies

A unique feature of VESIT is the fact that there are four Professional Societies totally managed by students under the supervision of Faculty In-Charges.

1. IEEE – Institute for Electrical and Electronics Engineers

"Excellence through Innovation" is what the IEEE-VESIT stands for in VESIT society culture. IEEE-VESIT gives its members ample opportunity to develop and inculcate their technical and soft skills, be it with theory,

seminar or in practice via interactive workshops.

In-house Publication "Innovations" Symposium "Melange"

Website <u>www.ieee-vesit.org</u>

Activities during the Year:

Quizard, PCB Workshop, Android Workshop, Image Processing Workshop, WordPress workshop, Online Treasure Hunt, Clash of Conquerors, Cryptocurrency workshop, Model United Nations, Technical Paper Presentation, TROTA, What Do You MEME, Article Writing, Annual Symposium - Melange 2018.

2. ISA- Instrumentation Systems and Automation

ISA is a leading global non-profit organization that caters to the needs of over 30,000 worldwide members and other professionals. ISA-VESIT student chapter is well rooted with the International Organization. ISA-VESIT chapter exposes its members to the upcoming competitive world and tries to give them an edge over the others when it comes to practical knowledge through various events and seminars.

In-house Publication "Envisage"

Symposium "Enguage"

Website www.isa.org

Activities during the Year:

PCB Workshop, Analog Electronics Workshop, Arduino Workshop, PSoC Workshop, Wordpress Workshop, Image Processing Workshop, IoT Workshop, Technovation'18, BE and Beyond, SE TE Fun Event, FE fun event, Article Writing, Annual Symposium-Engauge'18.

3. CSI- Computer Society of India

The CSI VESIT stands for ingenuity, innovation and versatility. CSI-VESIT aims to give its members an added dimension to technology by keeping them abreast with the rapid advancements in the wide arena of technology. It is one of the most active student societies with the objective to accelerate student expertise in the field of technology. The CSI-VESIT student chapter has been retaining the award of The Best Student Chapter for the past seven years.

In-house Publication "Random Thoughts" and "Nexus"

National Board "Communications"

Annual magazine "Interface"

Symposium "Impact"

Website <u>www.csi-vesit.org</u>

Activities during the Year:

Wordpress Workshop, Hidden Cipher, Business Quiz, Cric-O-Mania, LAN Gaming, Code Knights (Hackathon), Annual Symposium -Cynosure'18.

4. ISTE-Indian Society for Technical Education

VESIT-ISTE chapter's objective is to bridge the gap between the curriculum and the industry's requirements by giving students a platform to hone their technical and non-technical skills like overall personality, reasoning and presentation skills.

In-house Publication "Mirage"

Symposium "Synergy"

Website <u>www.iste-vesit.org</u>

Activities during the Year

Group Discussion, Notice the Unnoticed, Image Processing Workshop, Techtrix, PCB Workshop, Android Workshop, WordPress, Workshop, Technical Paper Presentation, Placement Week, Article Writing, Around the World, War Of Dragons, Annual Symposium –Synergy.

COUNCILS

Cultural Council

The Cultural Council looks after the complete management, build up, and the execution of all the Cultural Events of VESIT. Starting right from Ganesh Pooja, gradually cascading through Udaan, Illusion and Utsav, the Cultural Council aims to bring the best out of people in the events they arrange.

Cultural Activities

Udaan'17:

The newbies of VESIT were welcomed with a lot of enthusiasm and a warm fresher's party organised by the cultural council. The event was called Udaan'17, "redefining creativity". The event was held on 8th September, 2017. The theme for the day was 'Bollywood'. Every class participated in making a collage and posters and the best class was awarded at the end of the event. There were a lot of fun events such as mimicry, Dance, Mr. And Miss. Fresher's. The Mr. and Miss Fresher's competition had events such as one-minute game, Question and Answer round and Talent show. The Mr. Fresher 2017 award was won by Gaurav Shukla (D4A) and Miss Fresher 2017 award was won by CV Ananya (D4A). The best class was also awarded to D4A.

The MCA Fresher's party was on the next day 9th Sept'2017. Mr. fresher 2017 was awarded to Vaishak Nambiar and Miss fresher 2017 was awarded to Roma Motwani.

Illusion- See the Unseen:

Illusion is an intra-collegiate competition wherein six teams comprising of students from all the five branches of engineering, as well as M.C.A., compete in various portfolios like Dance, Drama, Sports, Aesthetics and Music.

UTSAV: The College organizes UTSAV, a cultural festival that showcases cultural talents. It witnessed overwhelming participation from our students.

Music Council

Bliss: stands for "Beauty Lies in Soulful Symphony". It is the musical group of our college which organizes musical events. Separate music room with very well equipped instruments from classical to western is provided for students to practice.

Octaves: Octaves is a music fest which was organized by the Music Council of VESIT. There was tremendous participation in this festival. For the first time in the college, an Intra-department music festival had an entry of 100 students and made a mark to the music club.



Sonic Highways: It's an event conducted by the council which focuses on fun events like pictionary and bamboozled which involve knowledge in music.

Illusion '18: the only league format cultural event in our University with the objective of promoting intra-college bonding and identifying the most phenomenal talents in the institution.

SoRT

Social Responsibility Team (SoRT)

"Together, we can make a difference". With this amazing thought inspiring theme, e Social Responsibility team grows stronger each year, depicting on how to make a difference actually does pay off. The major objective was reaching out to the needy, collaborating with NGOs- by volunteering to sell their products (handmade) in the college.

List of activities for the year 2017-2018.

Blood Donation-22nd July 2017

SoRT in association with Hashu Advani Memorial Trust and in collaboration with the doctors of J.J. hospital organised a Blood Donation camp, BMI checkup camp & eye checkup camp from 9am to 5pm.

Traffic Monitoring –Ganpati Visarjan, August 31st, 2017

SoRT VESIT in collaboration with Chembur welfare brigade successfully completed this event in which 45 VESITIANS came up and assisted Chembur welfare brigade to maintain the decorum & clear all traffic during Ganpati Visarjan of 7 days Ganpati at Teen Talao Chembur.



Traffic Monitoring – Ganpati Visarjan of RK STUDIO,

September 5th, 2017

SoRT VESIT assisted Chembur Welfare Brigade – Mumbai Police for the Ganapati Visarjan of the famous RK FILMS & STUDIO.

UMEED'17 – SoRT Week, 11th – 18th September, 2017

It is one of the biggest events in the college in which the major objective was to help out the needy by collaborating with different NGOs by selling the products made by the people of NGOs. This year, SoRT VESIT collaborated with 6 NGOs namely, National Association for Equal Opportunities for Handicap (NASEOH, CHEMBUR), Are you Recycling? RUR(Matunga), Aseema(Bandra), Snehalaya & We Change(Thane).

Bone Marrow Donation Registration, September 19th, 2017

SoRT in association with Marrow Donor Registry(India) held a Bone Marrow registration camp from 10 am to 4 pm where 120 registrations were made by students and staff, who actively donated sample of cheek epithelial cells.

Lakshya Foundation- Valentine, My Soldier, Feb 26th 2018

An event was held by the Lakshya Foundation in the college auditorium wherein they invited Major of the army, Mr. Mitheel Jaikar, who shared his experience of being in the Indian Army which motivated the students and created awareness at a much higher level. The event made the students realize the sacrifices and the hardships, our army men make to keep us safe behind the border.



Clean-up Drive

"SoRT VESIT with the help of our enthusiastic VESITIANS cleaned the area of Chembur station so as to keep the station clean and more importantly, spread awareness indicating how important it is to keep the station clean where thousands of people come and go on a daily and frequent basis.

NGO VISIT

Every year SoRT VESIT does a NGO visit where they interact with the kids of the orphanage & Senior citizens. Fun events & games are conducted and quality time is spent with our loved ones.

Sports Council

Sphurti: Sphurti is an inter class event where players from each class compete with the spirit of sportsmanship. It consists of 2 phases- Indoor and Outdoor Sphurti. Indoor events include competitions like carrom, table tennis, darts, chess and Badminton. Events like Cricket, football, basketball, athletics, kabaddi, volleyball, dodgeball, throwball are conducted in outdoor Sphurti. The council conducts the events strictly sticking to the rules given by the Mumbai University for every Sport.



E-Cell (tVEC)

Entrepreneurship is the capacity and willingness to develop, organize and manage a business venture along with any of its risks in order to make it a success. Innovation is the specific instrument of entrepreneurship...the act that endows resources with a new capacity to create wealth.

But entrepreneurship is neither an art nor science; it's pure practice. It takes years of self-improvement, determination



and perseverance to achieve what is seen as overnight success. Hence to bestow upon this practice of entrepreneurship to its students from the very beginning, the VESIT Entrepreneurship Cell (tVEC) was founded in 2014.

tVEC comes with many innovative events throughout the year to imbibe the entrepreneurial skill in students, because entrepreneurship is not about dreaming but doing.

Activities during the Year

BookSwap, Group Discussion, Envoyage, AdVenture, E-summit 2018, Hackathon, Internship Mela and Bizcafe.

CIRCLES

VESIT PhotoCircle

Vesit Photo Circle is a team of photographers. We photograph all the activities/events that take place in college. we also represent our college in inter college events regarding photography.

VESLit

It aims at teaching spoken English skills by taking special sessions for the students who want to hone their spoken English skills and groom their personalities. The newly constituted council of 2017-2018 with lots of enthusiasm and energy, marched on with these agendas in mind. They had a very successful year with great new activities lined up in both the semesters.

Events (2017-18): Whiplash, Make the Cut, VESLit Week, Sell it off, Game of Shows, Stand Up comedy and Playoffs 2018.



VESIT RENAISSSANCE CELL

In the academic year 2015-16 VESIT has come up with 'VESIT RENAISSANCE CELL' (VRC) with the mission, "To Suggest and implement innovative schemes to afflux VESIT for achieving academic excellence."

VESIT Summer and Winter Internship:

The Summer Internship of 2017, was an outcome of Principal Madam's vision of providing practical experience to the VESIT students, and the job was assigned to VESIT Renaissance Cell (VRC). The winter Internship conducted by VRC during Dec 2017- Jan 2018 a total of 45 students were selected for internship.

PRAXIS

In order to be successful, one must have a competitive spirit and a optimistic attitude towards learning from mistakes. To nurture and promote this competitive spirit, VESIT organizes PRAXIS every year. PRAXIS is our





annual inter-collegiate technical festival which has an exceptionally competitive Robotics event that has become a much awaited event among engineering students. It is gaining popularity not only in the Mumbai University but in other Universities as well. PRAXIS also boones and cultivates a sense of team spirit and event management skills in the students along with a sense of belonging.

VESITConnect

VESITConnect is the monthly newsletter of our institution which has been dedicatedly working since September 2015 to keep the college connected. The motto towards the team's determination is to keep the students, staff, management and the alumni abreast with the various events happening in the college, and also to keep them informed about the upcoming events. The newsletter captures all the joy and excitement around the college.

VES LEADERSHIP ACADEMY AND RESEARCH CENTRE (VESLARC)

To be successful in today's world of severe competition and stress, it is necessary to have the right attitude and positive thinking. At VESIT, the students are helped by the specially appointed faculty of VESLARC to develop a strong personality with leadership qualities.



VESLARC (VES Leadership Academy & Research Centre)

"Manage the self; Manage the world"

Workshops on Self-development and Employability Skills
offered by VESLARC

EMPLOYABILITY SKILLS

Effective Public Speaking
Confident Body Language & Grooming
Group / Case Discussions
Personal Interview
Résumé / CV writing
Impactful Presentations
Teamwork & Leadership

SELF MANAGEMENT SKILLS

Time Management
Goal Planning
Stress Perception Management
Attitude Management
Handling Relationships
Handling Peer / Group Pressure
Personality Enhancement

CONTACT: +91 86528 11026 | veslarc@gmail.com



SPICE

SPICE was formed, with an aim to bridge the gap between the college syllabus and the placements. It conducts aptitude tests, sessions by experts and ex-students to make students understand the aptitude level of dream

companies as well as prepare them for such placements. It also aides in honing their overall personality by giving them an impetus to speak with confidence through activities like GD and debate sessions and prepare them for the future corporate world.

OTHER EVENTS AND ACTIVITIES

Annual Day: Annual Day is a cultural extravaganza which is a complete event management exercise for our students under the supervision of our faculty. Annual prize distribution is also held on the same day.

Teachers' day: The College organizes Teachers' day function for staff members every year. Teachers completing 20 years of service are felicitated on this day.

Technology Day: On Technology day, we organize project exhibition and poster competitions for our final year and pre-final year students. Students actively participate and demonstrate their research ability as well as get to know the advancement in technology. This day is celebrated in the memory of our founder trustee "Shri. K. B. Mansharamani".

VES Premier League: This is organized by a group of Football enthusiasts from our college in collaboration with other VES members. VESIT's premier football tournament, the VESIT Premier League re-turned with its fourth season this academic year..

Green Army Maharashtra: Green Army is Maharashtra Government's initiative to expand the green cover of Maharashtra. The institute's staff and students are registered to the program and conduct tree plantation programs in the campus and around the campus. Around 25 plants have been planted as a part of the program.

VESIT Cricket League: This is similar to the VESIT Premier League but for all the cricket lovers of VESIT. VESIT Cricket League returned this academic year with its third installment. The league comprised of eight teams and all captains were from the third year. The captains initially battled it out in a scintillating auction round. The teams were named after iconic cricketers.

Celebration of 69th Republic Day: To celebrate our 69th Republic Day, VES organized a ceremony at VES Polytechnic ground on 26 January 2018. The members of various VES institutions assembled to celebrate the proud moment. The events commenced with flag hoisting followed by a parade of the student scouts and cultural programmes presented by the students from various VES Institutes. The ceremony concluded with national anthem and a grand salute to the national flag.

Martyr's Day: Every year, 30th January is mourned as Martyr's Day (Hutatma Din). On this day, we pay our respects to the brave freedom fighters and all those valiant soldiers who sacrificed their lives for the sake of the nation. This year also the Government of India had issued a circular to all the institutions in the country to maintain two minutes of silence to pay homage to the martyrs. All the institutes including VESIT, as instructed maintained two-minute silence at 11.00 a.m. and paid a heartfelt tribute to all the departed brave souls.

Engineers' Day : Every year 15th September is celebrated as Engineers' Day marking the birth day of Sir Mokshagundam Visvesvarayya.

37

23. ACHIEVEMENTS

Deep Blue @ Mastek

In yet another one of its triumphs, the students of VESIT have showcased their talent and made themselves proud. An event called Deep Blue took place in mid-October, wherein the students had to provide a solution to one of the 5 problem statements put up by the Mastek team on their website. All the problem statements were related to the **real life industrial**



issues faced by NGOs, thus making them even more meaningful and inspiring. It was an illustrious moment when the **second position** was bagged by the VESITians, Kaushal Bhogale, Asutosh Padhi, Adheesh Juvekar of third year IT and Nishant Shankar of third year Computer, who were mentored by Mr. Qushai Dalal, an employee at Mastek for a period of three months.

VESIT shines at DRONA - IRIS 2017

VESIT proved its mettle in the field of higher education yet again, with its wondrous show in the **Pan-India Managerial Competition Drona**. The competition (a part of their management fest, IRIS), was held in **IIM-Indore from 10th to 12th November,2017**. The first round included an online test to check the aptitude of students from all over the country. Many students from VESIT participated and quite a few made it to the second round, where **250** from the country were shortlisted to send in a video of themselves. The video demanded the students to introduce themselves and solve a problem statement in a time limit of just a minute and a half. Anirudh Roy, Omkar Nerurkar, Chinmay Mahale, Abhishek Kalgutkar and Priyanka made it to the top 50 ,who were a part of the team sent to **IIM Indore** for Campus rounds. **Anirudh, had a podium finish, placing 3rd in the entire competition.**

E-YANTRA

The e-Yantra ideas competition provides a platform to encourage innovative project ideas from Robotics lab setup through the e-yantra lab setup initiative (eLSI), in colleges across the country. Students get to demonstrate their knowledge and programming skills in embedded systems and robotics. It provides an opportunity for students to apply their knowledge to solve a real life problem with given hardware. A total of **9 teams** had participated from our college, from which four were selected for Stage 1 on 3rd November 2017. Team Representatives submitted the Idea Proposal based on the Idea Proposal Format on 7th November 2017. Each team had a different mentor. Only one team (Vijay Kataria (D17B), Shivani Valecha (D17B), Nikhil Vatwani(D17B), Priyanka Wadhwani (D17B)) was selected for Stage 2, having Mrs. Sharmila Sengupta as their mentor. They

presented their project on 27th February along with about **40 other teams** from the Western Region(Mumbai)in **K.J. Somaiya College of Engineering** for Regional finals, in which they got selected and won award under Most Realistic Category at IIT Bombay held during 22nd – 24th March 2018for National Finals 2018.



EYRC

The e-yantra Robotics Competition uses a Project-

Based Learning approach which imparts knowledge on concepts in Embedded systems and Robotics and solving real world problems. The competition is conducted completely online over a period of 4-5 months. During this period, Teams work on Tasks – each Task being a step in the learning/problem-solving process. Based on the performance in the Tasks, teams are selected for further stages of the competition/Finals of the competition. Finals of the competition was held at **IIT Bombay** in March 2018.

Students from all branches and batches are eligible for this competition. The selection is done through an online test in the form of MCQ questions. All the members of the team have to attempt the test simultaneously. The team's selection test score is used to determine the eligibility of the team to participate in the competition. **6 teams** from our college have cleared the test and are shortlisted for round 1. But the only team that completed this task and proceeded for round 2 was the team of from BE EXTC. - Hitesh Tewani and Yash Saraf, Shubham Singh, and Aneesh Gunda. "Eyic and Eyrc are great platforms for students to convert their theoretical knowledge into practical by doing industry related projects which are funded by MHRD ministry of India and driven by IITB" said Hitesh Tewani.

Through this competition, students are not only learning and exploring technologies, but also learning to analyse a problem and find appropriate solutions.

RoboFest

Deepesh Sidhwani and Abhishek Chhabra from our college have bagged **first position in RoboFest organized by IIT Roorkee** from 23rd March to 25th March.

VESIT Students shine at Inter-College Elocution Competition

An Inter-college Elocution Competition for students of the colleges **affiliated to Mumbai University** was organized by **MLDC ALUMNI ASSOCIATION** on 27th January, 2018. It was held at M.L. Dahanukar College of Commerce, Mumbai. While seven students from VESIT participated in the event, two of them; Rakshit Ganvir (D9) and Poorvi Parmar (D14) won the first place and the third place respectively.

Around 100 students from various colleges in Mumbai had participated in this competition. The time allotted to each participant to speak was 5 to 7 minutes. The students had to select a topic from the 5 given topics. Rakshit Ganvir spoke on the topic 'Brain Drain from India-causes and effects' and Poorvi spoke on the topic 'Should students take foreign language courses to make a better career'. Both of them were awarded with a Trophy, Cash Prize and certificate.

VESIT Students shine at Inter-College Debate Competition

Rakshit Ganvir (D9A) and Harish Muthalagan (D9A) representing VESIT, won the second position SAMVAAD'18, the inter-college Debate Competition held at Fr. C. Rodrigues Institute of Technology on 3rd March, 2018. Around **70 teams** from different colleges across Mumbai had participated in this competition. The competition was held in four rounds. A list of topics was given to all the participants and final topic was known 15 minutes prior the first round. Semi-final and final round topics were given on the day of competition. Apart from winning the second position, Rakshit Ganvir was also declared as the **'Most Dynamic Speaker'** of the event. Both of them were awarded a certificate and cash prize.

VESITians dance their way to victory

The talented dance troupe of VESIT, the **VESIT Dance Crew**, matched their steps perfectly to clinch multiple winning titles. An amalgamation of different styles, genres into masterful interpretations makes this crew stand apart from the rest. They have won the first position at Nrityadhara, the **Inter-Collegiate Dance Competition** held at **K. J. Somaiya Medical College, Vidhya Vihar**, on 1 March 2018 and as well at the inter-collegiate dance competition Balissmus at **Cooper hospital Medical College, Juhu**, Mumbai, held on 4th of March 2018. The team has been the **second runners** up at the dance competition Footloose held at **K. J. Somaiya Institute of Technology, Sion**, on 8 March 2018. They have also bagged the **third position** at the **Tatva Moksh Lakshya** conducted by the **SIES Graduate school of technology** on March 10, 2018.

The Stage is Ours!

While having delivered performances with finesse, the drama team of VESIT has never failed to prove that they can move our minds and souls alike! Taking their very nuances to other stages, the inter college Drama Team of VESIT has struck **gold** with their breath taking delivery of emotions 'CHITA'. With it, they threw to the audience a hauntingly stellar show that pricked their conscious with subtle truths. This earned them the first V.E.S. INSTITUTE OF TECHNOLOGY 40

positions at Spandan the Inter-College Drama Competitions held by St. Xavier Institute of Engineering on 4 February 2018 and Verve held by Shah and Anchor engineering college on 9 March 2018. The team explored newer avenues by bagging the third place at the ETAMAX Street Play held at FCRIT, Vashi We wish our best to the team to be blessed enough to move many more hearts.

SMART INDIA HACKATHON



First Prize @ SIH 2018:

And this year too VESIT won **First prize at Pune** Center. The Problem statement was proposed by the Council of Scientific and Industrial Research (**CSIR**), Government of India(GoI) and our Student of Computer Department gave solution to it.

Second Prize @ SIH 2018:

Another team from IT Department won **second position** and developed a robust platform to boost up the start-up ecosystem in India and providing a common platform for all the stockholders of a start-up for **Ministry of Skill Development & Entrepreneurship, GoI**





Most Persistent System Award @SIH 2018:

Our team of students from IT also bagged **Inspirational Award** for making a tool for fetching and editing information from scanned document mainly an image type document for **AICTE**, India at New Delhi.

CII Student Essay Competition "The India We Want" 4th Edition

Rohan Mohandas, EXTC student won **Second Prize** in Undergraduate & Postgraduate Category at **CII Student Essay Competition, "The India We Want"** 4th Edition.



Education World Private Engineering Institutes Rankings

Education World is India's first education-focused magazine. Education World conducted a survey of India's Top 100 Universities, Engineering Colleges and Top 50 B-Schools. EducationWorld India Private Engineering Institutes Rankings 2018-19 survey was conducted by the Delhi-based Centre for Forecasting and Research Pvt. Ltd (C fore), one of the country's premier market research and public opinion polling companies.

To compile the national and state rankings, C fore field researchers interviewed faculty and final year students of engineering institutes countrywide and industry representatives, asking them to rate engineering institutes on following nine parameters Leadership / Governance Quality, Faculty Welfare and Development, Competency of Faculty, Curriculum and pedagogy, Industry Interface, Research and Innovation, Infrastructure and Facilities, Placements, Value for Money.

Further, the interviewees were also asked to assign weightage to each parameter. The scores awarded by respondents under each parameter were totaled for the purpose of final ratings and ranking. V.E. S. Institute of Technology (VESIT) has earned the **FIRST RANK in Maharashtra State**.

Shreshthata Award

The 'Shreshthata award' is an initiative by VESLARC (VES Leadership Academy and Research Centre) to acknowledge and encourage students who endeavor to make a difference in spite of unaccommodating circumstances.

Tinkerers' Lab: Students' Secret to Creativity

"It's not about ideas. It's about making ideas happen." - Scott Belsky

The Tinkerer's Lab at VESIT was started with an aim to bring together the students who wanted to learn and teach. It also aimed at using the available resources in the college optimally and to eliminate the hassle of starting a project from the scratch and working on redundant tasks. It is currently operational in three departments viz. Department of Electronics, Department of Electronics and Telecommunication and Department of Instrumentation. The aim of Tinkerer's Lab is to make lab resources available to the students and also e-waste of various labs.

Summer & Winter School Training

Training is arranged for the students who wish to get some academic edge over their peers during winter and summer vacations. Different departments organize training programs on recent technologies or industry relevant topics; where experts of the domain deliver highest quality of input to the participants. The training schools offer students thoughtful inquiry, robust skill development and insightful discourse.

Tech geeks flex intellectual muscle

Students of technology institute showcase their technical talent at Praxis 2017

By Parth Khatau

echnical wizardry was at its innovative best as students of Vivekanand Education Society's Institute of Technology (VESIT) dis-played a unique blend of imagination and engineering artistry at Praxis 2017, a techfest organized by the institute on September 22 and 23. Since its inception 15 years ago, the festival has been receiving an overwhelming response from students.

This year has seen the unveiling of innovative gadgets and gizmos, conceived and designed by students, applying theory into practicals. Ankit Panyalkar and his team designed a self-steering car which is guided by a GPS tracking system. The vehicle has also been equipped with an IR system for obstacle detection and works on a virtual interface

'Accident prevention and lowering of fatality rates remains the underlying objective of road safety measures. We have designed this vehicle to dwell on the im-



portance of safety technology solutions and underscore the need to devise indigenous technologies for enhancing road safety standards," stated Panyalkar who is a third year student of Electronics at VESIT.

The two-day technical event was a showcase of various innovations across Robotics, Mechanics, Technical, Electronics and other events beside the inclusion of several fun events.

'VESIT has always been at the forefront of providing a podium for students to display their technical skills. Praxis has been conceived by Vesit Renaissance Cell as an event to bring out the innovator in the students and helps build a

विद्यार्थ्यांचा पुढाकार : लघुपट केंद्रीय दक्षता आयोगाच्या सप्ताहात

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> मुंबई : देनंदिन जीवनात अनेक वेळा प्रष्टाचाराचा परिणाम होताना दिसतो. काही भ्रष्ट लोकांमुळे अनेकदा व्यवस्थेला नावे ठेवली जातात. याच सर्व भ्रष्टाचारावर महाविद्यालयातील विद्यार्थ्यांनी पुढाकार घेऊन लघुपटाच्या माध्यमातून भाष्य केले आहे. मुंबईतील विद्यार्थ्यांचा हा लघुपट कॅद्रीय दक्षता आयोगाच्या दक्षता जागृती सप्ताहात दाखबिण्यात येणार आहे.

विवेकानंद एज पोसायटीच्या 'इन्स्टिस्यूट वनॉलॉजी'(येसीट)मधील

विद्यार्थ्यांनी लघुपट या व्यापक अभियानाचा एक लघुपटांच्या सात माध्यमातून भ्रष्टाचाराच्या प्रश्नावर प्रकाशञ्जीत टाकला. हा उपक्रम भ्रष्टाचाराच्या समस्येवर जागृती करण्याच्या कटिबद्धतेचा एक भाग भाग आहे.

दृष्टीकोन दाखेवून दिला. 'भ्रष्टाचारमुक्त भारत - माझा दृष्टीकोन' हा अभियानाचा विषय आहे. इस्ता जागृती सप्ताह ३० ऑक्टोबर ते ४ नोव्हेंबरदरम्यान आयोजित करण्यात आला असून, विद्यार्थ्यांनी तयार केलेले

Hello Mumbai Page No. 3 Oct 19, 2017 Powered by: erelego.com

होता. हे सर्व लघुपट विद्यार्थ्यांनी अभिनित आणि दिग्दर्शित केले होते.

यातून या विद्यार्थ्यांनी आपला आदर्श

आणि भ्रष्टाचारमुक्त समाजाचा दृष्टीकोन दाखवून दिला.

भाग आह. भ्रष्टाचार ही एक वाईट सामाजिक प्रवृत्ती असून, तिचे समाजाच्या सर्व अगायर विपरीत परिणाम होत असतात. भ्रष्टाचाराविरोधात स्वात्यालं रावाणकं संस्थात सातत्याने जागृती अभियान राषयण्यास प्रोत्साहन देणे आवश्यक आहे. विद्यार्थ्यांनी सनयलेले हे लघुपट सामाजिक बदल आणि विकासाची आस असलेल्या पिढीचा दृष्टीकोन प्रकट करतात, असे वेसीटच्या विज्ञानेस कम्युनिकेशन विभागाच्या सहायक प्राध्यापिका गीता अजित यांनी

Showcase of issues with a creative twist!

Students of Vivekanand Education Society's Institute of Technology (VESIT) flex creative muscle by highlighting corruption issues through a showcase of short films

Students from Vivekanand Education Society's Insti-tute of Technology (VESIT) showcased a series of 7 short flims as part of their commi-ment to spearhead an awareness drive against cor-ruption. The students acted in, conceived the states response to the state of the st

corruption should be ecouraged across all eductional campuses in tounity. The films made the students are the definition of the students are the definition of the students are the definition of the students of the institution had bullish opinio about the array of talent display. "The visual mediumakes a lasting impression the human mir Through these films, we hareiterated the commitme of a generation towar spreading corruption awar mess and promoting the visuals and the students of the stu ness and promoting the v-ues of honesty at transparency," said Har Bhatia, a student. Harsh's classmate Dec



25. PROHIBITION OF RAGGING

Ragging within or outside the institute is strictly prohibited. Any student who directly or indirectly participates in, abets or propagates ragging within or outside the institute shall face strict disciplinary action as per the provision of Maharashtra Prohibition of RAGGING ACT, 1999.

If a case is to be made as per law the following procedure is to be followed:-

- > Investigation by squad
- Findings reported to the Principal
- Principal's discussion with the Committee to decide whether to file an FIR (within 24 hrs) with Police or Local Authorities OR punishment is to be awarded by the Anti-Ragging Committee of VESIT

Punishments may be in the form of:

- > Suspension from attending classes and academic privileges.
- ➤ Withholding/ withdrawing scholarship/ fellowship and other benefits.
- > Debarring from appearing in any test/ examination or other evaluation process.
- ➤ Withholding results.
- > Debarring from representing the institution in any regional, national or international meet, tournament, youth festival, etc.
- Suspension/ expulsion from the hostel.
- > Cancellation of admission.
- Rustication from the institution for period ranging from one to four semesters.
- Expulsion not only from the institution but also consequent debarring from admission to any other institution for a specified period. In case, the persons committing or abetting the act of ragging are not identified, the college will resort to collective punishment.

ADVICE TO ALL STUDENTS:

All students are advised to be aware of anti-ragging measures and report any ragging incident to college authorities in person or by telephone at the earliest.

More information is available on our institute's website: http://ves.ac.in/vesit
Students should submit an affidavit as per #ANNEXURE I and #ANNEXURE II at the time of admission.
(Refer page nos. 56 and 57)

Anti-Ragging/ Disciplinary Committee

Sr. No.	Name Designation		Position
1	Mr. Manoj Sabnis	Faculty, INFT	Chairperson
2	Mrs. M. Vijayalakshmi	Vice Principal, VESIT	Member
3	Dr. R. K. Kulkarni	Faculty, EXTC	Member
4	Mr. A. Nagananda	Faculty, EXTC	Member
5	Mr. Vivek Umrikar	Faculty, H & Sci.	Member
6	Mr. P. Birajdar	Faculty, ETRX	Member
7	Dr.(Mrs.) S. Mujumdar	Faculty, H & Sci.	Member

26. WOMEN DEVELOPEMENT CELL

Constitution – as per Guidelines from Mumbai University				
Sr. No.	Name	Designation	Position	
1	Mrs. M. Vijayalakshmi	Faculty, MCA	Member	
2	Mrs. Shobha Krishnan	Faculty, EXTC	Member	
3	Dr.(Mrs.) Saylee Gharge	Faculty, EXTC	Member	
4	Mrs. Naveeta Kant	Faculty, ETRX	Member	
5	Mrs. Nishi Tiku	Faculty, MCA	Member	
6	Dr. Nupur Giri	Faculty, CMPN	Member	
7	Mrs. Sonali Rahate	Superintendent	Member	

27. ANTI SEXUAL HARASSMENT COMMITTEE

The Government of India had promulgated The Sexual Harassment of Women at Workplace (Prevention, Prohibition & Redressal) Act, 2013 (Act) and the Sexual Harassment of Women at Workplace (Prevention, Prohibition & Redressal) Rules, with effect from 9th December, 2013. The

act was laid down to ensure that women in particular are protected against sexual harassment at all workplaces, be it in public or private. It has now become mandatory to have an Anti-Sexual Harassment Committee as well as an Internal Complaint Committee to be formed in every institution in addition to the reports being sent to the Government for the same.

It is compulsory for all institutes/colleges under AICTE / UGC to Constitute "Internal Complaints Committee (regulation has come into force since 1st July 2016)

Sr. No.	Name Designation		Position
1	Mrs. Shobha Krishnan	Faculty, EXTC	Chairperson
2	Mrs. Jayshree Pandit	Co-ordinator/ Counselor, Sri Mukta	Member (NGO)
		Sanghtana,	
		R. C. Marg, Chembur - 71	
3	Dr. (Mrs.) Nupur Giri	Faculty, CMPN	Member
4	Dr.(Mrs.) S. Mujumdar	Faculty, H&Sci.	Member
5	Mrs. Nishi Tikku	Faculty, MCA	Member
6	Mrs. Sonali Rahate	Office Superintendent, VESIT	Member
7	Student Representative (Girl	Cultural Secretary/SoRT Secretary /	Member
	Student)	Deputy Secretary, VESIT	

28. INTERNAL COMPLAINT COMMITTEE

Sr. No.	ICC Posts	Name of the Member
1.	Presiding Officer	Prof. Smita Jangle
2.	Non-Teaching Employee	Mrs. Sonali Rahate
3.	Non-Teaching Employee	Mr. Vikas Page
4.	Faculty Member	Prof. Sujata Khedkar
5.	Faculty Member	Prof. Nandini Ammanagi
6.	External Member(NGO)	Mrs. Jaishree Pandit
7.	Student	UG- SORT Secretary
8.	Student	Mrs. Abha Tiwari (PG)
9.	Student	Mrs. Asma Praveen (PhD)

29. GRIEVANCE REDRESSAL COMMITTEE

For A.Y. 2017-18 To A.Y. 2019-20

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Shoba Krishnan	Faculty, EXTC Dept., VESIT	Chairperson
2.	Dr. (Mrs.) Nupur Giri	Faculty, CMPN Dept., VESIT	Member
3.	Dr. Nadir Charnia	Faculty, EXTC Dept., VESIT	Member
4.	Mrs. Navita Kant	Faculty, ETRX Dept., VESIT	Member
5.	Mrs. Nishi Tikku	Faculty, MCA Dept., VESIT	Member
6.	Dr. (Mrs.) S.M. Mujumdar	Faculty, H&A Dept., VESIT	Member
7.	Dr. (Mrs.) Sayalee Gharge	Faculty, EXTC Dept., VESIT	Member
8.	Mrs. Sonali Rahate	Office Superintendent, VESIT	Member

30. COMMITTEE FOR SC/ST

With reference to Scheduled Castes and Scheduled Tribes (prevention of atrocities) Act, 1989, No. 33, of 1989, 11 September 1989, the Constitution of the Committee for SC/ST is as follows

For A.Y. 2017-18 To A.Y. 2019-20

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Sujata Khedkar	Asso. Professor, CMPN Dept., VESIT	Chairperson
2.	Dr. Nadir Charnia	Professor, EXTC Dept., VESIT	Member
3.	Mrs. Asha Bharambe	Asst. Professor, INFT Dept., VESIT	Member
4.	Mrs. Indu Dokare	Asst. Professor, CMPN Dept., VESIT	Member
5.	Mrs. Sangeeta Bhalerao	Sr. Lab. Asst., VESIT	Member
6.	Mrs. Nilima Tambade	Sr. Clerk , VESIT	Member
7.	Mr. Vinayak Doifode	Sr. Clerk , VESIT	Member
8.	Student	Secretary, SoRT, VESIT	Member

31. ADMISSION GRIEVANCE COMMITTEE

For A.Y. 2017-18 To A.Y. 2019-20 STAGE - I

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Shoba Krishnan	Faculty, EXTC Dept., VESIT	Chairperson
2.	Mr. Manoj Sabnis	Faculty, INFT Dept., VESIT	Member
3.	Mrs. Nishi Tikku	Faculty, MCA Dept., VESIT	Member
4.	Mrs. Asma Parveen	Faculty, INFT Dept., VESIT	Member
5.	Mr. Vinayak Doifode	Administrative Staff, VESIT	Member

STAGE - II

Sr. No.	Name	Department/Address	Position in Committee
1.	Dr. P.P. Vaidya	Faculty, INST Dept., VESIT	Chairperson
2.	Dr. R.K. Kulkarni	Faculty, EXTC Dept. , VESIT	Member
3.	Dr. (Mrs.) Gresha Bhatia	Faculty, CMP Dept., VESIT	Member

STAGE - III

Sr. No.	Name	Department/Address	Position in Committee
1.	Dr. (Mrs.) J. M. Nair	Principal , VESIT	Chairperson
2.	Dr. (Mrs). M. Vijayalakshmi	Faculty, EXTC Dept., VESIT	Member

ACADEMIC YEAR 2017-18

(Placement Statistics as on 8th June 2018)

	Overall College	Electronics Engg	Computer Engineering	Instrumentation Engg	Electronics & Telecomm Engg	Information Technology	MCA*
Job Offered	573	86	182	29	128	72	76
Second Job	40	3	11	2	5	9	10
Placed	533	83	171	27	123	63	66
Strength (Total)	847	155	223	74	211	78	106
Strength (Eligible 55%+)	727	127	211	60	177	71	81
% Offered (Eligible)	78.81	67.71	86.25	48.33	72.32	101	93.82
% Placed (Eligible)	73.31	65.35	81.04	45	69.49	88.73	81.04
Average Salary				3.5 Lakhs			
Maximum Salary	16 Lakhs						
No of Companies Visited till date				48			

^{*} Training and Placement Cell also facilitate Internships for MCA Students during their Semester VI.

[#] Figures likely to increase as placement drives are still going on for 2017-18 batch.

Training and Placement Cell Structure:

Training and Placement Officer

Deputy Training and Placement Officer

ETRX	CMPN	INST	EXTC	INFT	MCA
Placement	Placement	Placement	Placement	Placement	Placement
In-charge	In-charge	In-charge	In-charge	In-charge	In-charge
Student	Student	Student	Student	Student	Student
Student Placement Coordinators	Student Placement Coordinators	Student Placement Coordinators	Student Placement Coordinators	Student Placement Coordinators	Student Placement Coordinators

Training and Placement Cell Activities

Year	Activities
I YEAR	Inputs during Induction
II YEAR	Eligibility, Employability, impact of CGPI Contents of Resume and Building your Resume
III YEAR	Mock tests and Placement Readiness Workshops, Interaction with Final year students
IV YEAR	Interaction with alumni / companies. Online mock aptitude test

33. VESIT ALUMNI ASSOCIATION

Alumni Meet of this year was held on January 7, 2017. About 150 ex-students participated in this event. They were indeed very impressed with the change that has taken place in VESIT. They were also recollecting and reminiscing about the wonderful time they had spent here. The Meeting served as platform for current student council members to interact with their seniors and share experiences.



34. PARTICIPATION IN INTER-COLLEGIATE FESTIVALS

Other than in-house events, the Institute also makes sure that the students are allowed to participate in various inter-collegiate events/cultural festivals as it helps them to develop and enhance their talents without affecting regular studies.

35. DISCIPLINE

Discipline plays a vital role in every institute. All the students admitted in the first year (F.E.) of the institute will have to give an undertaking that they would observe general discipline and decent behavior throughout the four years of their engineering course.

35.1. IDENTITY CARDS

Every student who has taken admission and duly confirmed, must immediately take the I-Card, which is issued from the Principal's office and must wear it all the times. A student cannot even enter the premises without his/her I-Card and must readily present it when asked for, by the college authorities.

At the beginning of a new academic year, each student must take a new I-Card issued by the college after surrendering the old I-Card.

35.2. LOSS OF I-CARD AND ISSUE OF DUPLICATE I-CARD

In case the I-Card is lost, students can take special permission from the Principal which is valid only for three days. Within the stipulated time period the student should apply for the I-Card by submitting the prescribed fee. Any student using an I-Card (Original or Duplicate) without the signature of the Principal or without the Institute Rubber Stamp will be penalized.

35.3. EX-STUDENT'S I-CARD

For ex-students the college has made a special provision of separate I-Cards so that they enjoy and take advantage of the college library facilities. The ex-students however, are permitted to avail the facility only on working days

51

during a stipulated time period.

35.4. LIBRARY AND OFFICE TIMINGS

During the term the library will remain open on all working days from 8.0 a.m. to 6.00 p.m. For the benefit of the student the college allows them to use the library facilities for extended hours during holidays and preparatory leaves. The students can visit administrative office between 11 a.m. to 5.30 p.m. for any pending work provided they do not have any scheduled lecture, lab and tutorial during this time slot.

35.5. USE OF SPORTS GROUND

Students should ensure that they adhere to the Rules and Regulations of the college strictly while using the college playground. Strict action would be taken if they are found playing while the lectures/ practicals/ tutorials are in progress.

35.6. HOLIDAYS AND VACATIONS

The list of the holidays and vacations will be displayed on the notice board. The vacation period is liable to change every year and the students should be prepared for such exigencies.

35.7. RAILWAY CONCESSION

Students who are less than 25 years old can avail concession for commuting from College to their residence in and around Mumbai and for travelling to their home town (where their parents reside as declared in their application form for admission) from Mumbai during vacations. Concession forms for Local Railway tickets are issued from the Office of the Institute on working days.

The long journey concession form will be issued only for the vacation period. The students should write their local and permanent home /town address in the application form, for availing this concession. Concessions are being made within three days from the receipt of their application.

35.8. HOSTEL

There are separate hostels for girls and boys in this College where the students are offered residential accommodation in the best possible living environment. It will be provided on first-come-first -served basis and preference is always given to outstation candidates. Before leaving for vacation or holidays its mandatory for hostelites to take permission letter from the class teacher/class counsellor and submit a copy of the same to the Warden.

35.9. SAFETY PRECAUTION

Safety instructions given by teachers should be stringently followed by the students while doing their practical. In case of a mishap, the entire accountability shall lie on the student concerned and the Institute will no way be held responsible.

35.10. LOST AND FOUND

The authorities of the institute will not be accountable or responsible for any damage to the possessions or belongings of the students. However, articles which are lost and found are deposited in the office and are returned V.E.S. INSTITUTE OF TECHNOLOGY 52

to the concerned student after identification.

35.11. DRESS CODE

Students are supposed to wear decent dresses and footwear suiting Indian Culture. **Indecent, skimpy, sleeveless, tightfitting, short and gaudy dresses and t-shirts with crude messages and pictures are strictly prohibited in the college campus.** Students should also keep proper well-groomed hair styles which depicts a positive outlook and persona.

35.12. CLEANLINESS OF PREMISES

All the students should strive for maintaining the cleanliness of the college premises and help in the maintenance and upkeep of the institutional property. They should not scribble anything on the walls, furniture etc. Strict action would be taken against the erring students and if not detected, a common fine will be levied on the entire class. Breakage of furniture will be treated in the same manner. All the students should strive for cleanliness of the premises and help in the better maintenance and upkeep of the institutional property.

35.13. INDULGING IN ANTI-SOCIAL ACTIVITIES

If a student is found in indulging in any anti-social or anti-national activities, he/she is liable to be expelled from the college without any prior notice.

35.14. CHEWING PAN/TOBACCO AND SMOKING

Chewing pan/tobacco or smoking in and around the premises of the institute is strictly prohibited.

35.15. USE OF ELECTRONIC ITEMS/GADGETS

Students are advised to use mobile phones **only outside class rooms/laboratories**. If they are found using mobile during lectures/ labs, the college authorities can confiscate such items and take strict action against the concerned students. Other electronic gadgets/equipment which is needed for project purpose can be brought in with proper permission and documentation.

35.16. REFUND OF CAUTION MONEY

After the completion of the course, an application for the refund of the caution money should be given (along with F.E. fee receipt) within the same financial year. Unclaimed caution money will be forfeited. Lifetime membership fee for alumni will be deducted from the caution money.

35.17. LOCKER FACILITY

Locker facility for the first year students is provided on first-come-first-served basis. The student should forward an application for using this facility for which he/she has to pay a locker fee.

35.18. RETURN OF ORIGINAL CERTIFICATES

All original certificates like Mark Sheets, Passing Certificate, Caste Certificate, Domicile Certificate etc. will be returned to the students after all the admission formalities of the University /DTE are completed. Normally, these certificates are returned to first year students in the month of March/April.

53

35.19. NOTICE BOARDS

Students are advised to read the notice boards regularly for various updates/circulars/notices displayed regarding payment of fees, commencement of examination and other matters of academic interest. Students are expected to pay the Tuition Fees/Exam Fees etc. before the last date. This will avoid penalty as well as inconvenience to them. Students should not go to the office for Railway concession or any other matter while their theory classes /practicals are in progress. Further, whenever they come to the office, they should maintain proper discipline and decorum. They shall always follow the Queue system and shall not crowd near the counters and should not block main entrance and exit points.

35.20. GRE/TOEFL/GATE/GMAT

Students will not be allowed to miss the college for the preparation of GRE/TOEFL/GATE/GMAT or other forms of competitive examinations. It may be noted that students need to plan their schedule well in advance so that it does not affect their academic curriculum. Students who desire to appear for the same will be permitted for a maximum period of 15 days, provided their academic record and attendance in the semester is satisfactory.

35.21. CO-CURRICULAR ACTIVITIES

All co-curricular, extracurricular activities and seminars/symposia/conferences/workshops of the students' professional societies of the college will be held only after the regular college timings. No activities will affect the regular working of the college. Students who wish to participate in local and outstation technical seminars, paper presentation contests, project exhibitions and other competitions will have to take prior permission.

35.22. PICNICS AND OUTINGS

Students will not be permitted to organize/participate in private picnics/outings even on Sundays/Holidays without the written consent of the college Principal. All picnics/outings have to be authorized and accompanied by college staff. Any failure to comply with any of the rule will lead to strict action taken against them.

35.23. INTERNET USAGE

Internet facility is provided to our students at various places. Students are expected to use this facility only for academic purposes and not for any other purposes like chatting. Misuse of this facility and indecent behavior on the NET is liable for punishment and strict action.

35.24. THEFT

Students found stealing other's property or institutional property will be dealt very strictly. If the institutional property is damaged/stolen, the entire batch of students will be penalized and the cost will be recovered as deemed fit.

35.25. SUGGESTION BOX

Students can avail the suggestion box facility provided outside the Principal's Office, in ease of any suggestions, complaints etc.

35.26. BIRTHDAY CELEBRATIONS

Birthday celebrations of fellow students are not allowed inside the class or within the premises or in the vicinity of the college.

35.27. PRINCIPAL'S SIGNATURE

Students should come to Principal's Office for signature or for attestation during the recess time or only after their theory classes/practicals are over.

The above conditions are as per the Rules and Regulations of the Institute and all students should strictly follow them. Strict action would be taken against the student who fails to observe them.

Any matter not covered by these Rules will rest at the absolute discretion of the Principal.

*** Most Important Note ***

- **❖** All Admissions and Cancellations will be conducted/done as per the DTE rules and Guidelines.
- All available Scholarships of University, State Government, etc will be granted to the eligible candidates as per rules. (Please refer website.)

ANNEXURE 1

AFFIDAVIT BY THE STUDENT (On rupees 100 stamp paper with notary)
I,(full name of student with admission/registration/enrollment number) s/o - d/o
Mr./Mrs./Ms
1. having been admitted to V.E.S. Institute Of Technology, HAMC, Chembur, Mumbai-400074., have
received a copy of the AICTE regulations on Curbing the menace of Ragging in Higher Educational Institutions
2009, (hereinafter called the "Regulations") carefully read and fully understood the provisions contained in the
said Regulations.
2. I have, in particular, perused clause 3 of the Regulations and am aware as to what constitutes ragging.
3. I have also, in particular, perused clause 7 and clause 9.1 of the Regulation and am fully aware of the
penal and administrative action that is liable to be taken against me in case I am found guilty of or abetting ragging.
actively or passively, or being part of a conspiracy to promote ragging.
4. I hereby solemnly aver and undertake that
a) I will not indulge in any behaviour or act that may be constituted as ragging under clause 3 of the
Regulations.
b) I will not participate in or abet or propagate through any act of commission or omission that may be
constituted as ragging under clause 3 of the Regulations.
5. I hereby affirm that, if found guilty of ragging, I am liable for punishment according to clause 9.1 of the
Regulations, without prejudice to any other criminal action that may be taken against me under any penal law or
any law for the time being in force.
6. I hereby declare that I have not been expelled or debarred from admission in any institution in the country
on account of being found guilty of, abetting or being part of a conspiracy to promote, ragging; and further affirm
that, in case the declaration is found to be untrue, I am aware that my admission is liable to be canceled.
Declared thisday of month of year
Name & Signature of Deponent

Address & Telephone/Mobile No. :

ANNEXURE 2

AFFIDAVIT BY PARENT/GUARDIAN (On rupees 100 stamp paper with notary)

I, Mr./Mrs./Ms (full name of parent/guardian) father / mother/guardian of
(full name of student with admission /registration/enrolment number),
$1.\ having\ been\ admitted\ to\ V.E.S.\ Institute\ Of\ Technology,\ HAMC,\ Chembur,\ Mumbai-400074.,\ have\ received$
copy of the AICTE regulations on Curbing the menace of Ragging in Higher Educational Institutions, 2009
(hereinafter called the "Regulations") carefully read and fully understood the provisions contained in the said
Regulations.
2. I have, in particular, perused clause 3 of the Regulations and am aware as to what constitutes ragging.
3. I have also, in particular, perused clause 7 and clause 9.1 of the Regulations and am fully aware of the pena
and administrative action that is liable to be taken against my ward in case he/she is found guilty or abetting
ragging, actively or passively, or being part of a conspiracy to promote ragging.
4. I hereby solemnly aver and undertake that
a) My ward will not indulge in any behaviour or act that may be constituted as ragging under clause 3 of the
Regulations.
b) My ward will not participate in or abet or propagate through any act of commission or omission that may b
constituted as ragging under clause 3 of the Regulations.
5. I hereby affirm that, if found guilty of ragging, my word is liable for punishment according to clause 9.1 of the
Regulations, without prejudice to any other criminal action that may be taken against my ward under any pena
law or any law for the time being in force.
6. I hereby declare that my word has not been expelled or debarred from admission in any institution in the country
on account of being found guilty of, abetting or being part of a conspiracy to promote, ragging; and further affirm
that, in case the declaration is found to be untrue, I am aware that my admission my word is liable to be canceled
Declared this day of month of year
Name & Signature of Deponent:
Address & Telephone/Mobile No:



Vivekanand Education Society's Institute Of Technology



SAY

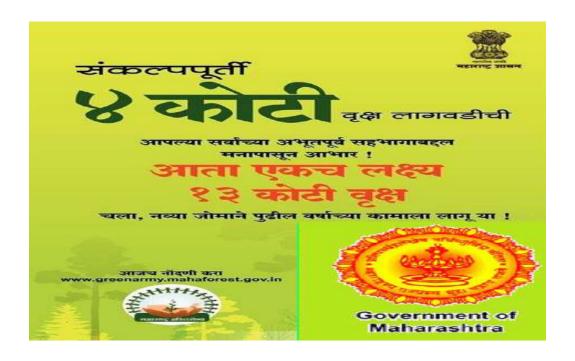
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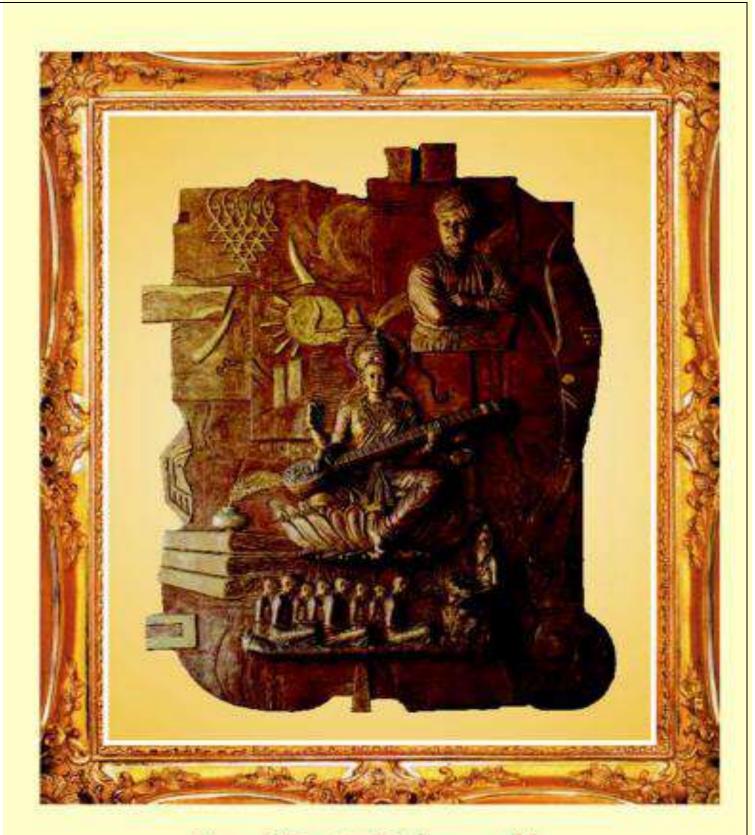
PLASTIC











We need education that forges our life, generates the ideal man, imbibes noble thoughts and develops the abilities in us.

-Swami Vivekanand.



VIVEKANAND EDUCATION SOCIETY



V.E.S. Institute of Technology

Affiliated to Mumbai University & approved by A.I.C.T.E



PROSPECTUS

VESIT SALIENT FEATURES

- **Established Institution Innovation Council (IIC)**
- **♦** Ph.D. Centre of University of Mumbai (Electronics Engg.)
- **♦** Library having more than 48000 books
- **♦** Permanent Affiliation to University of Mumbai
- **♦** Competency Development Centre of Automation Industries Association (AIA)
- **♦** LabView Academy with National Instruments
- **♦** Sprawling Complex inviting Playground & Hostel
- **♦** Training provided for Industries in the State of the Art Instrumentation Laboratory

- **♦** State of Art R & D Centre
- **♦** Zonal Partner with leadingindia.ai
- **Active Placement Cell**
- **♦** Highly Qualified Teaching Faculty
- **♦** Innovation Lab with Accenture and D-Link Academy
- **E** Yantra Lab with IIT
- **Professional Societies of Students**
- **♦** Big Data Analytics Lab with Cloudera, USA
- Two times Accreditation by National Board of Accreditation in 10 Years for all eligible UG Courses
- 1st RANK in Maharashtra State in "Education World —India's first education-focused Magazine".

(Rankings 2018-19 & 2019-20)

- "One of the best top 10 Private Institutes in India 2018" by "The Knowledge Review Magazine".
- 'Most Promising Engineering College with Excellent Faculty in Maharashtra' award in "Asia Education Summit & Awards 2017".
- Platinum Category in AICTE-CII Industry-linked Technical Institute Survey 2016.
- Five Awards in "Smart India Hackathon 2019".
- Results: 100% in MCA and More than 95 % in B.E. for the last 8 Years.
- Placements: More than 75% Placements for last 8 Years.







VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY

Affiliated to Mumbai University & Approved by A.I.C.T.E



Website: http://vesiac.in/ves

INDEX

1.	ABOUT VIVEKANAND EDUCATION SOCIETY (V.E.S.)	1
2.	GOVERNING BODY	3
3.	VISION AND MISSION	3
4.	COURSES OFFERED.	4
5.	FROM TRUSTEE'S DESK.	5
6.	FROM PRINCIPAL'S DESK	6
7.	FROM VICE-PRINCIPAL'S DESK	9
8.	SCHEME OF STUDY	10
	8.1.1 BATCHELOR OF ENGINEERING (B.E.) (DTE CODE: EN3185)	10
	8.1.2 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME3185)	11
	8.1.3 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME3185)	11
	8.1.4 Ph.D. PROGRAMME	11
9.	GRADING SYSTEM OF THE UNIVERSITY OF MUMBAI.	12
10.	TERM GRANT	12
11.	EXAMINATIONS	12
12.	USING UNFAIR MEANS IN THE EXAMINATIONS	13
13.	ATTENDING LECTURES/LABS	13
14.	INFRASTRUCTURE AND SALIENT FEATURES	13
15.	FEE STRUCTURE	15
16.	ABOUT CAPITATION FEE	16
17.	SPECIAL PROGRAMMES	16
18.	MAJOR LABORATORIES	17
19.	INDUSTRY COLLABORATION	24
20.	RESEARCH AND DEVELOPMENT LAB	26
21.	COMMON INFRASTRUCTURE	27
22.	TEACHING FACULTY	29
23.	STUDENT'S COMMITTEE/ACTIVITIES	29
24.	ACHIEVEMENTS	38
25.	PROHIBITION OF RAGGING.	42
26.	WOMEN DEVELOPMENT CELL	43
27	ANTI SEXUAL HARASSMENT COMMITTEE	43

28. INTERNAL COMPLAINT COMMITTEE	44
29. GRIEVANCE REDRESSAL COMMITTEE	45
30. COMMITTEE FOR SC/ST	45
31. ADMISSION GRIEVANCE COMMITTEE	46
32. CAREER GUIDANCE, TRAINING, PLACEMENT &	
ENTREPRENEURSHIP CELL.	47
33. VESIT ALUMNI ASSOCIATION	49
34. DESCIPLINE.	49
34.1 IDENTITY CARD	49
34.2 LOSS OF I-CARD AND ISSUE OF DUPLICATE I-CARD	
34.3 EX-STUDENT'S I-CARD	50
34.4 LIBRARY AND OFFICE TIMINGS	50
34.5 USE OF SPORTS GROUND.	50
34.6 HOLIDAYS AND VACATIONS	50
34.7 RAILWAY CONCESSION	50
34.8 HOSTEL	50
34.9 SAFETY PRECAUTIONS	51
34.10 LOST AND FOUND.	51
34.11 DRESS CODE.	51
34.12 CLEANLINESS OF PREMISES	51
34.13 INDULGING IN ANTI-SOCIAL ACTIVITIES	51
34.14 CHEWING PAN/TOBACCO AND SMOKING	51
34.15 USE OF ELECTRONIC ITEMS/GADGETS	51
34.16 REFUND OF CAUTION MONEY	51
34.17 LOCKER FACILITY	51
34.18 RETURN OF ORIGINAL CERTIFICATES	52
34.19 NOTICE BOARDS	52
34.20 GRE/GOEFL/GATE/GMAT	52
34.21 CO-CURRICULAR ACTIVITIES	52
34.22 PICNICS AND OUTINGS.	52
34.23 INTERNET USAGE	52
34.24 THEFT	52
34.25 SUGGESTION BOX.	53

34.26 BIRTHDAY CELEBRATIONS	53
34.27 PRINCIPAL'S SIGNATURE	53
ANNEXURE 1	54
ANNEXURE 2	55

1. ABOUT VIVEKANAND EDUCATION SOCIETY



Founder of Vivekanand Education Society



Shri Hashu Advani along with his 10 friends started Vivekanand Education Society (VES) as a small school, in a barrack, which has grown like a banyan tree, with a spread of 26 institutions.

Shri Hashu Advani was the Municipal Corporator, MLA Urban Development Minister and Finance Minister in the Government of Maharashtra.

Philosophy of Shri. Hashu Advani ji & Values of VES

Samarpan Total dedication and devoted to serving Humanity Samdrishti Humanitarian feeling, behavior with equality for all

Vishalta Visionary in pursuit of excellence for a value rich education

Karm Yogi A doer, Actions speaking louder than words

Siddhant Valuing humanity and the importance of education and learning, in building a better

future

Vision

- Increase the intellectual wealth of the country by providing quality education and building excellent technical skills
- Make individuals highly confident, self reliant and truly global citizens
- Provide for holistic development, in terms of Knowledge, Skill and Wisdom, leading to cultural and spiritual integration.

VES Mission – Value Based Education

Develop new generation leaders to bring a positive change in the world using modern education system emanating from India's rich cultural heritage blended with western science!

Educational Facilities Offered

- · Professional, Technical & Higher Education
- Junior & Senior Colleges
- Certificate Level Technical & Vocational Education
- · High School Level Education

- · Primary & Preprimary Level Education
- · Welfare Activities
- · Sports & Fine Arts





Distinctive Essentials

- · Admired for Ethics, Values, Discipline and Excellence in education
- Management board of honorary professionals in different fields. Honorary Associates and Alumni assisting in various activities of the trust.
- · 26 Institution from creche to schools to colleges upto Ph.D. level
- · More than 18,000 students studying every year and over 1200 teaching & non-teaching stuff Staff.
- · No capitation or donation from students or parents
- · There are no Management quota seats in all professional Institutions of VES.
- · All admissions are purely on merit
- 12 buildings in 3 complex's with over 395000 sq.ft of area
- · 3 Large Sports grounds and, Hostel & Staff guarters.
- · Finanacial help to poor & needy students.

Institutions of VES

Professional, Technical & Higher Education Institutions

- V.E.S. Institute of Management Studies and Research
- V.E.S. Institute of Technology
- V.E.S. College of Pharmacy
- V.E.S. Law College
- V.E.S. College of Science, Arts & Commerce
- V.E.S. Leadership Academy & Research Center
- V.E.S. College of Architecture
- V.E.S. Polytechnic

High Schools and Junior Colleges

Swami Vivekanand Jr. College, English Medium

Swami Vivekanand Kanishta Mahavidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, English Medium, Chembur

Swami Vivekanand Vidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, Marathi Medium, Kurla

Certificate Level Technical and

Vocational Institutions

V.E.S. Tulsi Technical Institute, Sindhi Society, Chembur

V.E.S. Catering Craft Center

V.E.S. Nari Shala

Sports and Fine Arts Institutions

V.E.S. Vivekanand Sports Academy

V.E.S. Vivekini

Other Institutions

V.E.S. Creche & Day Care Centre

V.E.S. Vocational Guidance Bureau

V.E.S. Welfare Section

V.E.S. Vivek Sindhi Kendra

Primary & Preprimary Schools

Swami Vivekanand Preprimary & Primary School English Medium, Sindhi Society, Chembur

Swami Vivekanand DBC Pre-Primary & Primary School English Medium, Collectors Colony, Chembur

Swami Vivekanand Prathmik & Purva Prathmik Shala Hindi Medium, Kurla

Swami Vivekanand Pre-Primary & Primary School English Medium, Kurla (E)

Swami Vivekanand Prathmik & Purva Prathmik Shala, Marathi Medium, Kurla

Swami Vivekanand Nursery & Play School, Chembur

2. GOVERNING BODY

VES MANAGING COMMITTEE MEMBERS

1.	Shri. Mahesh Tejwani	(President)
2.	Shri. Amar T. Asrani	(Secretary)
3.	Shri. Suresh Malkani	(Treasurer)
4.	Shri. B. L. Boolani	(Member Trustee)
5.	Smt. P.U. Vasvani	(Member Trustee)
6.	Shri. R.K. Bhagia	(Member Trustee)
7.	Adv. (Shri) Laxman Kanal	(Member Trustee)
8.	Dr. (Shri) Prakash Lulla	(Member Trustee)
9.	Shri. Bansi Wadhwa	(Member Trustee)
10.	Shri. Vishu Lala	(Member Trustee)

VESIT GOVERNING BODY MEMBERS

1.	Shri. Mahesh Tejwani	Chairman
2.	Shri. Amar T. Asrani	Member
3.	Shri. Suresh Malkani	Member
4.	Shri. B. L. Boolani	Member
5.	Dr. (Shri) Prakash Lulla	Spl. Invitee
6.	Shri. Vijay Talreja	Spl. Invitee
7.	Shri. Sunil Khanna	Member
8.	Shri. R. N. Mukhija	Member
9.	Dr. Amit Dutta	AICTE Nominee
10.	Dr. M.D. Desai	AICTE Nominee
11.	Dr. N.R. Gilke	M. U. Nominee
12.	Dr. Abhay Wagh	DTE Nominee
13.	Shri. P. K. Krishnamurthy	State Govt Nominee
14.	Dr. (Mrs.) J.M. Nair	Principal/Secretary
15.	Dr. P. P. Vaidya	Staff Representative
16.	Dr. Gresha Bhatia	Staff Representative

3. VESIT VISION AND MISSION

Vision

To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of socially conscious application of technology.

Mission

- To inculcate culture of value based education.
- To enthuse students to develop in an ambient environment of caring and of sharing information.
- To enable students to work towards excellence in their chosen fields with professional bent of mind.

4. COURSES OFFERED

National Board of Accreditation (NBA) has approved the accreditation of Under-Graduate Programmes. Department of Computer Engineering is accredited for a period of 2 years. Department of Electronics Engineering, Electronics and Telecommunication Engineering, Instrumentation Engineering and Information Technology are accredited for a period of 3 years, after the visit of Expert Committee with effect from 01/07/2016.

Starting Year	Course /Programme /Intake
1984 – 1985	BE Electronics Engineering – 60
	BE Computer Engineering – 60
	BE Instrumentation Engineering – 60
1992 – 1993	BE Electronics and Telecommunication Engineering – 60
1999 - 2000	BE Information Technology – 60
2002 - 2003	ME Electronics and Telecommunication Engineering – 18
2002 - 2003	ME Information Technology – 18
2003 - 2004	ME Instrumentation and Control – 18
2003 - 2004	Master of Computer Applications – 60
	Increase in intake of the existing programmes
2011 - 2012	BE Computer Engineering – 60
2012 - 2013	BE Electronics and Telecommunication Engineering – 60
	BE Electronics Engineering – 60
	BE Computer Engineering – 60
	Master of Computer Applications – 60
	Recognized as Ph.D. Centre for Electronics
2013-2014	Electronics and Telecommunication Engineering – 60(Regular Second Shift)
	(discontinued from 2019-20)
2019-2020	BE Information Technology – 60
	(Sanctioned additional 60 seats)

5. FROM TRUSTEE'S DESK

Research & Development activities are considered as an essential component of technical education because of their role in creating new knowledge and insights. It will also impact excitement and dynamics to the educational process. I, sincerely, hope that all VESIT faculty will concentrate on creating a passion for research activities. I am sure that it will augment your quality of teaching also.



Shri. B.L. Boolani

I take this opportunity to congratulate our students and faculty for their outstanding performance in prestigious competitions such as Smart India Hackathon (SIH) and Avishkar Inter-University Research Convention for their innovative projects. I am also very happy to note that students have suggested technological solutions to help farmers in Smart India Hackathon. I really appreciate that SIH, three VESIT teams won Rs.1 lakh prize money in complex category, one team won Rs.75,000/- prize money in complicated category and one team won Rs.50,000/- prize money in simple category. As an industrialist, I take keen interest in the latest technological developments, during my interaction with the young innovators as Managing Trustee; I talked about the newly instituted Innovation and Research Corpus Fund in the institute. I am very happy that winners have contributed 15% of their prize money of Hackathon voluntarily to this fund.

Innovation is not only about new inventions, it is also about using new and current resources in a more innovative way. As we live in the evolution of a new world, where artificial intelligence, quantum computing and other technologies have far reaching consequences, not just for our economy, but also for our society and culture, it is important that students should work on innovative projects to solve the problems faced by the common man and underprivileged section of the society. I can assure you that VES management will give you all the needed support and encouragement to make your product reach the end users. I have taken personal interest to develop an Incubation Centre for all VES Stakeholders. We invite all of you to come forward and make use of this opportunity to become an entrepreneur. I understand that almost 800 students graduate from VESIT every year. I hope to see at least five entrepreneurs in a year.

Best Wishes to all the graduating students and Hearty Congratulation to the students and staff of VESIT for their achievements and I extend my best wishes for further growth of the institute especially in the areas of research, innovation, application and entrepreneurship.

Shri. B. L. Boolani Managing Trustee

6. FROM PRINCIPAL'S DESK

The year of new A-B-C-D Artificial Intelligence, Block Chain Technology, Cloud Computing and Data Science, was a year of recognition for our institute. University Grants Commission has included VESIT in the list of colleges under Section **2(f) & 12(B) of UGC Act 1956**, which now up scales us to receive Central Assistance. We are proud to announce that VESIT was ranked **FIRST in Maharashtra State for Second**



Dr. (Mrs.) J.M.Nair

consecutive year and 29th among the top 100 Engineering Colleges in India by "Education World –India's first education-focused Magazine" and also been recognized as one of the best top 10 Private Institutes in India 2018, by "Knowledge Review Magazine".

VESIT is proud of our faculty Prof. Asma Parveen I.Siddavatam who won Silver medal in **State**Level Inter University Avishkar Research Convention in the teacher category for her project

"Design and construction of High Resolution Spectroscopy System based on new technique of
Estimation". Mrs. Kadambari Sharma secured bronze medal in engineering and technology stream
in Inter Zonal Level of Avishkar Research Convention conducted by Mumbai University in the
teacher category for her project "A Research Grade Computer Controlled Light Source" and Mrs.
Lekshmi Ajesh secured Gold Medal in Post PG category and represented Mumbai University at
State level for her project "A Novel System for Emulation of Partial Discharges to aid HV
equipment Breakdown Diagnostics".

VESIT has also done remarkably well in **Smart India Hackathon 2019**. Five groups from VESIT were declared winners in Smart India Hackathon for their projects, Digital India – Smart Subsidy System for **Robert Bosch GmbH** (**Engineering Company**), Artificial Intelligence for Farmers for **Rashtriya Chemicals & Fertilizers**, Mobile application to get Regional Connectivity Scheme flight seat availability with details in real time for **Ministry of Civil Aviation**, management of material during Disaster to the beneficiary for **Goonj NGO** and Farm Input Calculation using Artificial Intelligence for **Kisan Forum Pvt. Ltd**.

As new initiative in Academics & Research, VESIT has become one of the **Zonal lead Partners** with **leadingIndia.ai**, a Nationwide Initiative by Bennet University to promote Artificial

Intelligence among students. 30 Artificial Intelligence & Deep learning projects are identified and students are being encouraged to work for foolproof solutions.

TEDx VESIT was organized on 18th July 2018. Mr. Vibhas Sen International Wheelchair fencer, Mrs. Ridhi Parekh, professional Photographer, Mr. Debi Goenka, Environmental activist, Mrs. Parul Tank, Professional Psychotherapist and Mrs. Sindhutai Sakpal, Social activist were the speakers for the event.

We are very proud to announce that VESIT was recognized as "Outstanding College 2018-19 by Mastek Company in the season 4 of their Deep Blue Competition. In the election year, our students have built a remote voting application using Block Chain Technology. Their app based product won first prize in the intercollegiate Hackathon conducted by TSEC. In the Hack M fintech event sponsored by Deloitte, our student's project on "Multilingual Farmer Android App" an Expert Dashboard website and KrushiDost — Farmer's Friend a digital solution to support farmers by providing on-demand labour and technology devices won prizes. In the competition held by Honeywell, our students won the first prize for their project on solving the problems for stakeholders using Artificial Intelligence on edge devices.

Our students fared very well in university exams this year. The pass percentage of last academic year 2017-18 of B.E Electronics, Electronics & Telecommunication and Information Technology department has been 98.17, 98.58 and 98.72 respectively while B.E Computer, Instrumentation and MCA secured 100% result.

Our alumni meet was held on 15th December 2018. The distinguished Alumni awards were received by Mr. Amit Rambhia, Chairman, Managing Director and Executive Director at Panache Digilife Limited and Mr. Vivek Arora, Global Head-Strategic Deals Sutherland Global Services.

This year placement record was very impressive with 73.23% of eligible students getting placed in various companies. Dream companies offered a record 110 placements. JP Morgan has selected 23 students this time with 11 lakh/annum. Nomura has selected seven students this year. Accenture continued as our day one recruiter selecting 294 students. 34 students were selected by Cap Gemini. TCS selected 66 students. BNP Paribas and L&T ITL selected 22 students. The newly instituted Special Recruitment Cell catering to the students who are not able to participate in regular placement drive, placed 12 students of 2018-19 batch in various industries.

As the institute is gearing towards working in autonomy mode, we are planning to give more emphasis to Research, Innovation, Application and Entrepreneurship. We are glad that our institute was selected as **Institute Innovation Council** by AICTE to nurture activities on Entrepreneurship and Innovation. Our parent society is planning to set up full-fledged **Incubation Centre** in the

near future. We expect that all our students, who have wonderful innovative ideas and has potential for startups will come forward. VESIT is ready to handhold each one of you to successfully launch your dream idea. We invite all students to make use of this opportunity and become job creators. This is a glimpse of activities conducted in this academic year. Let us all work collectively towards achieving more of what has already been achieved.

"Just because something doesn't do what you planned it to do doesn't mean it's useless".

-Thomas Edison

7. FROM VICE-PRINCIPAL'S DESK

In the age of Digital Transformation, technology has been adapting to the social mind map, which is a positive step towards making society better. Organizations have to embrace new emerging technologies such as the Internet of Things, Artificial Intelligence, Robotics, Cloud, Blockchain, Augmented Reality and Virtual Reality.



Dr. (Mrs.) Vijayalakshmi

Alongside large firms, start-ups too have mushroomed

and contributed significantly towards the growth and adoption of emerging technologies in India.

Further initiatives of the government, aimed at economic galvanization and inclusion, such as Make in India, Digital India, and Skill India, have begun to look at and leverage the above emerging technologies to create efficiencies and increase reach across to provide societal benefits across all sections of society.

We at VESIT can proudly state that our team of faculty and students are at the forefront of such endeavour. A value-added course like **leadingIndia.ai**, **AIA**, **Dlink** and **Cloudera** helps add skill sets to the students to improve employability. Further **Summer** *and* **Winter** schools in current technology areas keep the students abreast with significant trends in technology.

A plethora of **guest lectures** from industries further helps the industry and our students in gaining useful employability skills.

It is a moment of pride that we have added on to the winning history of **VESIT**, 5 out of 8 teams have won the Grand Finale of **Smart India Hackathon** 2019.

Diving deeper VESIT is organizing Hackathon, initiated by Leading India AI, **vesAIthon'19**, to make an impact on the community by building a workable AI solution for most pressing social challenges faced by senior citizens, differently abled and kids.

Wishing you all the very best. "May you live in Technology Enriching Times"

"Challenges are what make life interesting and overcoming them is what makes life meaningful".

-Joshua J. Marine

8. SCHEME OF STUDY

8.1.1 BACHELOR OF ENGINEERING (B.E.) (DTE CODE: EN3185)

The Bachelor of Engineering is a four year course, which is divided into eight semesters. The first year (F.E.) course spreads over two semesters dealing with general engineering, common to all branches of engineering. The basic objective is to give elementary foundation needed in different aspects of engineering.

During the second year (S.E.) and third year (T.E.), students gain basic knowledge in their chosen branches. The final year i.e. fourth year (B.E.) is designed to strengthen the specialized skills which help the students to apply the concepts studied earlier by participating in project work. The subjects and the syllabus are as prescribed by the University of Mumbai.

Approved Intake for Academic Year 2019-20

Regular First Shift	<u>Intake</u>
B.E Electronics Engineering	120
B.E Computer Engineering	120
B.E Instrumentation Engineering	60
B.E Electronics and Telecommunication	120
B.E Information Technology	120
Regular Second Shift	Intake
B.E Computer Engineering	60

Eligibility: - As per DTE, Mumbai University and State Government Rules.

Note: -100% admissions will be done **through CAP** (Centralized Admission Process) conducted by DTE (Directorate of Technical Education).

8.1.2 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME3185)

The M.E. Program comprises of four semesters with duration of two years. The courses are designed/prescribed by the University of Mumbai.

The numbers of seats offered are as mentioned below:

Approved Intake for Academic Year 2019-20

Regular First Shift	<u>Intake</u>
M.E Electronics and Telecommunication.	18
M.E Instrumentation and Control	18
M.E Information Technology	18

Eligibility: - As per DTE, Mumbai University and State Government Rules.

Note: -100% admissions will be done through CAP (Centralized Admission Process) conducted by DTE (Directorate of Technical Education).

8.1.3 MASTER OF COMPUTER ENGINEERING (M.C.A) (DTE CODE: MCA3185)

Master of Computer Application (M.C.A) is a full time three year course (six semesters). The course is conducted as per the syllabus laid down by the Mumbai University. Examinations are conducted by the University for all the six semesters and the degree is also awarded by the University of Mumbai.

Approved Intake for Academic Year 2019-20 Intake

MCA -	Regular First Shift	60
MCA -	Regular Second Shift	60

Eligibility: - As per DTE, Mumbai University and State Government Rules.

Note: -100% admissions will be done **through CAP** (Centralized Admission Process) conducted by DTE (Directorate of Technical Education)

8.1.4 Ph.D. PROGRAMME

VESIT has always given importance to the experimental way of teaching and learning of engineering concepts which can only be perfected through the best research and development practices. VESIT offers Ph.D. course in Electronics Engineering. VESIT has well equipped R&D Laboratory which is utilized by P.G. and Ph.D. students.

9. GRADING SYSTEM OF THE UNIVERSITY OF MUMBAI

Mumbai University introduced Choice Based System from the academic year 2016-17. All information about pattern and syllabus is available on Mumbai University's official web site www.mu.ac.in Salient Features:

- ✓ Final grade for B.E. Degree will be based on the average of all eight semesters. So each semester/each subject is important.
- ✓ 75% attendance is compulsory.
- ✓ Journal submission must be done on or before every term end.
- ✓ Two tests will be conducted according to the given model by the University for Internal Assessment of each subject. The average of the two scores of internal assessment will be considered as the final grade in the subject and will be printed in the final mark sheet. No re-test shall be held.
- ✓ Failing in eight heads in a year is the maximum limit allowed in the ATKT system. (Maximum five theory exams and remaining Practical/Oral/Internal etc.)
- ✓ Failure to submit the journal in any subject (even for a single subject) will result in detention (i.e. loss of one year). Admission for the next academic year is to be taken by repayment of fees.

10. TERM GRANT

Term Sanctioning (allowing the students to appear for the University examination) entirely depends upon the following: -

- Minimum of 75% attendance in all subjects.
- Satisfactory completion of all term work.
- Practical and Assignments as specified in Syllabus or as instructed within prescribed periods.
- General behavior of students, both inside and outside the classroom.

11. EXAMINATIONS

Every semester, the University of Mumbai conducts end semester examinations and two internal class tests are conducted by the college in every subject. The performance in these tests is considered for grade calculations as per the MU circular. The submission of term work in each subject should be taken very seriously. If a student fails to submit his/her Term or Sessional Work before the prescribed date, he/she will not be allowed to sit for the concerned examination.

12. USING UNFAIR MEANS IN THE EXAMINATIONS

During an examination, if a student is found either possessing any material (written or printed) with the intention of copying or actually copying, he/she is liable to be severely punished according to the rules laid out by the University. In such circumstances, a student may either be debarred from appearing for the entire examination or expelled from the University of Mumbai for a minimum period of three years as per the decision of "Unfair - Means Committee".

13. ATTENDING LECTURES/LABS

- 1. Students coming late to the Institute will not be allowed to enter the premises.
- **2.** Bunking classes is strictly forbidden. If the student misses even one period, he/she may be marked absent for the whole day.
- 3. In order to monitor the academic progress as well as the overall development of our students, we nominate one staff member as faculty advisor to whom a student can approach for guidance. For day to day functioning, class teachers and class counsellors are associated with each class. The students as well as their parents are welcome to contact either the class teacher/class counsellor or the faculty advisor any time during the session.
- **4.** While the lecture or the lab is in progress, a student is supposed to keep his/her mobile phone switched off, failing to which severe punishment can be given.
- **5.** After the classes are over, the students should leave the college premises in a proper disciplined manner, without making any noise or crowding in the corridors.

14. INFRASTRUCTURE AND SALIENT FEATURES

The VESIT campus strikes a balance between modern facilities and natural surroundings to create an atmosphere that is ideal for students to learn, explore, and grow.

- ✓ Sprawling Complex
- ✓ Equipped Library having more than 48000 technical books
- √ Professional Societies
- ✓ Active Placement Cell
- ✓ Strong Alumni Association with Alumni Web Portal
- ✓ Conference Hall
- ✓ Seminar Halls
- √ Auditorium
- √ Music Room
- √ Huge Canteen
- √ Inviting Playground

- √ Amphi Theatre
- √ Recreation Room

Labs set up in collaboration with industry

- √ Automation Industry Association Lab
- ✓ Cloudera Big Data Analytics Lab, U.S.A.
- ✓ National Instruments Labview Academy, U.S.A.
- ✓ D Link Academy, National Instruments (U.S.A.), Bangalore
- ✓ e-Yantra-MHRD-IITB initiative

15. FEE STRUCTURE

V E S INSTITUTE OF TECHNOLOGY Fees Structures for Academic Year 2019 - 20 for - FIRST YEAR - BACHELOR OF ENGINEERING

9	General Quota		OBC / EWS / SEBC		SC/ST/VJ/DT/NT/SBC		TFWS		J&K/	
Particulars of Fees	Maharashtra Board	Other University / Boards	GOI	J&K PMSSS						
Tuition Fees	93777	93777	46889	46889	0	0	0	0	15000	93777
Development Fees	10223	10223	10223	10223	10223	10223	10223	10223	5000	10223
Caution Money	1000	1000	1000	1000	1000	1000	1000	1000	1000	1000
Gymkhana / Other University Fees	546	546	546	546	546	546	546	546	4046	546
University Exam Fees	2037	2037	2037	2037	2037	2037	2037	2037	2037	2037
Enrollment Fees	200	200	200	200	200	200	200	200	200	200
Yuvaraksha Insurance	100	100	100	100	100	100	100	100	100	100
Eligibility Fees	0	300	0	300	0	300	0	300	300	300
Grand Total	107883	108183	60995	61295	14106	14406	14106	14406	27683	108183

V E S INSTITUTE OF TECHNOLOGY Fees Structures for Academic Year 2019 - 20 for - FIRST YEAR - MASTER OF ENGINEERING

Particulars of Fees	General - N	faharashtra	General	SC – Maharashtra (applicable for annual income below 2 lakhs)		
	Mumbai University	Other University / Boards	Other than Maharashtra	Mumbai University	Other University / Boards	
Tuition Fees	83633	83633	83633	0	0	
Development Fees	9367	9367	9367	9367	9367	
Caution Money	1000	1000	1000	1000	1000	
Gymkhana / Other University Fees	546	546	546	546	546	
Yuvaraksha Insurance	100	100	100	100	100	
University Exam fees (SEM I & II)	3007	3007	3007	3007	3007	
Enrollment Fees	0	200	200	0	200	
Eligibility Fees	0	200	300	0	200	
PG Registration	825	825	825	825	825	
Grand Total	98478	98878	98978	14845	15245	

V E S INSTITUTE OF TECHNOLOGY Fees Structures for Academic Year 2019 - 20 for - FIRST YEAR - MASTER OF COMPUTER APPLICATION

	90478	90878	52259	023	025	14439	90978	28978
PG Registration	825	825	825	825	825	825	825	825
Eligibility Fees	0	200	0	200	0	200	300	300
Enrollment Fees	0	200	0	200	0	200	200	200
University Exam Fees (SEM I & II)	3007	3007	3007	3007	3007	3007	3007	3007
Yuvaraksha Insurance	100	100	100	100	100	100	100	100
Gymkhana / Other University Fees	546	546	546	546	546	546	546	4046
Caution Money	1000	1000	1000	1000	1000	1000	1000	500
Development Fees	8561	8561	8561	8561	8561	8561	8561	5000
Tuition Fees	76349	76349	38220	38220	0	0	76349	15000
Particulars of Fees	Mumbai University	Other University / Boards	Mumbai University	Other University / Boards	Mumbai University	Other University / Boards	Other than Maharashtra	J & K / GOI and NEUT
	General - Maharashtra		OBC / EWS / SEBC Maharashtra		SC/ST/VJ/DT/NT/SBC Maharashtra		General	7.0 12 / 0.01

16. ABOUT CAPITATION FEE

INSTITUTE DO NOT ACCEPT ANY TYPE OF CAPITATION.

Refer to Anti Capitation Fee Act available on our college website: http://vesit.ves.ac.in or http://vesit.ves.ac.in or http://vesit.ves.ac.in or

17. SPECIAL PROGRAMMES

Looking Beyond Syllabus

Looking beyond syllabus was launched in the year 2011, with an aim to expose the students to evolving technologies and opportunities and to work as teams on multidisciplinary projects under the mentorship of a faculty member assigned to them, in their area of interest/passion that may not necessarily be their stream of study.

The main objective of looking beyond syllabus of technical education is to infuse prospective technocrats with the ability to apply basic science and experimentation skills, to understand problems in the industry, academia and humanity at large and also be able to develop practical solutions. It also aims to model them into wholesome professionals in the field of Technology. Looking beyond syllabus also prepares the students to use their skills with a strong base for higher education and acquisition of knowledge to sustain a life-long career in related areas.

Awakening the Scientist

Awakening the Scientist is an opportunity given to the First year students to take the first step in the transformation of a student into a professional. It is a group activity. The objective is to identify the science, engineering and technology concomitant with gadgets, equipment and tools used in day to day life and also to relate the past, present and future of them.

Skill Enhancement Lectures

VESIT believes in giving its students an invigorating and vitalising academic environment. The syllabus of Mumbai University is taught very efficaciously by our faculty. An added edge is given to our students by inviting connoisseurs from the industry and research institutions. This endeavor is termed as 'Skill Enhancement Lectures'.

18. MAJOR LABORATORIES

18.1 Department of Electronics Engineering

1. Basic Electrical Engineering Lab

Laboratory courses conducted: - Basic Electrical and Electronics Engineering, Electronic Instruments and Measurements, Circuit Theory. This Lab is equipped with 1-phase and 3-phase Motors, different types of Transformers, A.C. and D.C. Generators, Motors, Trainer kits for network theorems, analog and digital measuring instruments etc.





2. VLSI Design and Communication Lab

Laboratory courses conducted: -VLSI Design, CMOS VLSI, IC Technology. The department is a member of Cadence University Program; subscribed to cadence VLSI design suite and microwind running on Linux clients. Also in this lab all analog and digital communication kits are available for experiments of subjects Digital Communication, Principles of Communication Engineering.





3. Basic Electronics Circuit Lab

Laboratory courses conducted: - Basic of Electronic Circuits, Electrical Network Analysis and Synthesis, Electronic Devices and Circuits. This lab is equipped with sufficient number of CRO, Function Generator, DMM etc. Softwares required for design and simulation of electronic circuits are also available.





4. Digital Circuit Design Lab

Laboratory courses conducted: - Digital Circuit Design, Digital System Design, Design with Linear Integrated Circuits, Computer Communication Network, Advance Networking Technology.

Lab is equipped with softwares for digital and analog circuit simulation, FPGA & CPLD training kits, Digital measuring instruments, Advanced Networking Technology etc. This Lab also extends support to the final year students for their Project Work.



5. Advanced Microprocessor and Embedded System Lab

Laboratory courses conducted: - Microcontroller and Applications, Microprocessor and Application, Embedded System Design, MEMS Technology, etc. The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. All the electronic testing



equipment are available, Digital Trainer Kits and Demonstration kits (RAM & ROM), Universal Programmer, Cards for ISA, PCI and different connectors are kept for better understanding of computer architecture. Also lab is equipped with 8085, 8051 and 8086 boards along with ARM Cortex M based Microcontroller boards.

6. Power Electronics Lab

Laboratory courses on Industrial and Power Electronics are conducted in this lab. The Lab is adequately equipped with Test equipment for Power Electronic Devices & circuits, various demonstration kits & measuring instruments are available in this lab.



7. Electronics Workshop – PCB lab

Laboratory courses conducted: - Electronics Workshop, PCB Workshop. Lab Facilities – PCB Exposer, Film Exposer, Dip Coat Machine, Etching Tank, Drilling Machine etc. Students perform their Electronic Workshop project designing, testing and implementation in this lab. This lab is equipped with all the facilities required for PCB fabrication.



8. Project Lab

This lab is dedicated for various Project work for Second, Third and Final Year students. This facility is exclusive for projects so that students should learn various subjects through Project Based learning. This lab is fully equipped with hardware, software, debugging & testing tools required for Electronics Project.

9. IOT lab

IOT lab is intended to provide all latest IOT component for related subjects. Also this lab is equipped with IOT components such as Arduino, Raspberry Pi, WiFi Model, Zigbee model and IOT simulation software.

10. Robotics and AI Lab

It is a state of art facility to create awareness about various Robotics Technologies and Artificial Intelligence fields. This lab is equipped with 3D printer, Humanoid, mobile robots. This lab will nurture the environment of robotics through various Projects. This lab is fully equipped with required resources for students as well as researchers.

18.2 Department of Electronics and Telecommunications Engineering

1. Analog & Digital Communication

Laboratory courses conducted: - Principle of Communication Engineering, Digital Communication, Mobile Communication Systems & other analog & digital communication related subjects. Lab is equipped with DSO-(Agilent, Aplab, Tektronics, Rigon), Spectrum Analyzer-3GHz (Agilent, Hameg), Signal Generators-



AM/FM,

Computers with latest configuration etc. COMMSIM: - Network & Communication Simulation Software, Digital Communication Trainer kits like 16-QAM, QPSK, PSK, TDMD, Error correcting codes etc.

2. Microwave & Advanced Communication

Laboratory courses conducted: - RF Circuit Design, Microwave Devices and Circuits, Computer Communication Networks, Electromagnetic wave Theory etc. Lab is equipped with DSO-(Agilent, Aplab, Tektronics, Rigon), Spectrum Analyzer-3GHz (Agilent, Hameg), Standard Signal Generators for RF-range, Network Analyzer (Agilent), Transmission Line Trainer, Computers with latest configuration etc. Software like MATLAB, Ansoft, PCADD are used simulation.

3. Network & Wireless Communication

Laboratory courses conducted: Discrete Time Signal Processing, Computer Communication Networks, Image Processing, Wireless Communication, ICE etc. This LAB consists of 25-PCs with latest configuration, server, Wireless LAN, PDA's & Mobile phones for experimental purpose. All computers are installed with necessary simulation softwares.

4. Optical Fiber Communication

Laboratory courses conducted: - Optical Fiber Communication, MOC. This Lab also extends support to the final year students for their Project Work. The lab consists of the latest equipments/ softwares like OTDR, Optiwave, and Trainer kits for calculation of IV Characteristics, WDM, Chromatic dispersion analog & digital signal transmission, 1550nm & 1310nm optical sources, etc.

5. T.V. and Satellite Communication

This lab supports conducting practicals & some project work for SE to BE students. The practicals conducted for the subjects like TV and Video Engineering, Satellite Communication, Antenna and Wave Propagation, RADAR etc. equipments like different TV models, B/W & color pattern generators, Satellite communication module, and complete setup of cable- TV & CCTV etc.

18.3 Department of Computer Engineering

All the computer laboratory are spatial and well equipped with the latest configuration and well networked Machines.

1. Database Management Systems

A well-equipped lab comprising of latest computers installed with various Database tools & Servers like Microsoft SQL Server 2005/08 & MS Access (under dreamsparx license), Oracle 11g, Visual Studio 08/10., Netbeans etc. The courses conducted are Distributed databases, Operating Systems and Systems Designing.

2. Web Engineering & Multimedia

A dedicated lab that caters to requirements of Web Engineering & Multimedia with latest softwares like Maya 9.0, Adobe Macromedia Studio, Macromedia Flash, Dreamweaver, Autodesk 8.0 & Fireworks. The Lab typically caters to second year and third year computer students.

Under (MODROBs grant – Ref No 12/AICTE/RIFD/MOD (Policy4) Pvt109/201213), this specialized lab is an extension of the Web Engineering and Multimedia Lab which is high end lab and includes 3-Apple Machines, 2 Aakash Tablets, 1 iPad and 1 Interactive kiosk under the AICTE MODROB Grant.

3. Microprocessor and Robotics



This lab is equipped with the Trainer kits of Microprocessor 8085 & Advanced 8086 Trainer Kits. Several interfacing kits that aid Robotics and i-Bot trainer kits are provided. Also houses computers with tools such as TASM & MASM. The courses conducted are Microprocessor, Microcontroller and Artificial Intelligence.

4. Networking

Aims to provide an environment that aids in effective learning of Networking & Mobile Computing concepts. Systems on the Linux Platform and several development toolkits for mobile computing are some of the features. It consists of Packet Tracer, NetSim Academic, Sun Java Wireless Toolkit, Visual Paradigm. The courses conducted are Computer Networks, Cryptography and System security, Networking Programming Laboratory.

5. Object Oriented and Programming

To aid in proper understanding of System Design and programming Concepts, this lab houses computers with several system design tools like Visual Paradigm, Rational Rose, MS access, MS SQL Server 2005 and Eclipse Ganymede Arch Studio are some of the several software estimation tools. The courses conducted are Object Oriented Programming Methodology and Web Technology.

6. Specialized Lab – Cloud Computing Lab

This new Lab caters to the final year course and has opensource softwares like Open stack, OwnCloud, XEN and KVM also VirtualBox. Also the resources are used for various projects.

7. Specialized Lab – Machine Learning & Big Data Analytics Lab

This specialized lab is for final year Elective courses Big data analytics and machine learning. It is equipped with latest high end computers. Cloudera distribution for Apache Hadoop version 4.4.1 which comprises of Hadoop, Hive, Pig, Hbase is used along with other opensource tools like R, Mahout.

8. Project Lab

This lab has been specially provided for the students to perform their project work with several advanced computers with latest softwares like MATLAB, Visual Studio 2010.

18.4 Department of Instrumentation Engineering

1. Process Automation Lab

Process Automation Laboratory is Equipped with Latest Technology Control systems including PLC'S with simulators along with SCADA software & DELTA V Distributed Control system from well-known industry brands like Allen Bradley & Emersion Process Management. This Laboratory facilitates students to learn Basic PLC Programming, PLC Panels and simulators, DCS Features, DCS based Control systems etc.

2. Process Instrumentation Lab

Process Instrumentation Laboratory is a state of the Art Experimental Facility made up of 14 pilot plant set ups. This was designed & developed, which enable students to identify the instruments & Control systems as well as to provide demonstration of working instruments to the students. This laboratory is utilised to study and understand various types of Processes and Role of Instrumentation involved in the same.



It also creates an Industrial Environment for the students to grow them with the Instrumentation utilization. Students can perform various supporting Instrumentation activities like Calibration, Tuning, Mounting, Trouble Shooting, Loop Testing, checking which provide them Hands on Exposure to make them comfortable and answerable for their campus preparation. Laboratory consists of various Field Instruments like SMART Transmitters, Flow meters & Control Valves along with accessories. It is also equipped with Single Loop PID Controllers & Variable Frequency Drives.

3. Transducers & Sensors Lab

This is one of the Fundamental Labs which facilitates students to learn various sensors & Transducers used in Process & Factory Environment. Students can learn Testing & Performance characteristics using Calibrators. It also provides Hydraulic set up that allows studying Flow Measurement Experiments, Level Monitoring set up using various Instruments like Rota meter, Orifice / venture meters & ultrasonic Level Transmitter. Lab also has Dead weight Testers & Master Pressure Gauge to provide Testing of Pressure Gauges.

4. Biomedical /Analytical /Nuclear Instrumentation Lab

This Laboratory has been established to familiarize the students with capturing, analyzing and processing different Bio signals. Biopac is an integrated instrument which the students use for such experiments. Physiology Lab kits and digital temperature feedback system are also used for training the students in the area of Biomedical Instrumentation.

5. Control Systems & Digital Signal Processing Lab

The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. All the electronic testing equipments are available, Digital Trainer Kits and Demonstration kits (RAM & ROM), Universal Programmer, Cards for ISA, PCI and different connectors are kept for better understanding of computer architecture.

6. Analog & Digital Lab

Laboratory courses on Analog 7 Digital Electronics are conducted in this lab. The Lab is adequately equipped with Test equipments for Electronic Devices & circuits, various demonstration kits & measuring instruments are available in this lab.

18.5 Department of Information Technology

Information Technology department aims to create a high quality academic and research environment, which empowers faculty and students to attain the highest level of excellence as professionals in the field of IT. To achieve its aim, the Department has seven well equipped and spacious Computer Laboratories with more than 250 networked computers and support equipment like Servers, Printers etc.



The laboratories are installed with all the supporting software required to fulfill university curriculum including Free and Open Source Software and other proprietary software such as MSDN DreamSpark, SPSS, Microsoft Windows Compute Cluster Server 2003, Microsoft Windows Compute Cluster ED, Adobe CS3 Design Premium, IBM Rational Rose, Qualnet Software, IBM Cognos BI, Matlab, MS-Office Professional.



The department's labs cater to the following domains: Big data analytics, Data mining, Web and Mobile application development, Artificial Intelligence and Soft computing, Cloud computing, Networking and Security. These labs are used for performing lab assignments for various subjects like Oracle, SQL, Database Management Systems, Advanced Database Management Systems, Data Mining and Business Intelligence,

Big Data Analytics, Web Programming, Software Engineering and Project Management, App Development, Networking, Linux – Open source OS, System Security, Artificial intelligence, Internet of Things- IoT Technology. The department has specialized lab like:-.

IoT lab: One of the labs is used for IoT projects with adequate number of components.

Project lab: A labs that is exclusively reserved for final year students with high end machines to work on their projects.

18.6 Department of Master of Computer Applications

1. Programming Lab

Laboratory courses conduct:-Programing Concepts and C++, Programming and Data Structure, Computer Graphics and Image Processing, Object Oriented Concepts through Java, Web Applications through Servlets, JSP,EJB etc. Lab is equipped with thirty machines. & nbsp. Lab is equipped with thirty Machines with i5 configuration.

2. Database Lab

Laboratory courses conducted: - Database Managements Systems with SQL and database Programming with PL/SQL, Parallel and Distributed database, Data warehousing and Mining Concepts, Software Testing, etc. Lab is equipped with thirty Machines with i3 and i5 configuration etc. Software: Oracle11g, Weka7, Visual Paradigm etc.

3. Web Technology Lab

Laboratory courses conducted: - Computer Network Lab, Web Technology, .NET, Mobile Application, User Experience Design (UXD).Lab is equipped with thirty Machines with i5 configuration. Software: Android studio, Visual Studio 15 prof., SQL SERVER 2008 R2, Cisco packet Tracer, Visual Studio 2010, etc.

4. Software Engineering & Project Lab

Laboratory courses conducted: - Software Engineering, Software Testing Unified Modeling Language lab and Summer project. The lab consist of Machines with i5 configuration softwares; SQL SERVER 2008 R2, Cisco packet Tracer, Visual Studio 2010, MS Project 2007, Visual Paradigm, etc.



19. INDUSTRY COLLABORATION

1. AIA (Automation Industry Association): VESIT is the only technical institution in Mumbai, which is authorized by AIA (Automation Industry Association) to be a 'Competency Development Centre' for conducting course on "Industry Automation" as a part of its Campus Connect Initiative. The newly developed Integrated Automation Lab was inaugurated by Padmashri Dr. G. D. Yadav, Director, Institute of Chemical Technology, Mumbai on 2 March 2016.



System Demonstration by Mr. Prasad Raverkar, Siemens LTD

- 2. NI LabVIEW Academy: V.E.S. Institute of Technology has started NI LabVIEW Academy in collaboration with National Instruments (USA), Bengaluru on 9 July, 2013. This is the first of its kind in Mumbai region. LabVIEW is a graphical programming software which provides comprehensive tools that are required for any type of test, measurement or control application in dramatically less time. After completing this course, the participants will be able to appear for CLAD (Certified LabVIEW Associate Developer) examination, conducted by National Instruments (NI), USA. The CLAD distinguishes basic proficiency in using NI LabVIEW to develop applications. The certification and course work conducted by academy is useful for developing expertise in the LabVIEW environment for career advancement and employment opportunities. NI LabVIEW Academy will provide strong platform for interaction with industries and R & D centres to undertake research projects. Under this academy, training programmes are conducted for UG and PG Engineering staff and students.
- **3. e-Yantra Lab Setup Initiative (eLSI):** V.E.S. Institute of Technology is equipped with state of art facility for Robotics, e-Yantra Robotic Lab under eLSI (e-Yantra Lab Setup Initiative). e-Yantra Lab Setup Initiative (eLSI) is an initiative by the IIT-Bombay under which colleges are encouraged to set up robotics labs. It is designed as a scalable and sustainable approach that addresses infrastructure creation and teacher training to create an eco-system at the colleges to impart effective engineering education. This facility is availed to the students, so that they can focus on embedded system, robotics projects.



- e-Yantra Lab is equipped with Firebird V , Spark V , Qu-Bot robots, various hardware, software platforms required to build develop embedded projects. Every year 20-25 teams of students participate in Robotic competition.
- **4. Accenture Lab:** "VESIT-Accenture Innovation Centre" is a specialized lab developed in accordance with VESIT's MoU with Accenture. This lab is used by Accenture to conduct their Head Start Foundation Program (HSFP) for students selected through campus placement, faculty development program and other similar activities.
- **5. Cloudera Lab:** The Big Data Analytics lab in collaboration with Cloudera provides a high end distributed environment for Big Data processing, through which training is provided in specialized area 'Hadoop' and projects are undertaken to apply the concepts using Cloudera tools. A six week course is regularly conducted for the final year students.
- **6. VESIT D-link Academy Lab:** D-Link academy at VESIT started on 1 April 2015. This academy trains students in batches which will



Cloudera Lab

equip them for better placement opportunities. As a part of this venture, a specialized laboratory is set up in EXTC Department for wireless communication. Under this academy, our faculty members were trained by D-Link to conduct this course for our students. This is an excellent opportunity for our students to get industry certification in their own campus.

- **7. Microsoft Certification:** VESIT has recently signed up an MOU with ATS learning solutions for facilitating Microsoft certifications and arranging short term skill development workshops on Cross Platform Mobile Application Development, Internet of Things, and Cyber Security.
- **8. LeadingIndia:** As a new initiative in Academics and Research, VESIT has become one of the Zonal Lead Partner with LeadingIndia.ai, Nationwide Initiative. The mission of this initiative is to promote Deep Learning and AI skills mainstream in India, to fulfill the trilateral needs of entrepreneurship, Industry-academia partnership, and application-inspired Engineering Research. 30 Artificial Intelligence & Deep learning projects are identified and students are being encouraged to work for foolproof solutions.





Deep Learning and AI workshop

20. RESEARCH AND DEVELOPMENT LAB

R&D Department undertakes various R&D projects from industries and works on various research problems for M.E and Ph.D. students in VESIT.

Some of the successfully completed R&D Projects are:

- > "Electronic Radioactivity Simulator" in collaboration with BARC, which was funded by BRNS.
- "Analysis and Monitoring of Degradation of Insulation in H V Transformers" in collaboration with L&T
- > "Nuclear Detector Characterization" in collaboration with I.I.T, Mumbai.
- ➤ "Sensor network for Agricultural application" in collaboration with TCS and "Low level signal generation" signal processing circuit for Rogowski coil based integrator with low time constant in collaboration with L&T.
- Low level sensing (from 2mA onwards) in industrial environment, DC level shift correction for sensing true AC signal, Peak detection of low level signals in frequency range of 5KHz to 25KHz, High Speed Time to Amplitude Converter and Leakage detection using Fibre Optics with L&T.
- ➤ Simulation of shielding characteristics using GEANT4 (Software for simulation of nuclear data).

Our students are carrying out research work in the areas of:

➤ Low Level Signal Processing systems

- ➤ Reconfigurable ADCs
- Optimized Nuclear Spectroscopy System
- Research in New Techniques for on-line monitoring insulation degradation
- > Research for changes in characteristics of optical fibres with changes in various Parameters like temperature, vibration etc.
- Universal Research Grade Biomedical Signal processing system
- ➤ Timing spectroscopy systems
- > Application of Kalman Filter to Nuclear Engineering Data and Bayesian Estimation of Nuclear Engineering Data
- Nuclear data evaluation for 241Am using Kalman Filter Technique
- Design and construction of precision and sliding pulse generator

21. COMMON INFRASTRUCTURE

LIBRARY

Our library has more than 48,000 books and journals of national and international level. The number of standard journals / magazines: Journals – 30, Magazines-60, e-journals - 1121. Library also provides:

- Internet and all e-journal access through college campus as well as off campus
- Open access facility for all
- ➤ IIT Library Membership
- Membership with NDL
- Best User facility for student
- ➤ NPTEL lecture facilities
- ➤ Best user get additional book facility

Along with technical updates via latest journals, students can also keep themselves abreast with the latest happenings in the world via newspaper, magazines and internet facility. The library is divided into two sections:

Reference Books: - This section stocks recent editions of textbooks. Student can borrow one book at a time against their Library Identity Card. The books/journals in this section are meant for reading in the hall only. The previous year question papers of Mumbai University are also available in this section.

Lending Section: - Students can borrow two textbooks for one week from this section. A fine will be charged if the book is not returned within the given period.

Students are required to abide by the following rules of the Library. Any violation of these rules will lead to a fine or punishment by the Principal.

- a. Every student must submit his/her valid identity at the entrance of the library and reading room. Exstudents of the college, who are members of Alumni Association, can also use the library facilities.
- b. Pencil or ink marking, writing on the pages or any other way of mutilating the books will be treated as library indiscipline. The reader will be held responsible until or unless he/she points out to the Librarian at the time of issue of the book.

- c. The reference books, journals, question paper sets, dictionaries etc. will be issued for reference in the Reading Room only and not to be taken out of the Reading Room.
- d. Students must refrain from disturbing other members in Library and Reading Hall.
- e. The student should inform the Librarian immediately of any loss or damage to the books borrowed by him/her on the library card.
- f. Any other matter not covered by these Rules will rest at the absolute discretion of the Principal and Librarian.
- g. Student should not bring any valuable article to the Library. If such things are lost, the Library staff cannot be held responsible for it.
- h. Students should maintain complete silence in the Reading Hall.
- i. Students should contact the Librarian if they face any problem regarding usage of library facilities. They should not argue with the staff on duty.





OTHER FACILITIES:

1. PROJECT ROOM

Separate Project Rooms are provided for each department with the laboratory facilities and computers installed so that the students can carry out their project work even after the college hours.

2. CANTEEN

The college has a huge canteen, with modern kitchen facility, serving fresh and hygienic food which offers a multi-cuisine variety.

3. AUDITORIUM

Seminars and Students' presentations are held in the Conference Hall, which has a seating capacity of 250 students. It is equipped with a good sound system along with video and data projectors.

4. MUSIC ROOM

Music Room is well equipped with instruments such as Flute, Violin, Guitar, Tabla etc. Students can learn and practice after the college hours. Student Council wing "Bliss" (Beauty lies in Soulful Symphony) is incharge of Music Room and conducts music competitions and workshops for the benefit of students.

5. SPORTS

Life of an engineer is incomplete without any extracurricular activities. For a healthy body and mind, apart from the usual workload and stress, VESIT provides its students with a sporting extravaganza SPHURTI. SPHURTI is an integral part of VESIT's history and it has been so since the beginning of VESIT.

> Play ground: 8177 sqm

- > Outdoor games: Football, Cricket, Volley ball, Basketball, Throw ball, Athletics, Discuss throw, Shotput.
- > Indoor games: Carom, Chess, Table Tennis.
- Gymkhana and Recreation Room

22. TEACHING FACULTY

VESIT believes that the faculty of an institution determines its success in delivering quality education. The college is well known for its highly knowledgeable and immensely proficient teachers who conduct classes with full zeal and robustness. They have contributed greatly in imparting quality education at our Institute. Our teaching faculty includes 30 Ph.D. holders and 23 faculty pursuing Ph. D. Each department also takes initiative to organize "Faculty Development Programs" for helping the teachers to apprise their technical knowledge which in turn helps them to deal with their subject and the projects in a professional manner. The staff members are also encouraged to acquire higher qualifications.

23. STUDENT'S COMMITTEE / ACTIVITIES

23.1 PROFESSIONAL SOCIETIES

A unique feature of VESIT is the fact that there are four Professional Societies totally managed by students under the supervision of Faculty In-Charges.

1. IEEE – Institute for Electrical and Electronics Engineers

"Excellence through Innovation" is what the IEEE-VESIT stands for in VESIT society culture. IEEE-VESIT gives its members ample opportunity to develop and inculcate their technical and soft skills, be it with theory, seminar or in practice via interactive workshops.

In-house Publication	"Innovations"
Symposium	"Melange'18"
Website	ww.ieee-vesit.org

Activities during the Year 2018-19:

Film it:, Roadshow, Case Closed, WIE Workshop, Android Workshop, Quizzard 1,Clash of Conquerors, Quizzard 2:,Online Treasure Hunt, Comflict, Android Workshop 2.0,Technical Paper Presentation & seminar, Save The Earth MUN, Article Writing, TROTA,SE Coordinators' Event.



Participants engrossed in the Android Workshop Lecture

2. ISA- Instrumentation Systems and Automation





Students participating enthusiastically in Comflict

ISA is a leading global non-profit organization that caters to the needs of over 30,000 worldwide members and other professionals. ISA-VESIT student chapter is well rooted with the International Organization. ISA-VESIT chapter exposes its members to the upcoming competitive world and tries to give them an edge over the others when it comes to practical knowledge through various events and seminars.

In-house Publication	"Envisage"
Symposium	"Enguage'18"
Website	www.isa.org

Activities during the Year 2018-19:

The Labyrinth of Mystery, FE Fun event, Arduino workshop, PSoC Workshop, Internet of Things (IoT) Workshop, Technovation'18, Raspberry Pi and Python workshop, Community Project, Automation in IT, Crack the Code, Hardware Auction, Analog Workshop, IoT and Wireless Sensor Networks,3-D printer inauguration, Image Processing, App Development workshop, Intercollege Events-Aayam, SYMPOSIUM "Engauge'18"



A session on Project Development during IOT Wireless Sensor Networks

3. CSI- Computer Society of India

The CSI VESIT stands for ingenuity, innovation and versatility. CSI-VESIT aims to give its members an added dimension to technology by keeping them abreast with the rapid advancements in the wide arena of technology. It is one of the most active student societies with the objective to accelerate student expertise in the field of technology. The CSI-VESIT student chapter has been retaining the award of The Best Student Chapter for the past seven years.

In-house Publication	"Random Thoughts" and "Nexus"
National Board	"Communications"
Annual magazine	"Interface"
Symposium	"Impact"
Website	www.csi-vesit.org

Activities during the Year 2018-19:

Praxis 2018: -. Code Knights, Sherlock and Watson, The Amazing Race, Github Workshop, JRC Workshop, Hidden Cipher, PHP & MySQL Workshop, Bootstrap Workshop, Python workshop, LAN gaming, Placement Talks, Cricomania, Linux workshop:, Laravel workshop, SE Coordinators' Event



Students listening attentively to the Speaker in the Python workshop





Teams at amazing race event

4. ISTE-Indian Society for Technical Education

VESIT-ISTE chapter's objective is to bridge the gap between the curriculum and the industry's requirements by giving students a platform to hone their technical and non-technical skills like overall personality, reasoning and presentation skills.

In-house Publication	"Mirage"
Symposium	"Synergy"
Website	www.iste-vesit.org

Activities during the Year 2018-19:

Photoshop, Technical debate, Math and logic, Notice the unnoticed, Group discussion, MS abroad Q&A session, Around the world, Placement week, Techtrix, Dalal street, Technical paper presentation



Participants competing in an intense game of math and logic

23.2 COUNCILS

Cultural Council

The Cultural Council has been organising events over the years that bring together fun, knowledge, creativity, innovation and freshness in all aspects of our life. Their events usually bring out the

creative side of a student's college life by having competitions catering to acting, film-making, singing, dancing and all the various contemporary art forms.

List of Activities during the year 2018-19:

PRARAMBH

The odd semester journey for the cultural council required a lot of hard work. Soon after the election, the Cultural Council began earnestly in preparations for PRARAMBH'18 - to welcome the Freshers into the college.



The Culture Council after Prarambh '18 celebrations

GANESH MAHAPUJAN

To invoke the blessings of Shree Ganesha, the cultural council joined hands in making Ganesh Chaturthi special occasion.

UTSAV

Utsav is the Annual College event that begins in February and Cultural council during Ganesh Aarti ends in the middle of March. During Utsav the Council conducts more than 30 events in the sphere of literature, Dance, Music, Drama, Art, Technical knowledge, Speaking skills, etc.

VESIT Dance Crew

The VESIT Dance Crew (VDC) managed to uphold their legacy by securing top positions at various dance competitions this academic year.

VESIT Drama Club

The VESIT Drama Club is a team of the super talented students who represent the institution in a host of inter-college drama competitions including national competitions.

23.3 Music Council

Every music enthusiast will agree with Leonard Bernstein on the power of music. Keeping up with this spirit of music, the Music Council for the academic year 2018-19 headed and came up with two major events in the odd semester where the whole college turned into a musical fiesta with just music resonating all around.



Music Council 2018-19

Bliss'18

"Beauty lies in Soulful Symphony" abbreviated as "Bliss" was held on 27th August, 2018 in the college auditorium. This event marks the beginning of the 'event' calender of the college. All the music council members performed on some of the most beautiful songs.



BEs at Bliss'18

Octaves'18

Octaves- "The Eight Notes" or "The Basic Miracle of Music". This was a two day musical festival - Each class tuned their guitars, cleared their throats and sharpened their knowledge on music as they geared up to perform at the Musical Fiesta-Octaves' 18.



Octaves '18 - Battle Of Bands

23.4 **SoRT**

Social Responsibility Team (SoRT)

"Not all of us can do great things, but we can do small things with great love."
- Mother Teresa

"Towards the better, together" With this attitude and utmost dedication, the Social Responsibility Team enthusiastically performed tasks to make positive changes in our society.

List of Activities during the year 2018-19: Kerala Donation Drive: 21st August, 2018, Traffic Monitoring For Janmashtami: 3rd September 2018, UMEED 18: 5th September to 11th September 2018, Traffic Monitoring At R.K Studio For Anant Chaturdasi: 20th September 2018, Dustbin Awareness Drive: 3rd October, 2018, Mock Fire Drill: 16th October, 2018, Blood Donation Camp: 22nd February, 2019, NGO Visit & Awareness Camp.



SoRT Council alongside volunteers from VESIT With the goods sent to the flood affected regions.



Drill Instructors explaining the procedure to use a fire extinguisher

23.5 Sports Council

Sports Council has been working tirelessly throughout the year mainly for their two grand events: Sphurti Indoor and Sphurti Outdoor conducted in the odd and even semesters respectively. Being the most active Council ever, it is naturally quite popular since the hype is always present, and lack of participation is rarely a problem for this Council since year after year, the best players come back to reclaim themselves, and newbies are given a chance to prove themselves and hone their skills.



Indoor Sphurti: Indoor Sphurti includes various indoor games such as Carrom, Table Tennis, Chess etc. Also introduced a new game called Futsal which is a version of Football.

Outdoor Sphurti: Outdoor

Sphurti has outdoor games for both girls and boys. For boys, it includes Football, Volleyball, Kabaddi, etc. whereas for girls it includes Dodgeball, Throwball, etc.



23.6 E-Cell (tVEC)

VESIT Entrepreneurship Cell was founded on 14th March 2014 and it aims to develop and nurture the entrepreneurship skills within the students while creating robust infrastructure to support budding entrepreneurs. Under the new leadership VESIT E-Cell has seen a lot of fundamental changes in its work culture and is all set to function like a 'Society' instead of 'Student Council' from the next academic year.

List of Activities conducted by Council 2018-2019: Project Deep Blue by Mastek and Majesco, BookSwap, Wantrepreneurs Meet, E-Summit 2018:- Wall Street Musings & Hackathon.



23.7 Tinkerers' Lab: Students' Secret to Creativity

"It's not about ideas. It's about making ideas happen." - Scott Belsky

The Tinkerer's Lab at VESIT was started with an aim to bring together the students who wanted to learn and teach. It also aimed at using the available resources in the college optimally and to eliminate the hassle of starting a project from the scratch and working on redundant tasks. It is currently operational in three departments viz. Department of Electronics, Department of Electronics and Telecommunication and

Department of Instrumentation. The aim of Tinkerer's Lab is to make lab resources available to the students and also e-waste of various labs.

23.8 CIRCLES

VESIT PhotoCircle

"There is one thing the photograph must contain, the humanity of the moment." ---

Robert Frank

VESIT Photo Circle is a group of students who come together for their love and passion for photography. The best photographers in VESIT are drafted through 'Recruitment Drive'. VPC captures memories throughout the events in the academic year and share them on their Facebook page. They also conducted a PhotoWalk. VESIT PhotoCircle is a small family in the big community of VESIT.

WORKSHOP AND PHOTOWALK:

With the aim of helping students to learn, enhance and understand the ideology behind the art of expressing stories through pictures. To get hands on practice of the workshop, a photowalk was conducted on 9th March 2019, starting from CSMT station to the various heritage sites of Mumbai CSMT area.



VESLit

It aims at teaching spoken English skills by taking special sessions for the students who want to hone their spoken English skills and groom their personalities. The Annual Literature Fest of VESIT, The VESLit Week, marks a full week of online and offline events that range from story writing to skits to microfiction. This festival is one of the most hyped and is the only festival, which has events held in collaboration with companies like The Union Bank of India and The Times of India.



D6A on stage during the Playoffs

Events (2018-19): Elocution, Terribly Tiny Tales, Expose, "Swatchh Bharat Abhiyaan", "Corruption", "Game Of Shows", Play Offs, Debate "Surgical Strikes to Gross Domestic Happiness (GDH)"



The VESLit Circle at the finale of VESLit Week

23.9 VESIT RENAISSSANCE CELL

In the academic year 2015-16 VESIT has come up with 'VESIT RENAISSANCE CELL' (VRC) with the mission, "To Suggest and implement innovative schemes to afflux VESIT for achieving academic excellence."

1. VESIT Summer and Winter Internship

The Internship aims to provide practical experience to the VESIT students, and the job was assigned to VESIT Renaissance Cell (VRC). The summer Internship conducted by VRC during May 2018- June 2019 a total of 75 successfully completed internship. The winter Internship conducted by VRC during Dec 2018- Jan 2019 more than 80 students were selected for internship.

2. MICROSOFT CERTIFICATION

VRC has coordinated Microsoft certification at VESIT facilitated by ATS. Three different courses were offered: Cross Platform Mobile App Development, Cyber & Mobile Security and IoT.

3. PRAXIS

In order to be successful, one must have a competitive spirit and an optimistic attitude towards learning from mistakes. To nurture and promote this competitive spirit, VESIT organizes PRAXIS every year. PRAXIS is our annual inter-collegiate technical festival, which has an exceptionally competitive Robotics event that has



Paper Eliminations of Science Quiz



Participants of Bridge the Gap

become a much awaited event among engineering students. It is gaining popularity not only in the Mumbai University but in other Universities as well. PRAXIS also boones and cultivates a sense of team spirit and event management skills in the students along with a sense of belonging.

23.10 VESITConnect

VESITConnect is the monthly newsletter of our institution which has been dedicatedly working since September 2015 to keep the college connected. The motto towards the team's determination is to keep the students, staff, management and the alumni abreast with the various events happening in the college, and also to keep them informed about the upcoming events. The newsletter captures all the joy and excitement around the college.

23.11 Institution Innovation Council

VESIT has joined hands with MHRD'S Innovation Cell (MIC) to systematically fosters the culture of Innovation amongst all Higher Education Institutions (HEI) and encourages, inspires and nurtures young students by supporting them to work with new ideas. MIC has envisioned the creation of 'Institution's innovation council (IICs)' across selected HEIs in India.

In the year 2018-19 VESIT II hosted various events including India First Leadership Talk Series with by Shri Anand Mahindra,



Inauguration of VESIT IIC

Chairman, Mahindra Group, Workshop and Live Session on Intellectual Property Right (IPR) by Ms Shwetasree Majumder IP lawyer, Agile Methodology of Software Development by TCS Team, How and What of Intellectual Property Rights by Mr Akhilesh C Shrivastav, (TCS Innovation Lab, Mumbai), India First Leadership Talk Series with Dr. Anand Deshpande, Founder & CMD, Persistent Systems Ltd.

23.12. VES LEADERSHIP ACADEMY AND RESEARCH CENTRE

To be successful in today's world of severe competition and stress, it is necessary to have the right attitude and positive thinking. At VESIT, the students are helped by the specially appointed faculty of VESLARC to develop a strong personality with leadership qualities.

23.13. SPICE

SPICE was formed, with an aim to bridge the gap between the college syllabus and the placements. It conducts aptitude tests, sessions by experts and ex-students to make students understand the aptitude level of dream companies as well as prepare them for such placements. It also aides in honing their overall personality by giving them an impetus to speak with confidence through activities like GD and debate sessions and prepare them for the future corporate world.

23.14 OTHER EVENTS AND ACTIVITIES

1. Annual Day: Annual Day is a cultural extravaganza, which is a complete event management exercise for our students under the supervision of our faculty. Annual prize distribution is also held on the same day.

- **2. Teachers' day:** The College organizes Teachers' day function for staff members every year. Teachers completing 20 years of service are felicitated on this day.
- **3. Technology Day:** On Technology day, we organize project exhibition and poster competitions for our final year and pre-final year students. Students actively participate and demonstrate their research ability as well as get to know the advancement in technology. This day is celebrated in the memory of our founder trustee **Shri. K. B. Mansharamani.**
- **4. VES Premier League:** This is organized by a group of Football enthusiasts from our college in collaboration with other VES members. VESIT's premier football tournament, the VESIT Premier League re-turned with its fourth season this academic year.
- **5. VESIT Cricket League:** This is similar to the VESIT Premier League but for all the cricket lovers of VESIT. VESIT Cricket League returned this academic year with its third installment. The league comprised of eight teams and all captains were from the third year. The captains initially battled it out in a scintillating auction round. The teams were named after iconic cricketers.
- **6.** Celebration of 70th Republic Day: To celebrate our 70th Republic Day, VES organized a ceremony at VES Polytechnic ground on 26 January 2019. The members of various VES institutions assembled to celebrate the proud moment. The events commenced with flag hoisting followed by a parade of the student scouts and cultural programmes presented by the students from various VES Institutes. The ceremony concluded with national anthem and a grand salute to the national flag.
- **7. Engineers' Day:** Every year 15th September is celebrated as Engineers' Day marking the birthday of Sir Mokshagundam Visvesvarayya.

24. ACHIEVEMENTS

Other than in-house events, the Institute also makes sure that the students are allowed to participate in various inter-collegiate events/cultural festivals as it helps them to develop and enhance their talents without affecting regular studies.

24.1 SMART INDIA HACKATHON

It is a moment of pride that we have add on to winning history of VESIT, that 5 out of 8 teams have won the Grand Finale of Smart India Hackathon 2019.



Team SPARX

Problem Statement : Digital India – Smart Subsidy System.

Prize amount Won: ₹ 1,00,000/-Problem Statement: AI for Farmers

Prize amount Won: ₹ 1, 00,000/-



Team Agronomists



Team MATRIX

Problem Statement: Mobile application to get RCS flight seat availability /Fare/Departure /

Destination in real time

Prize amount Won: ₹ 1, 00,000/-

Problem Statement: Mgmt of material during Disaster receipt, availability, movement and its receipt to the last person

Prize amount Won: ₹75,000/-



Team AccheDin

Activat



Team TechBACCRA

Problem Statement: Mgmt of material during Disaster-receipt, availability, movement and its receipt to the last person

Prize amount Won: ₹50,000/-

24.2 Education World Private Engineering Institutes Rankings

Education World is India's first education-focused magazine. Education World conducted a survey of India's Top 100 Universities, Engineering Colleges and Top 50 B-Schools. Education World India Private Engineering Institutes Rankings 2019-20 survey was conducted by the Delhi-based Centre for Forecasting and Research Pvt. Ltd (C fore), one of the country's premier market research and public opinion polling companies.

To compile the national and state rankings, C fore field researchers interviewed faculty and final year students of engineering institutes countrywide and industry representatives, asking them to rate engineering institutes on following nine parameters Leadership / Governance Quality, Faculty Welfare and Development, Competency of Faculty, Curriculum and pedagogy, Industry Interface, Research and Innovation, Infrastructure and Facilities, Placements, Value for Money.

Further, the interviewees were also asked to assign weightage to each parameter. The scores awarded by respondents under each parameter were totaled for the purpose of final ratings and ranking. V.E. S. Institute of Technology (VESIT) has earned the **FIRST RANK in Maharashtra State.**

24.3 E-YANTRA

eYantra Ideas Competition is a national level competition, initiated by IIT Bombay. This year they had more than 400 entries from all over India. The Regionals finals of this competition was conducted at six different centers in India. In the eYantra regional finals, two teams from VESIT - Team Squid and Team Drishti were placed in the top 36 groups. Also Team Squid went ahead to win the Best Algorithm Category.





24.4 AVISHKAR

Avishkar Research Convention is organized by University of Mumbai. It is designed to develop a research culture and scientific temper among the students, scholars and teachers from undergraduates to Doctoral level in the state of Maharashtra.

Six teams were selected and participated at the University Inter - Zonal/ Inter - District - 3 from UG category, 1 from PG category, PPG Cateroy and TH category, out of which Lekshmi Ajesh Kaimal secured 1st rank in PPG category, while Prof. Asma Parveen I. Siddavatam and Prof. Kadambari Sharma secured 2nd and 3rd rank in TH category respectively.



Prof. Asma Parveen I. Siddavatam, Mrs. Lekshmi Ajesh Kaimal, Mr. Ajit Patil & Prof. Kadambari Sharma



Prof. Asma Parveen I.Siddavatam receiving silver medal

Prof. Asma Parveen I. Siddavatam and Lekshmi Ajesh Kaimal represented University of Mumbai at Inter-University State Level competition that was held at Gondwana University, Gadhchiroli from 15th to 19th January 2019. Prof. Asma Parveen I. Siddavatam secured silver medal at Inter-University State Level competition in TH category. The teams/candidates presented university at state level were also got a chance to interact with Hon'ble Education Minister, Shri.Vinod Tawde at Mantralaya.

24.5 Deep Blue @ Mastek

VESIT was recognized as "Outstanding College 2018-19 by Mastek Company in the season 4 of their Deep Blue Competition. In the election year, our students have built a remote voting application using Block Chain Technology.

24.6 Shreshthata Award

The 'Shreshthata award' is an initiative by VESLARC (VES Leadership Academy and Research Centre) to acknowledge and encourage students who endeavor to make a difference in spite of unaccommodating circumstances.

25. PROHIBITION OF RAGGING

Ragging within or outside the institute is strictly prohibited. Any student who directly or indirectly participates in, abets or propagates ragging within or outside the institute shall face strict disciplinary action as per the provision of Maharashtra Prohibition of RAGGING ACT, 1999.

If a case is to be made as per law the following procedure is to be followed:-

- Investigation by squad
- Findings reported to the Principal
- Principal's discussion with the Committee to decide whether to file an FIR (within 24 hrs) with Police
 or Local Authorities OR punishment is to be awarded by the Anti-Ragging Committee of VESIT

Punishments may be in the form of:

- Suspension from attending classes and academic privileges.
- Withholding/ withdrawing scholarship/ fellowship and other benefits.
- Debarring from appearing in any test/ examination or other evaluation process.
- Withholding results.
- Debarring from representing the institution in any regional, national or international meet, tournament, youth festival, etc.
- Suspension/ expulsion from the hostel.
- Cancellation of admission.
- Rustication from the institution for period ranging from one to four semesters.
- Expulsion not only from the institution but also consequent debarring from admission to any other
 institution for a specified period. In case, the persons committing or abetting the act of ragging are
 not identified, the college will resort to collective punishment.

ADVICE TO ALL STUDENTS:

All students are advised to be aware of anti-ragging measures and report any ragging incident to college authorities in person or by telephone at the earliest.

More information is available on our institute's website: http://ves.ac.in/vesit

Students should submit an affidavit as per #ANNEXURE I and #ANNEXURE II at the time of admission. (Refer page nos. 56 and 57)

Anti-Ragging/Disciplinary Committee

Sr. No.	Name	Designation	Position	
1	Dr. Manoj Sabnis	Faculty, INFT	Chairperson	
2	Dr.(Mrs.) M. Vijayalakshmi	Vice Principal, VESIT	Member	
3	Dr. R. K. Kulkarni	Faculty, EXTC	Member	
4	Mr. A. Nagananda	Faculty, EXTC	Member	
5	Mr. Vivek Umrikar	Faculty, H & Sci.	Member	
6	Dr. P. Birajdar	Faculty, ETRX	Member	
7	Dr.(Mrs.) S. Mujumdar	Faculty, H & Sci.	Member	

26. WOMEN DEVELOPEMENT CELL

Constitution – as per Guidelines from Mumbai University				
Sr. No.	Name	Designation	Position	
1	Dr.(Mrs.) M. Vijayalakshmi	Faculty, MCA	Member	
2	Mrs. Shobha Krishnan	Faculty, EXTC	Member	
3	Dr.(Mrs.) Saylee Gharge	Faculty, EXTC	Member	
4	Mrs. Naveeta Kant	Faculty, ETRX	Member	
5	Mrs. Nishi Tiku	Faculty, MCA	Member	
6	Dr.(Mrs.) Nupur Giri	Faculty, CMPN	Member	
7	Mrs. Sonali Rahate	Superintendent	Member	

27. ANTI SEXUAL HARASSMENT COMMITTEE

The Government of India had promulgated The Sexual Harassment of Women at Workplace (Prevention, Prohibition & Redressal) Act, 2013 (Act) and the Sexual Harassment of Women at Workplace (Prevention, Prohibition & Redressal) Rules, with effect from 9th December, 2013. The act was laid down to ensure that women in particular are protected against sexual harassment at all workplaces, be it in public or private. It has now become mandatory to have an Anti-Sexual Harassment Committee as well as an Internal Complaint Committee to be formed in every institution in addition to the reports being sent to the Government for the same.

It is compulsory for all institutes/colleges under AICTE / UGC to Constitute "Internal Complaints Committee (regulation has come into force since 1st July 2016)

Sr. No.	Name	Designation	Position
1	Mrs. Shobha Krishnan	Faculty, EXTC	Chairperson
2	Mrs. Jayshree Pandit	Co-ordinator/ Counselor, Sri Mukta Sanghtana, R. C. Marg, Chembur - 71	Member (NGO)
3	Dr. (Mrs.) Nupur Giri	Faculty, CMPN	Member
4	Dr.(Mrs.) S. Mujumdar	Faculty, H&Sci.	Member
5	Mrs. Nishi Tikku	Faculty, MCA	Member
6	Mrs. Sonali Rahate	Office Superintendent, VESIT	Member
7	Student Representative (Girl Student)	Cultural Secretary/SoRT Secretary / Deputy Secretary, VESIT	Member

28. INTERNAL COMPLAINT COMMITTEE

Sr. No.	ICC Posts	Name of the Member
1.	Presiding Officer	Prof. Smita Jangle
2.	Non-Teaching Employee	Mrs. Sonali Rahate
3.	Non-Teaching Employee	Mr. Vikas Page
4.	Faculty Member	Prof. Sujata Khedkar
5.	Faculty Member	Prof. Nandini Ammanagi
6.	External Member(NGO)	Mrs. Jaishree Pandit
7.	Student	UG- SORT Secretary
8.	Student	Mrs. Abha Tiwari (PG)
9.	Student	Mrs. Asma Praveen (PhD)

29. GRIEVANCE REDRESSAL COMMITTEE

For A.Y. 2017-18 to A.Y. 2019-20

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Shoba Krishnan	Faculty, EXTC Dept., VESIT	Chairperson
2.	Dr. (Mrs.) Nupur Giri	Faculty, CMPN Dept., VESIT	Member
3.	Dr. Nadir Charnia	Faculty, EXTC Dept., VESIT	Member
4.	Mrs. Navita Kant	Faculty, ETRX Dept., VESIT	Member
5.	Mrs. Nishi Tikku	Faculty, MCA Dept., VESIT	Member
6.	Dr. (Mrs.) S.M. Mujumdar	Faculty, H&A Dept., VESIT	Member
7.	Dr. (Mrs.) Sayalee Gharge	Faculty, EXTC Dept., VESIT	Member
8.	Mrs. Sonali Rahate	Office Superintendent, VESIT	Member

30. COMMITTEE FOR SC/ST

With reference to Scheduled Castes and Scheduled Tribes (prevention of atrocities) Act, 1989, No. 33, of 1989, 11 September 1989, the Constitution of the Committee for SC/ST is as follows

For A.Y. 2017-18 to A.Y. 2019-20

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Sujata Khedkar	Asso. Professor, CMPN Dept., VESIT	Chairperson
2.	Dr. Nadir Charnia	Professor, EXTC Dept., VESIT	Member
3.	Mrs. Asha Bharambe	Asst. Professor, INFT Dept., VESIT	Member
4.	Mrs. Indu Dokare	Asst. Professor, CMPN Dept., VESIT	Member
5.	Mrs. Sangeeta Bhalerao	Sr. Lab. Asst., VESIT	Member
6.	Mrs. Nilima Tambade	Sr. Clerk , VESIT	Member
7.	Mr. Vinayak Doifode	Sr. Clerk , VESIT	Member
8.	Student	Secretary, SoRT, VESIT	Member

31. ADMISSION GRIEVANCE COMMITTEE

For A.Y. 2017-18 to A.Y. 2019-20 STAGE - I

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Shoba Krishnan	Faculty, EXTC Dept., VESIT	Chairperson
2.	Dr.(Mr.) Manoj Sabnis	Faculty, INFT Dept., VESIT	Member
3.	Mrs. Nishi Tikku	Faculty, MCA Dept., VESIT	Member
4.	Mrs. Asma Parveen I. Siddavatam	Faculty, INFT Dept., VESIT	Member
5.	Mr. Vinayak Doifode	Administrative Staff, VESIT	Member

STAGE - II

Sr. No.	Name	Department/Address	Position in Committee
1.	Dr. P.P. Vaidya	Faculty, INST Dept., VESIT	Chairperson
2.	Dr. R.K. Kulkarni	Faculty, EXTC Dept., VESIT	Member
3.	Dr. (Mrs.) Gresha Bhatia	Faculty, CMP Dept., VESIT	Member

STAGE - III

Sr. No.	Name	Department/Address	Position in Committee
1.	Dr. (Mrs.) J. M. Nair	Principal , VESIT	Chairperson
2.	Dr. (Mrs). M. Vijayalakshmi	Faculty, MCA Dept. , VESIT	Member

32. CAREER GUIDANCE, TRAINING, PLACEMENT & ENTREPRENEURSHIP CELL

ACADEMIC YEAR 2018-19 (Placement Statistics as on 13th June 2019)

	Overall College	Electronics Engg	Computer Engineering	Instrumentation Engg	Electronics & Telecomm Engg	Information Technology	MCA*
Job Offered	677	98	191	54	144	75	115
Second Job	109	12	33	7	18	16	23
Placed	568	86	158	47	126	59	92
Strength (Total)	872	147	234	80	222	77	112
Strength (Eligible 55%+)	722	108	178	66	186	72	112
% Offered (Eligible)	93.77	90.74	107.30	48.33	77.42	104.17	102.68
% Placed (Eligible)	78.67	79.63	88.76	45	67.74	81.94	82.14
Average Salary				2.2 Lakhs			
Maximum Salary				15 .9 Lakhs			
No of Companies Visited till date				69			

^{*} Training and Placement Cell also facilitate Internships for MCA Students during their Semester VI.

[#] Figures likely to increase as placement drives are still going on for 2018-19 batch.

Training and Placement Cell Structure:

Training and Placement Officer

Deputy Training and Placement Officer

ETRX **CMPN** INST EXTC INFT MCA Placement Placement Placement Placement Placement Placement In-charge In-charge In-charge In-charge In-charge In-charge Student Student Student Student Student Student Placement Placement Placement Placement Placement Placement Coordinators Coordinators Coordinators Coordinators Coordinators Coordinators

Training and Placement Cell Activities

Year	Activities
I YEAR	Inputs during Induction
II YEAR	Eligibility, Employability, impact of CGPI Contents of Resume and Building your Resume
III YEAR	Mock tests and Placement Readiness Workshops, Interaction with Final year students
IV YEAR	Interaction with alumni / companies. Online mock aptitude test

33. VESIT ALUMNI ASSOCIATION

Alumni Meet of this year was held on December 15, 2018. About 150 exstudents participated in this event. They were indeed very impressed with the change that has taken place in VESIT. They were also recollecting and reminiscing about the wonderful time they had spent here. The Meeting served as platform for current student council members to interact with their seniors and share experiences.



Alumni Meet: The annual Alumni day was conducted on 15th December, 2018. VESIT Alumni San Francisco chapter was inaugurated and a meet was organized at San Francisco on the 31st of March 2019. About 60 alumni from all departments starting from year 1989 attended the meet.



Mr.Vivek Arora receiving the Distinguished Alumni Award from the Principal



Mr.Amit Rambhia receiving the Distinguished Alumni Award from the Principal



Unveiling the WAVES magazine by the trustees



VESIT Alumni San Francisco chapter

34. DISCIPLINE

Discipline plays a vital role in every institute. All the students admitted in the first year (F.E.) of the institute will have to give an undertaking that they would observe general discipline and decent behavior throughout the four years of their engineering course.

34.1. IDENTITY CARDS

Every student who has taken admission and duly confirmed, must immediately take the I-Card, which is issued from the Principal's office and must wear it all the times. A student cannot even enter the premises without his/her I-Card and must readily present it when asked for, by the college authorities.

At the beginning of a new academic year, each student must take a new I-Card issued by the college after surrendering the old I-Card.

34.2. LOSS OF I-CARD AND ISSUE OF DUPLICATE I-CARD

In case the I-Card is lost, students can take special permission from the Principal which is valid only for three days. Within the stipulated time period the student should apply for the I-Card by submitting the prescribed fee. Any student using an I-Card (Original or Duplicate) without the signature of the Principal or without the Institute Rubber Stamp will be penalized.

34.3. EX-STUDENT'S I-CARD

For ex-students the college has made a special provision of separate I-Cards so that they enjoy and take advantage of the college library facilities. The ex-students however, are permitted to avail the facility only on working days during a stipulated time period.

34.4. LIBRARY AND OFFICE TIMINGS

During the term, the library will remain open on all working days from 8.0 a.m. to 6.00 p.m. For the benefit of the student the college allows them to use the library facilities for extended hours during holidays and preparatory leaves. The students can visit administrative office between 11 a.m. to 5.30 p.m. for any pending work provided they do not have any scheduled lecture, lab and tutorial during this time slot.

34.5. USE OF SPORTS GROUND

Students should ensure that they adhere to the Rules and Regulations of the college strictly while using the college playground. Strict action would be taken if they are found playing while the lectures/ practicals/ tutorials are in progress.

34.6. HOLIDAYS AND VACATIONS

The list of the holidays and vacations will be displayed on the notice board. The vacation period is liable to change every year and the students should be prepared for such exigencies.

34.7. RAILWAY CONCESSION

Students who are less than 25 years old can avail concession for commuting from College to their residence in and around Mumbai and for travelling to their home town (where their parents reside as declared in their application form for admission) from Mumbai during vacations. Concession forms for Local Railway tickets are issued from the Office of the Institute on working days.

The long journey concession form will be issued only for the vacation period. The students should write their local and permanent home /town address in the application form, for availing this concession. Concessions are being made within three days from the receipt of their application.

34.8. HOSTEL

There are separate hostels for girls and boys in this College where the students are offered residential accommodation in the best possible living environment. It will be provided on first-come-first -served basis and preference is always given to outstation candidates. Before leaving for vacation or holidays its mandatory for hostelites to take permission letter from the class teacher/class counsellor and submit a copy of the same to the Warden.

34.9. SAFETY PRECAUTION

Safety instructions given by teachers should be stringently followed by the students while doing their practical. In case of a mishap, the entire accountability shall lie on the student concerned and the Institute will no way be held responsible.

34.10. LOST AND FOUND

The authorities of the institute will not be accountable or responsible for any damage to the possessions or belongings of the students. However, articles which are lost and found are deposited in the office and are returned to the concerned student after identification.

34.11. DRESS CODE

Students are supposed to wear decent dresses and footwear suiting Indian Culture. **Indecent, skimpy, sleeveless, tightfitting, short and gaudy dresses and t-shirts with crude messages and pictures are strictly prohibited in the college campus.** Students should also keep proper well-groomed hair styles which depicts a positive outlook and persona.

34.12. CLEANLINESS OF PREMISES

All the students should strive for maintaining the cleanliness of the college premises and help in the maintenance and upkeep of the institutional property. They should not scribble anything on the walls, furniture etc. Strict action would be taken against the erring students and if not detected, a common fine will be levied on the entire class. Breakage of furniture will be treated in the same manner. All the students should strive for cleanliness of the premises and help in the better maintenance and upkeep of the institutional property.

34.13. INDULGING IN ANTI-SOCIAL ACTIVITIES

If a student is found in indulging in any anti-social or anti-national activities, he/she is liable to be expelled from the college without any prior notice.

34.14. CHEWING PAN/TOBACCO AND SMOKING

Chewing pan/tobacco or smoking in and around the premises of the institute is strictly prohibited.

34.15. USE OF ELECTRONIC ITEMS/GADGETS

Students are advised to use mobile phones **only outside class rooms/laboratories**. If they are found using mobile during lectures/ labs, the college authorities can confiscate such items and take strict action against the concerned students. Other electronic gadgets/equipment which is needed for project purpose can be brought in with proper permission and documentation.

34.16. REFUND OF CAUTION MONEY

After the completion of the course, an application for the refund of the caution money should be given (along with F.E. fee receipt) within the same financial year. Unclaimed caution money will be forfeited. Lifetime membership fee for alumni will be deducted from the caution money.

34.17. LOCKER FACILITY

Locker facility for the first year students is provided on first-come-first-served basis. The student should forward an application for using this facility for which he/she has to pay a locker fee.

34.18. RETURN OF ORIGINAL CERTIFICATES

All original certificates like Mark Sheets, Passing Certificate, Caste Certificate, Domicile Certificate etc. will be returned to the students after all the admission formalities of the University /DTE are completed. Normally, these certificates are returned to first year students in the month of March/April.

34.19. NOTICE BOARDS

Students are advised to read the notice boards regularly for various updates/circulars/notices displayed regarding payment of fees, commencement of examination and other matters of academic interest. Students are expected to pay the Tuition Fees/Exam Fees etc. before the last date. This will avoid penalty as well as inconvenience to them. Students should not go to the office for Railway concession or any other matter while their theory classes /practicals are in progress. Further, whenever they come to the office, they should maintain proper discipline and decorum. They shall always follow the Queue system and shall not crowd near the counters and should not block main entrance and exit points.

34.20. GRE/TOEFL/GATE/GMAT

Students will not be allowed to miss the college for the preparation of GRE/TOEFL/GATE/GMAT or other forms of competitive examinations. It may be noted that students need to plan their schedule well in advance so that it does not affect their academic curriculum. Students who desire to appear for the same will be permitted for a maximum period of 15 days, provided their academic record and attendance in the semester is satisfactory.

34.21. CO-CURRICULAR ACTIVITIES

All co-curricular, extracurricular activities and seminars/symposia/conferences/workshops of the students' professional societies of the college will be held only after the regular college timings. No activities will affect the regular working of the college. Students who wish to participate in local and outstation technical seminars, paper presentation contests, project exhibitions and other competitions will have to take prior permission.

34.22. PICNICS AND OUTINGS

Students will not be permitted to organize/participate in private picnics/outings even on Sundays/Holidays without the written consent of the college Principal. All picnics/outings have to be authorized and accompanied by college staff. Any failure to comply with any of the rule will lead to strict action taken against them.

34.23. INTERNET USAGE

Internet facility is provided to our students at various places. Students are expected to use this facility only for academic purposes and not for any other purposes like chatting. Misuse of this facility and indecent behavior on the NET is liable for punishment and strict action.

34.24. THEFT

Students found stealing other's property or institutional property will be dealt very strictly. If the institutional property is damaged/stolen, the entire batch of students will be penalized and the cost will be recovered as deemed fit.

34.25. SUGGESTION BOX

Students can avail the suggestion box facility provided outside the Principal's Office, in ease of any suggestions, complaints etc.

34.26. BIRTHDAY CELEBRATIONS

Birthday celebrations of fellow students are not allowed inside the class or within the premises or in the vicinity of the college.

34.27. PRINCIPAL'S SIGNATURE

Students should come to Principal's Office for signature or attestation during the recess time only or after their theory classes/practicals are over.

The above conditions are as per the Rules and Regulations of the Institute and all students should strictly follow them. Strict action would be taken against the student who fails to observe them.

Any matter not covered by these Rules will rest at the absolute discretion of the Principal.

*** Most Important Note ***

- All Admissions and Cancellations will be conducted/done as per the DTE rules and Guidelines.
- All available Scholarships of University, State Government, etc will be granted to the eligible candidates as per rules. (Please refer website.)

ANNEXURE 1

AFFIDAVIT BY THE STUDENT (On rupees 100 stamp paper) _____ (full name of student with admission/registration/enrollment number) s/o d/o Mr./Mrs./Ms having been admitted to V.E.S. Institute Of Technology, HAMC, Chembur, Mumbai-400074., 1. have received a copy of the AICTE regulations on Curbing the menace of Ragging in Higher Educational Institutions, 2009, (hereinafter called the "Regulations") carefully read and fully understood the provisions contained in the said Regulations. 2. I have, in particular, perused clause 3 of the Regulations and am aware as to what constitutes ragging. 3. I have also, in particular, perused clause 7 and clause 9.1 of the Regulation and am fully aware of the penal and administrative action that is liable to be taken against me in case I am found guilty of or abetting ragging, actively or passively, or being part of a conspiracy to promote ragging. 4. I hereby solemnly aver and undertake that I will not indulge in any behaviour or act that may be constituted as ragging under clause 3 of the a) Regulations. I will not participate in or abet or propagate through any act of commission or omission that may be b) constituted as ragging under clause 3 of the Regulations. 5. I hereby affirm that, if found guilty of ragging, I am liable for punishment according to clause 9.1 of the Regulations, without prejudice to any other criminal action that may be taken against me under any penal law or any law for the time being in force. I hereby declare that I have not been expelled or debarred from admission in any institution in the country on account of being found guilty of, abetting or being part of a conspiracy to promote, ragging; and further affirm that, in case the declaration is found to be untrue, I am aware that my admission is liable to be

V.E.S. INSTITUTE OF TECHNOLOGY

canceled.

Name & Signature of Deponent

Address & Telephone/Mobile No.:

Declared this ______day of _____ month of _____ year ___

ANNEXURE 2

AFFIDAVIT BY PARENT/GUARDIAN (On rupees 100 stamp paper)

$I,\ Mr.\ /Mrs./Ms.\ ____ \ (full\ name\ of\ parent/guardian)\ father\ /\ mother/guardian\ of$
(full name of student with admission /registration/enrolment number),
1. having been admitted to V.E.S. Institute Of Technology, HAMC, Chembur, Mumbai-400074., have
received a copy of the AICTE regulations on Curbing the menace of Ragging in Higher Educational
Institutions, 2009, (hereinafter called the "Regulations") carefully read and fully understood the provisions
contained in the said Regulations.
2. I have, in particular, perused clause 3 of the Regulations and am aware as to what constitutes ragging.
$3.\ I\ have\ also, in\ particular,\ per used\ clause\ 7\ and\ clause\ 9.1\ of\ the\ Regulations\ and\ am\ fully\ aware\ of\ the\ penal$
and administrative action that is liable to be taken against my ward in case he/she is found guilty or abetting
ragging, actively or passively, or being part of a conspiracy to promote ragging.
4. I hereby solemnly aver and undertake that
a) My ward will not indulge in any behaviour or act that may be constituted as ragging under clause 3 of the
Regulations.
b) My ward will not participate in or abet or propagate through any act of commission or omission that may
be constituted as ragging under clause 3 of the Regulations.
5. I hereby affirm that, if found guilty of ragging, my word is liable for punishment according to clause 9.1 of
the Regulations, without prejudice to any other criminal action that may be taken against my ward under any
penal law or any law for the time being in force.
6. I hereby declare that my word has not been expelled or debarred from admission in any institution in the
country on account of being found guilty of, abetting or being part of a conspiracy to promote, ragging; and
further affirm that, in case the declaration is found to be untrue, I am aware that my admission my word is
liable to be canceled.
Declared this day of month of year
Name & Signature of Deponent:
Address & Telephone/Mobile No:



Vivekanand Education Society's Institute Of Technology



SAY

NO

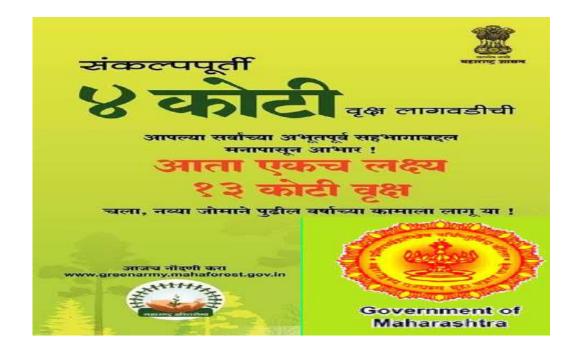




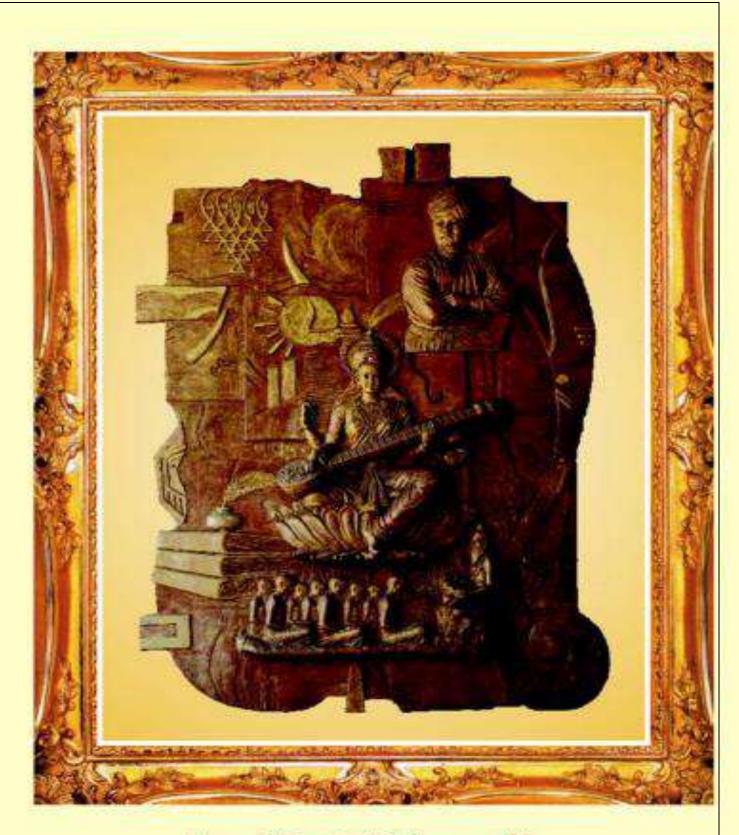












We need education that forges our life, generates the ideal man, imbibes noble thoughts and develops the abilities in us.

-Swami Vivekanand.



VIVEKANAND EDUCATION SOCIETY



VES Institute of Technology

Affiliated to Mumbai University, Approved by A.I.C.T.E & Recognized by Govt. of Maharashtra



PROSPECTUS

VESIT SALIENT FEATURES

- **Established Institution Innovation Council (IIC)**
- Ph.D Centre of University of Mumbai (Electronics Engineering)
- State of Art R & D Centre
- Library having more than 49000 books
- **❖** Permanent Affiliation to University of Mumbai
- Competency Development Centre of Automation Industries Association (AIA)
- **LabView Academy with National Instruments**
- Sprawling Complex, Inviting Playground & Hostel
- **❖** Training provided for Industries in the State of the Art Instrumentation Laboratory

- Zonal Partner with leadingindia.ai
- **Active Placement Cell**
- Highly Qualified Teaching Faculty
- Innovation Lab with Accenture and D-Link Academy
- * Aditya Jyot Eye Innovation Center
- **&** E Yantra Lab with IIT
- ***** Oracle Academy
- Professional Societies of Students
- Big Data Analytics Lab with Cloudera, USA

ACHIEVEMENTS

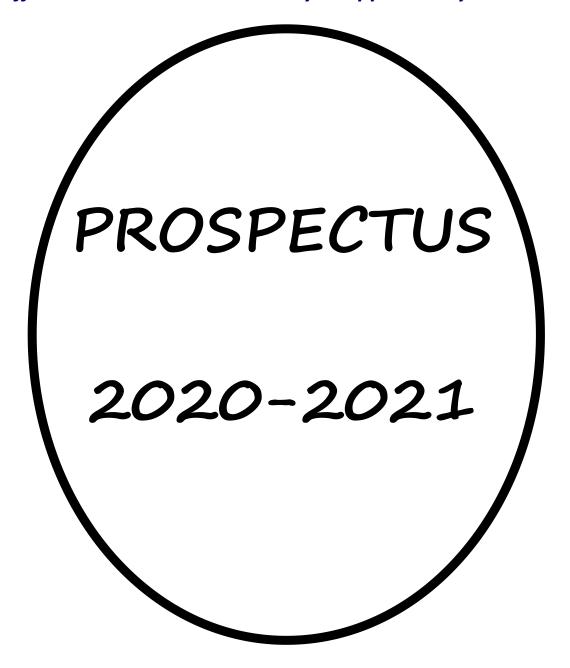
- > Two times Accreditation by the National Board of Accreditation in 10 Years for all eligible UG Courses.
- > VIVEKANAND EDUCATION SOCIETY Ranked in the band of 26th 50th in ARIIA 2019 under SELF-FINANCE/ PRIVATE INSTITUTONS category.
- > 18th RANK (West zone), 82nd RANK (All India) and 6th RANK (Mumbai) in PRIVATE ENGINEERING COLLEGES 2020 by "The WEEK".
- ➤ 1st RANK in Maharashtra State in "Education World –India's first education-focused Magazine". (Rankings 2018-19 & 2019-20)
- > Platinum Category in AICTE-CII Industry-linked Technical Institute Survey 2016.
- > "One of the best top 10 Private Institutes in India 2018" by "The Knowledge Review Magazine".

 Also Ranked in Surveys "India's Best Engineering Colleges 2019" by Chronicle of Career

 and Education"
 - "India's Best Engineering Colleges 2019" by Career 360"
 - "Times Engineering Institute Ranking Survey 2019" by Times Of India
 - "Best Colleges 2019" by The WEEK
 - "Best Colleges of India" by The India Today
- > 'Most Promising Engineering College with Excellent Faculty in Maharashtra' award in "Asia Education Summit & Awards 2017".
- > Winners in Smart India Hackathon for consecutively Four Years since its First Edition 2017
- Results: 100% in MCA and more than 95 % in B.E. for the last 8 Years.
- ➤ Placements: More than **75% Placements** for the last 8 Years.

VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY

Affiliated to Mumbai University & Approved by A.I.C.T.E



Website: http://vesit.ves.ac.in or http://ves.ac.in/ves

INDEX

1.	About Vivekanand Education Society (V.E.S.) 1			
2.	Governing Body/ College Development Committee			
3.	Courses Offered			
4.	From Trustee's Desk			
5.	From Principal's Desk			
6.	From Vice-Principal's Desk			
7.	Ranking of Vivekanand Education Society's Institute of Technology			
8.				
	8.1 Bachelor of Engineering (B.E.) (DTE Code: EN3185)	13		
	8.2 Master of Engineering (M.E.) (DTECode: ME3185)	14		
	8.3 Master of Computer Applications (M.C.A) (DTE Code: MCA3185)	14		
	8.4 Ph.D Programme	14		
9.	Grading System of the University of Mumbai			
10.	Term Grant			
11.	Examinations	15		
12.	Using Unfair Means in the Examinations	15		
13.	Attending Lectures/Lab			
14.	Infrastructure and Salient Features	16		
15.	Fee Structure	17		
16.	Special Programs			
	16.1 Looking beyond Syllabus			
	16.2 Awakening the Scientist	19		
	16.3 Skill Enhancement Lectures	19		
	16.4 Tinkerers' Lab: Students' Secret to Creativity	19		
	16.5 VESIT on Coursera	22		
	16.6 VESIT Swayam NPTEL local chapter	22		
17.	Major Laboratories	23		
	17.1 Department of Electronics Engineering	23		
	17.2 Department of Electronics And Telecommunication Engineering	25		
	17.3 Department of Computer Engineering	27		
	17.4 Department of Instrumentation Engineering	29		
	17.5 Department of Information Technology	31		
	17.6 Department of Master of Computer Applications	33		
	17.7 Department of Humanities and Applied Sciences			
18.	Industry Collaborations			
19.	Research and Development Lab	40		
20.	Common Infrastructure			
21.	Teaching Faculty and Achievements	43		
22.	Students Society's/ Councils/Committees			
	22.1 Professional Societies	46		
	22.1.1 IEEE – Institute For Electrical and Electronics	46		
	22.1.2 ISA- Instrumentation Systems and Automation	47		
	22.1.3 CSI- Computer Society of India	48		
	22.1.4 ISTE-Indian Society for Technical Education	48		
	22.2 Councils			
	22.2.1 Cultural Council	_		
	22.2.2 Music Council	50		
	22.2.3 Sports Council	51		

V.E.S. INSTITUTE OF TECHNOLOGY

	22.3 Social Responsibility Team (SORT)	55
	22.4 weCHANGE, an NGO the Brainchild of a Student of VESIT	
	22.5 Circles	
	22.5.1 Vesit Photocircle (VPC)	57
	22.5.2 VESLit	
	22.6 Vesit Renaisssance Cell	58
	22.6.1 Vesit Summer and Winter Internship	
	22.6.2 Microsoft Certification	
	22.6.3 Praxis	
	22.7 VESITconnect	59
	22.8 Institute Innovation Council	59
	22.9 VES Leadership Academy and Research Centre (VESLARC)	
	22.10 Spice	
	22.11 Other Events and Activities	60
	22.12 Unnat Bharat Abhiyan	62
	22.13 Ek Bharat Shreshtha Bharat	63
23.	Achievements	64
	23.1 Smart India Hackathon	64
	23.2 E-Yantra	66
	23.3 Avishkar	66
	23.4 Conduent Blockchain Hackathon	66
	23.5 Competition on Disaster Management	67
	23.6 Sankalp Semiconductor Hackathon	67
	23.7 Schneider Electric Hackware 2019 Innovation Challenge	67
	23.8 Buoyanci Innovation Challenge	68
	23.9 India Innovation Challenge Design Contest 2019	68
	23.10 "Transfer Window" - IIM-Indore	68
	23.11 Quiz-a-Thon	
	23.12 SAKEC Intercollegiate Quiz	69
	23.13Annual Debate Competition by JBIMS	70
	23.14 ISA Mharashtra Awards by Students	
	23.15 Shreshthata Award	72
	23.16 Scholarship by Jadavpur University Alumni Trust	72
	23.17 The Suman Ramesh Tulsiani Charitable Scholarship	
24.	Green Campus Initiative	73
25.	Career Guidance, Training, Placement and Entrepreneurship Cell	74
26.	VESIT Alumni Association	
27.	Discipline	
28.	Prohibition Of Ragging	83
29.	Anti-Ragging/ Disciplinary Committee	
30.	Women Development Cell	
31.	Anti Sexual Harassment Committee	
32.	Internal Complaint Committee	
33.	Grievance Redressal Committee	86
34.		86
35.	Admission Grievance Committee	87

1. ABOUT VIVEKANAND EDUCATION SOCIETY



Vivekanand Education Society

Distinctive Essentials

- · Admired for Ethics, Values, Discipline and Excellence in education
- Management board of honorary professionals in different fields. Honorary Associates and Alumni assisting in various activities of the trust.
- · 26 Institution from creche to schools to colleges upto Ph.D. level
- · More than 18,000 students studying every year and over 1200 teaching & non-teaching stuff Staff.
- · No capitation or donation from students or parents
- · There are no Management quota seats in all professional Institutions of VES.
- · All admissions are purely on merit
- · 12 buildings in 3 complex's with over 395000 sq.ft of area
- · 3 Large Sports grounds and, Hostel & Staff quarters.
- · Finanacial help to poor & needy students.

Institutions of VES

Professional, Technical & Higher Education Institutions

V.E.S. Institute of Technology

V.E.S. Institute of Management Studies and Research

V.E.S. College of Pharmacy

V.E.S. Artificial Intelligence and Data Science after Pharmacy

V.E.S. Law College

V.E.S. College of Science, Arts & Commerce

V.E.S. Leadership Academy & Research Center

V.E.S. College of Architecture

V.E.S. Polytechnic

High Schools and Junior Colleges

Swami Vivekanand Jr. College, English Medium

Swami Vivekanand Kanishta Mahavidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, English Medium, Chembur

Swami Vivekanand Vidyalaya, Hindi Medium, Kurla

Swami Vivekanand High School, Marathi Medium, Kurla

Certificate Level Technical and

Vocational Institutions

V.E.S. Tulsi Technical Institute, Sindhi Society,

Chembur

V.E.S. Catering Craft Center

V.E.S. Nari Shala

Sports and Fine Arts Institutions

V.E.S. Vivekanand Sports Academy

V.E.S. Vivekini

Other Institutions

V.E.S. Creche & Day Care Centre

V.E.S. Vocational Guidance Bureau

V.E.S. Welfare Section

V.E.S. Vivek Sindhi Kendra

Primary & Preprimary Schools

Swami Vivekanand Preprimary & Primary School English Medium, Sindhi Society, Chembur

Swami Vivekanand DBC Pre-Primary & Primary School English Medium, Collectors Colony, Chembur

Swami Vivekanand Prathmik & Purva Prathmik Shala Hindi Medium, Kurla

Swami Vivekanand Pre-Primary & Primary School English Medium, Kurla (E)

Swami Vivekanand Prathmik & Purva Prathmik Shala, Marathi Medium, Kurla

Swami Vivekanand Nursery & Play School, Chembur

2.GOVERNING BODY

MEMBERS OF VESIT GOVERNING BODY

1.	Shri. B.L. Boolani	Chairman	
2.	Shri. Amar T. Asrani	Member	
3. Shri. Suresh Malkani		Member	
4. Dr. (Shri) Prakash Lulla		Member	
5. Shri. R.N. Mukhija		Member	
6.	Shri. Sunil Khanna	Member	
7. Dr. (Mrs.) J.M. Nair		Principal	
8.	Dr.(Mrs.) M. Vijayalakshmi	Vice Principal	
9.	Shri. Vijay Talreja	Special Invitee Member	
10.	Dr. Amit Dutta	AICTE Nominee	
11.	Dr. M.D. Desai	AICTE Nominee	
12.	Dr. N.R. Gilke	M. U. Nominee	
13.	Dr. Abhay Wagh	DTE Nominee	
14.	4. Shri. P. K. Krishnamurthy State Govt Nominee		
15.	Dr. P. P. Vaidya	Staff Representative	
16.	Dr. Gresha Bhatia	Staff Representative	

COLLEGE DEVLOPMENT COMMITTEE

MEMBERS OF COLLEGE DEVELOPMENT COMMITTEE

1.	Shri. B.L. Boolani	Chairman
2.	Shri. Amar T. Asrani	Member
3.	Shri. Suresh Malkani	Member
4.	Dr. (Shri) Prakash Lulla	Special Invitee
5.	Shri. Laxman Kanal	Special Invitee
6.	Shri. J.K. Khushu	Member – Industry Expert
7.	Shri. Vijay Talreja	Member – Industry Expert (Alumnus)
8.	Dr. B.K. Lande	Member – Education Expert
9.	Shri. Milap Shah	Special Invitee (Alumnus)
10.	Shri. Niraj Gunde	Special Invitee (Alumnus)
11.	Dr.(Mrs.) M. Vijayalakshmi	Member-Coordinator, IQAC
12.	Dr. (Mrs.) J.M. Nair	Principal
13.	Dr. Gresha Bhatia	Special Invitee
14.	Dr. (Mrs.) Shanta Sondur	Teacher's representative
15.	Dr. C. D. Rawat	Teacher's representative
16.	Shri. Abhay Kshirsagar	Teacher's representative
17.	Mrs. Kajal Madnani	Member – Non-Teaching staff's representative
18.	President, College Student Council	Student Member
19.	Secretary, College Student Council	Student Member

3.COURSES OFFERED

Starting Year	Course /Programme /Intake
1984 – 1985	BE Electronics Engineering – 60
	BE Computer Engineering – 60
	BE Instrumentation Engineering – 60
1992 – 1993	BE Electronics and Telecommunication Engineering – 60
1999 – 2000	BE Information Technology – 60
2002 - 2003	ME Electronics and Telecommunication Engineering – 18
2002 - 2003	ME Information Technology – 18
2003 - 2004	ME Instrumentation and Control – 18
2003 - 2004	Master of Computer Applications – 60
Increase in intake of the existing programmes	
2011 – 2012	BE Computer Engineering – 60
2012 – 2013	BE Electronics and Telecommunication Engineering – 60
	BE Electronics Engineering – 60
	BE Computer Engineering – 60 (Regular Second Shift)#
	Master of Computer Applications – 60 (Regular Second Shift)#&
	Recognized as Ph.D Centre for Electronics
2013-2014	Electronics and Telecommunication Engineering – 60 (Regular Second Shift)
	(discontinued from 201 <mark>9</mark> -20)
2019-2020	BE Information Technology – 120
	(Increase in intake from 60 to 120)
2020-2021	BE Artificial Intelligence and Data Science – 60
	BE Electronics Engineering (Reduction in intake from 120 to 60)

^{# -} Converted as First Shift by AICTE w.e.f. A.Y. 2020-21

[&]amp; - Two Years Post Graduate Course w.e.f. A.Y. 2020-21

4. FROM TRUSTEE'S DESK

Dear students, it is a matter of pride as our students present the 36th edition for the Vishwakarma. I personally believe that real learning is possible only through practical experience in the laboratory. I also advocate the use of novel and innovative techniques that encourage critical thinking and develop an aptitude of problem-solving. We endeavor is to provide training to students which empowers them to learn; apply the knowledge acquired; improvise, innovate and create. Everyone should also cultivate a habit of reading regularly and



Shri. B.L. Boolani President, VES.

keeping oneself abreast with developments in the world of technology as it has become a necessity to become successful in one's chosen career. The latest technology report says that India's roll-out of 5G, the Fifth Generation spectrum wave, will create numerous opportunities in areas such as: - Machine Learning, - Big Data, - Cyber Security with 5G protocols, - GPS Technology, - Internet of Things (IoT) and - Embedded System. I am sure that our budding engineers will grab these opportunities and the enthusiastic engineers with entrepreneurial minds will also create opportunities in these areas for other job seekers.

The academic year 2019-20 has been an eventful year with our institute achieving many laurels at state and national level, in technical and extra-curricular activities. The Institution's Innovation Council (IIC) has been conducting various events to train young minds to innovate, create and nurture them to become entrepreneurs. As per the new National Education Policy 2019, Interdisciplinary research is the need of the hour with an optimum blend of science and technology. I sincerely believe that our engineering, science and pharmacy institutes can come and work together, complementing each other in the process, to bring about reforms in the healthcare and information technology sector. VES also looks forward to some innovative outcomes protected with Patents that can be made available to the industry for commercialization. I also urge faculty and students to invite problems from the industry to give them creative solutions. Last but not the least, personally, through my experience, I can state that each one needs to take the utmost care of their body and mind by maintaining a healthy lifestyle which comprises of regular exercise and nutritious food. My message to all young minds would be to remain healthy and think creatively. Wish you all the very best for all your future endeavors

Shri. B.L. Boolani President, V. E. S.

VISION

"To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of socially conscious application of technology".

5. FROM PRINCIPAL'S DESK

Education plays a fundamental role in the overall progress and development of students. The institution firmly believes in imparting knowledge and inculcating and nurturing basic human values, culture, and adaptability to its students so that they soar higher in the ladder of success of life. This global crisis is a great reminder that the world is moving to more virtual learning and working spaces. The lockdown period has given us an opportunity to enhance our knowledge and widen our horizon of minds. It's a beautiful opportunity to revamp the teaching-learning process too where high level of experience and exposure is given to students in a supportive and conducive environment. Our teachers and students have promptly adapted to the



changing scenario and commenced the classes of the new academic year from 3rd August 2020 in completely online mode using Google meet and Google classroom. I am optimistic that we would be able to conduct face to face classes soon. Till then I expect that both teachers and students will enthusiastically participate in online lectures and other activities.

It gives me an immense pleasure to inform you that VES is ranked in the Band 'B' (26th-50th) in ARIIA 2019 ranking under the private institutions' category. Ministry of Education, Govt. of India has initiated 'Atal Ranking of Institutions on Innovation Achievements (ARIIA)' to systematically rank education institutions and universities primarily on innovation-related indicators.

ARIIA considers all major indicators which are commonly used globally to rank most innovative education institutions/ universities in the world. ARIIA primarily focused on seven main parameters like Budget and funding support, Infrastructure and Facilities, Awareness and Promotion of Idea Generation and Innovation, Promotion and Support Entrepreneurship, Intellectual Property Generation, Technology Transfer and Commercialization, Innovative Learning Methods and Courses and Innovation in Governance of the Institution.

VESIT, VESCOP, VESP and VESIM together participated as VES in ARIIA ranking survey.

Hope all of you are acquainted with the advent of the new National Education Policy (NEP), 2020. Its purpose is not just the creation of greater opportunities for individual employment, it aims at a larger picture of creating a productive, innovative, progressive, and prosperous nation. It envisages a quality higher education that can develop thoughtful, well rounded, and creative individuals. In a nutshell, education will be holistic, more student-centric and flexibility will be its hallmark. For instance it provides flexibility for an engineering student to opt for some courses in philosophy, law or even music, along with his main stream engineering subjects. The implementation of this policy is expected to be completed by 2030. I hope all of you will appreciate this concept and welcome this change in policy.

Having appreciated this revolutionary change, I would like to add a quote of C. S. Lewis "You can't go back and change the beginning, but you can start from where you are and change the

5

ending." Let us now dwell upon the new initiatives for the present second year students (A.Y. 20-21) and related existing activities for all VESIT students in this direction.

As all of you know, the present second year students are studying in Choice Based system, where they can choose subjects from Institute Level Electives and Departmental Electives.

From this academic year, AICTE has introduced the concept of Honors Degree and Minor Degree. Honors Degree allows courses in emerging areas as specialization from the same department. The nine emerging areas identified are Artificial Intelligence, Internet Of things, Robotics Cybersecurity, BlockChain, Quantum Engineering, Data Sciences/Big Data and Analytics, 3D Printing and Design, and Augmented Reality/Virtual Reality. The minimum credits to be earned for such courses are 16-18 and will be mentioned as specialization in that particular area. For instance, doing extra credits of IoT by a student of Electronics Engineering, can earn B.E (Hons) Electronics Engineering with specialization in IoT.

Minor Program in Emerging areas is also allowed, wherein a student of any department can take courses with minimum credits in the range of 16-18 and get a degree with a minor from another department. For example, doing extra credits of Big Data/Data Science, a student of B.E (Electronics) can earn B.E Electronics with a minor degree in Big Data/Data Science.

Detailed guidelines regarding eligibility, the list of the four courses prescribed under each emerging area and its syllabus will be issued by the University of Mumbai and will be intimated to you in due course of time.

Another initiative of AICTE, which was implemented last year was the introduction of the Student Induction Program in the first year. Apart from the familiarization of the fresh students with the activities of the institute, both curricular and extracurricular the concept of Universal Human Values (UHV) also was introduced, in an attempt to provide holistic education. The UHV concept is about living with continuous fulfilment, which is the basic human aspiration and plays a significant role in the shaping of one's world-view, mindset, perspective, and values.

The key to future job opportunities will be integration of the technologies in diverse fields and shelf life of any new technology will be of average 3 to 5 years. As the world is heading towards a gig economy characterized by freelance work, every student has to compete with himself and make him worthy of the assignments. Therefore, lifelong learning and the ability to self-learn will be the key driver of success for an individual in the future.

Industry thought leaders have suggested that the emphasis should be, not on making people for the world, but making products for the world in the post COVID era. A bright future for small industries unit with better efficiency is also predicted compared to big industries with mass production. All these facts point out that the students should develop their problem-solving skills, ability to self-learn, and cultivate the habit of working in teams.

VESIT has been conducting "Looking Beyond Syllabus" projects for Second and Third year students and "Awakening the Scientist" for first year students and "Tinkerers Lab" for all students, which provides opportunities to develop their problem-solving skills and ability to work collaboratively. A UHV Cell will also be constituted in our institute from this academic year.

V.E.S. INSTITUTE OF TECHNOLOGY

All students are requested to participate in these co- curricular activities and reap their benefits, for a bright future.

This academic year, the **University of Mumbai** has also implemented a new concept of **Skill labs** for **second year students**. Skill labs are specially designed "**application-oriented practical sessions**" which focus not only on providing knowledge but also on building attitude and self-learning capability along with building skills. Another initiative is the introduction of **Mini projects** in the syllabus for **second year students**. I urge all the students of second year to participate in Skill labs and Mini projects wholeheartedly and consider it as the requirement for building a foundation for their future and not as a syllabus requirement.

My dear Students, grab all the opportunities available to you, enjoy your journey of learning to learn yourself and to work along with everyone, "Sabka Sath, Sabka Vikas".

Dr. J. M. Nair

Principal, VESIT



"Just because something doesn't do what you planned it to do doesn't mean it's useless".

-Thomas Edison

6. FROM VICE-PRINCIPAL'S DESK

VESIT takes pride in being able to impart holistic education to all its students. Our vision indicates our dream of being able "To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of socially conscious application of technology".

I am indeed proud to say that year after year we have been able to deliver on this promise. This is especially challenging at this difficult period the world and in particular India is facing. The concept of education has changed overnight and in these times of crisis, digital learning has emerged as an indispensable resource for education. Digital transformation of education leading to all sorts of remote learning opportunities for students and enabling teachers to create intriguing (virtual) experiences. Is the need of the hour.



Or. (Mrs.) M. Vijayalakshmi Vice Principal

But every challenge opens up an opportunity and VESIT has grabbed this window of chance.

We at VESIT are proud of the way we are leveraging existing digital platforms from the likes of Google classroom, Moodle, Microsoft Education and conferencing apps like Zoom etc to provide a continual enriching engagement experience to students. A series of digital events like Industry collaborated events, lectures on current topics by experts in the field, digital competitions and the like have continuously engaged the students during this pandemic times.

Further several E-Learning resource like MHRDs Swayam, Nptel Virtual Labs, are all easily available to students at a click. VESIT was a part of the Coursera-Covid -19 initiative that saw thousands of our students and faculty take advantage of the huge treasury of Coursera courses free of charge. This ensured that all our students were continuously engaged in world class learning from their homes.

Many of our library resources like **journals**, **newsletters** are all made available to students remotely from the safety of their homes. **Virtual Digital labs** have been designed for almost all courses so that students do not miss out on laboratory learning

In addition, VESIT boasts of a well equipped library, state of the art laboratories with industry calibre hardware and software. Further several students run professional societies, industry collaboration labs help to create and nurture a vibrant knowledge oriented environment ideal for creating next generation engineers ready to take our nation into a nation of job creators as versus job seekers.

We at VESIT never forget our commitment of "Giving Back to Society". Our SORT team is continually associating itself with several social organizations to participate in society beneficial activities, like village adoptions, educating girl children, cleaning up of railway stations, cancer walks etc. We are active participants in the MHRD's Unnat Bharat Abhiyan which works towards adoption of rural areas to help uplift them.

As John Ruskin has said, "Quality is never an accident; it is always the result of intelligent effort." At VESIT Quality is indeed the sum product of all the endeavours of the Faculty, staff and off course the students.

Wishing you all the very best. "May you live in Innovating Times."

Dr. M. Vijayalakshmi Vice Principal, VESIT



RE-ACCREDITATION OF UG PROGRAMS

- 1. ELECTRONICS ENGINEERING
- 2. INSTRUMENTATION ENGINEERING
- 3. ELECTRONICS & TELECOMMUNICATION ENGINEERING
- 4. INFORMATION TECHNOLOGY

BY

NATIONAL BOARD OF ACCREDITATION (NBA) FOR 3 YEARS (i.e. TILL JUNE 2022) (Tier-II)



BAND-B (26-50) in Private or Self-Financed College/Institutes



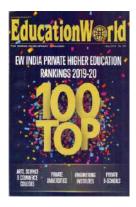
RANKED IN RANKING SURVEY 2018 - 19 (Without any Registration Fees or Sponsorship)

EducationWorld India Private Engineering Institutes Rankings 2019-20 Survey

SURVEY WAS CONDUCTED BY THE DELHI-BASED CENTRE FOR FORECASTING AND RESEARCH PVT. LTD (C FORE), ONE OF THE COUNTRY'S PREMIER MARKET RESEARCH AND PUBLIC OPINION POLLING COMPANIES.

V.E.S. Institute of Technology (VESIT) has earned the

RANK 1 in Maharashtra State





OREOVER, IT'S pertinent to note that although some are modestly ranked nationally, they are highly ranked nationally, they are highly ranked in their states. For instance, the Vivekanand Education Society's Institute of Technology, Mumbai ranked #24, nationally is ranked #21 in Maharash tra (pop. 13 million). Similarly, the Rajagiri School of Engineering and Technology, Ernakulam, ranked 439 all-India is #1 in Kerals (34 million), and Heritage Institute of Technology, Kolkata, ranked #24 nationally, is West Bengal's #2 private engineering college.

Dr. J.M. Nair: an alumna of IT-Bombay and College of Engineering, Trivandrum and principal of the Vivekanand Education Society's Institute of Technology (VESTI). Mumbai, is delighted that VESTI is the top-ranked private "and rigorously administered" engineering college in India's most industrialized state. Whe have great respect for the annual EWIFEI Rankings which are based on the assessment of knowledgeable leaders of Industry and academia as also senior students in higher education. It is public acknowledgement that we provide high-quality industry-relevant engineering education to our students. VESTI has applied to the University of Mumbai for autonomy status, which we hope to get soon. Autonomous status is important because it will give us fall freedom to update our curriculums, strengthe industry linkages, introduce intendiciplinary courses and focus on research, innovation and entrepreneurship," says Nair.

VESIT in "The 10 Best Private Institutes in India, 2018"

by The KnowledgeReview Magazine Team. Published in India Edition Nov'18

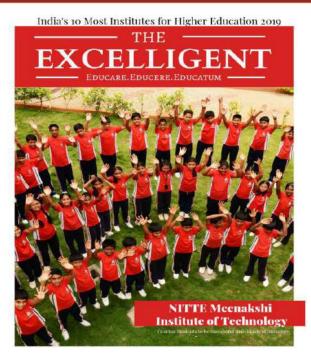
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RANKED IN RANKING SURVEY 2018 - 19 (Without any Registration Fees or Sponsorship)

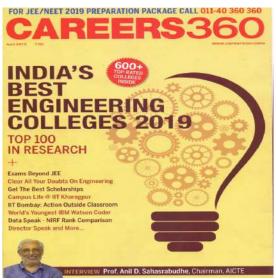


India's 10 Best Institutes for Higher Education 2019

In this issue Magazine present a few 'Leading Institutes' in India which are providing 'Quality Higher Education' and thrives to be the best in their field

(July -Aug 2019)











VESIT ranking in Private Engineering Colleges by "THE WEEK"

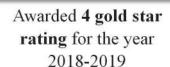




- ✓ 82nd RANK (All India)
- ✓ 18th RANK (West zone)
- ✓ 6th RANK ((Mumbai)



Institute Innovation Council (IIC) @ VESIT





VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY, is participating in UBA and adopted following 5 villages of WADA – THANE District, Maharashtra for their development in collaboration with district administration.

1. AMBISTE KH 2. AMGAON

3. GARGAON 4. ASNAS

5. APTI



एक भारत श्रेष्ठ भारत

Mapped with Odisha State



13th Intercollegiate/ Institute/ Department
Avishkar Research Convention 2018-2019
University Of Mumbai



"Overall Championship (First Rank)" in Engineering & Technology Category



Principal Dr. J. M. Nair and Dr. P. P. Vaidya receiving the Overall Championship (First Rank) award in Engineering & Technology Category in the 13th Intercollegiate/Institute/Department Avishkar Research Convention

8. SCHEME OF STUDY

8.1 BACHELOR OF ENGINEERING (B.E.) (DTE CODE: EN3185)

The Bachelor of Engineering is a four year course, which is divided into eight semesters. The first year (F.E.) course spreads over two semesters dealing with general engineering, common to all branches of engineering. The basic objective is to give elementary foundations needed in different aspects of engineering.

the second During year (S.E.) and third year (T.E.), students gain basic oftheir chosen branches. The final year i.e. fourth knowledge year (B.E.) is designed strengthen the specialized skills which help the students to apply the concepts studied earlier by participating in project work. The subjects and the syllabus are as prescribed by the University of Mumbai.

Approved Intake for Academic Year 2020-21

Regular Course	<u>Intake</u> (51% Sindhi Minority Quota)				
B.E Electronics Engineering	60*				
B.E Computer Engineering	120				
B.E Instrumentation Engineering	60				
B.E Electronics and Telecommunication	120				
B.E Information Technology	120				
B.E Artificial Intelligence and Data Science	60				

Regular Course

Intake (51% Sindhi Minority Quota)

B.E. - Computer Engineering

60#

- *- Decrease intake from 120 to 60
- #- Converted as regular course from second shift by AICTE w.e.f. A.Y. 2020-21

Eligibility: - As per DTE, University of Mumbai and State Government Rules.

<u>Note:-</u> Admissions will be done through CAP (Centralized Admission Process) conducted by DTE (Directorate of Technical Education).

8.2 MASTER OF ENGINEERING (M.E.) (DTE CODE: ME3185)

The M.E. Program comprises of four semesters with duration of two years. The courses are designed/prescribed by the University of Mumbai.

The numbers of seats offered are as mentioned below:

Approved Intake for Academic Year 2020-21

Regular Course	<u>Intake</u>	(51% Sindhi Minority Quota)
M.E Electronics and Telecommunication	-	18
M.E Instrumentation and Control		18
M.E Information Technology		18

Eligibility: - As per DTE, Mumbai University and State Government Rules.

Note: -Admissions will be done **through CAP** (Centralized Admission Process) conducted by DTE (Directorate of Technical Education).

8.3 MASTER OF COMPUTER ENGINEERING (M.C.A) (DTE CODE: MCA3185)

Master of Computer Application (M.C.A) is a full time two year PG course (fours semesters). The course is conducted as per the syllabus recommended by the **University of Mumbai**. The examinations for all the six semesters and the degree is also awarded by University of Mumbai.

<u>Co</u>	<u>ourse</u>	<u>Intake</u>
MCA	- Regular Course *	60
MCA	- Regular Course **	60

Eligibility: - As per DTE, University of Mumbai and State Government Rules.

Note: -Admissions will be done through CAP (Centralized Admission Process) conducted by DTE (Directorate of Technical Education).

- * Two Years Post Graduate Course w.e.f. A.Y. 2020-21
- ** Converted as regular course from second shift by AICTE w.e.f. A.Y. 2020-21

8.4 Ph.D PROGRAMME

VESIT has always given importance to the experimental way of teaching and learning of engineering concepts which can only be perfected through the best research and development practices. VESIT offers Ph.D course in Electronics Engineering and has a well equipped R&D Laboratory which is utilized by P.G. and Ph.D students.

9. GRADING SYSTEM OF THE UNIVERSITY OF MUMBAI

Mumbai University introduced Choice Based System from the academic year 2016-17. All information about pattern and syllabus is available on Mumbai University's official web site www.mu.ac.in

Salient Features:

- ✓ The final grade for B.E. Degree will be based on the average of all eight semesters. So each semester/each subject is important.
- ✓ Journal submission must be done on or before every term end.
- ✓ Two tests will be conducted according to the given model by the University for Internal Assessment of each subject. The average of the two scores of internal assessment will be considered as the final grade in the subject and will be printed in the final mark sheet. No re-test shall be held.
- ✓ Failing in eight heads in a year is the maximum limit allowed in the ATKT system. (Maximum five theory exams and remaining Practical/Oral/Internal etc.)
- ✓ Failure to submit the journal in any subject (even for a single subject) will result in detention (i.e. loss of one year). Admission for the next academic year is to be taken by repayment of fees.

10. TERM GRANT

Term Sanctioning (allowing the students to appear for the University examination) entirely depends upon the following: -

- ✓ Minimum of 75% attendance in all subjects.
- ✓ Satisfactory completion of all term work.
- ✓ Practical and Assignments as specified in Syllabus or as instructed within prescribed periods.
- ✓ General behavior of students, both inside and outside the classroom.

11. EXAMINATIONS

Every semester, the University of Mumbai conducts end semester examinations and two internal class tests are conducted by the college for every subject. The performance in these tests is considered for grade calculations as per the MU circular. The submission of term work in each subject should be taken very seriously. If a student fails to submit his/her Term or Sessional Work before the prescribed date, he/she will not be allowed to sit for the concerned examination.

12. USING UNFAIR MEANS IN THE EXAMINATIONS

During an examination, if a student is found either possessing any material (written or printed) with the intention of copying or actually copying, he/she is liable to be severely punished according to the rules laid out by the University. In such circumstances, a student may either be debarred from appearing for the entire examination or expelled from the University of Mumbai for a minimum period of three years as per the decision of "Unfair - Means Committee".

13. ATTENDING LECTURES/LABS

- 1. As per university 75 % attendance is compulsory.
- 2. To monitor the academic progress as well as the overall development of our students, we nominate one staff member as a faculty adviser to whom a student can approach for guidance. For day to day functioning, class teachers and class counsellors are associated with each class. The students as well as their parents are welcome to contact either the class teacher/class counsellor or the faculty adviser any time during the session.
- 3. While the lecture or the lab is in progress, a student is supposed to keep his/her mobile phone switched off, failing to which severe punishment can be given.

14. INFRASTRUCTURE AND SALIENT FEATURES

The VESIT campus strikes a balance between modern facilities and natural surroundings to create an atmosphere that is ideal for students to learn, explore, and grow.

- ✓ Sprawling Complex
- ✓ Equipped Library having 49337 books, 63 Journals,1245 E journals and Book Bank Facility
- ✓ Professional Societies
- ✓ Active Placement Cell
- ✓ Strong Alumni Association with Alumni Web Portal
- ✓ Conference Hall
- ✓ Seminar Halls
- ✓ Auditorium
- ✓ Music Room
- ✓ Spacious Canteen offering hygienic food
- ✓ Huge Playground
- ✓ Amphi Theatre
- ✓ Recreation Room
- ✓ Smart Class Rooms
- ✓ Seminar Rooms

Labs set up in collaboration with industries

- ✓ Automation Industry Association Lab
- ✓ Cloudera Big Data Analytics Lab, U.S.A.
- ✓ National Instruments Labview Academy, U.S.A.
- ✓ D Link Academy, National Instruments (U.S.A.), Bangalore

16

- ✓ e-Yantra-MHRD-IITB initiative
- ✓ Zonal partner with *leadingindia.ai*

15. FEE STRUCTURE

FIRST YEAR - BACHELOR OF ENGINEERING

Academic Year 2020 - 21

	General	Quota	OBC / I	EBC	SC/ST/VJ/D	T/NT/SBC	TFWS]	
Particulars of Fees	Maharashtra Board	Other University / Boards (CBSE / ICSE)	J & K / GOI and NEUT	J&K PMSS S						
Tuition Fees	101619	101619	50810	50810	0	0	0	0	15000	0
Development Fees	11381	11381	11381	11381	11381	11381	11381	11381	5000	0
Caution Money	1000	1000	1000	1000	1000	1000	1000	1000	1000	1000
Exam Fees	2134	2134	2134	2134	2134	2134	2134	2134	2134	0
Gymkhan / Sports and Other Fees	546	546	546	546	546	546	546	546	3500	0
Enrollment Fees	200	200	200	200	200	200	200	200	200	0
Yuvaraksha Insurance	116	116	116	116	116	116	116	116	116	0
Eligibility Fees	0	300	0	300	0	300	0	300	300	0
Grand Total	116996	117296	66187	66487	15377	15677	15377	15677	27250	1000

FIRST YEAR - MASTER OF COMPUTER APLLICATIONS Academic Year 2020 - 21

	General Q	uota - MH	OBC /	EB C	SC/ST/VJ/I	OT/NT/SBC		General Quota
Particulars of Fees	Mumbai University	Other University / Boards (CBSE / ICSE)	Mumbai University	Other University / Boards (CBSE / ICSE)	Mumbai University	Other University / Boards (CBSE / ICSE)	J & K / GOI and NEUT	OMS
Tuition Fees	89929	89929	44965	44965	0	0	15000	89929
Development Fees	10071	10071	10071	10071	10071	10071	5000	10071
Caution Money	1000	1000	1000	1000	1000	1000	1000	1000
Exam Fees	3152	3152	3152	3152	3152	3152	3152	3152
Gymkhan / Sports and Other Fees	546	546	546	546	546	546	3500	546
Enrollment Fees	0	200	0	200	0	200	200	200
Yuvaraksha Insurance	116	116	116	116	116	116	116	116
Eligibility Fees	0	200	0	200	0	200	300	300
PG Registration	825	825	825	825	825	825	825	825
University Share	2500	2500	2500	2500	2500	2500	2500	2500
Grand Total	108139	108539	63175	63575	18210	18610	31593	108639

15. FEE STRUCTURE

FIRST YEAR - MASTER OF ENGINEERING Academic Year 2020 - 21

	Fees General Quota - MH SC - MH		SC - MH	General Quota	
Particulars of Fees			Boards (CBSE /	OMS	
				_	
Tuition Fees	94425	94425	0	0	94425
Development Fees	10575	10575	10575	10575	10575
Caution Money	1000	1000	1000	1000	1000
Exam Fees	3152	3152	3152	3152	3152
Gymkhan / Sports and					
Other Fees	546	546	546	546	546
Enrollment Fees	0	200	0	200	200
Yuvaraksha Insurance	116	116	116	116	116
Eligibility Fees	0	200	0	200	300
PG Registration	825	825	825	825	825
University Share	2500	2500	2500	2500	2500
		A	1		
Grand Total	113139	1135 <mark>3</mark> 9	18714	19114	113639

DIRECT SECOND YEAR - BACHELOR OF ENGINEERING Academic Year 2020 - 21

Particulars of Fees	General Q <mark>uota</mark>	OBC / EBC	SC/ST/VJ/DT/NT/SBC
	Since 1962		
Tuition Fees	93777	46889	0
Development Fees	10223	10223	10223
Caution Money	1000	1000	1000
Exam Fees	2134	2134	2134
Gymkhan / Sports and Other Fees	546	546	546
Enrollment Fees	200	200	200
Yuvaraksha Insurance	116	116	116
Eligibility Fees	300	300	0
Grand Total	108296	61408	14519

16. SPECIAL PROGRAMMES

16.1 LOOKING BEYOND SYALLABUS

Looking beyond syllabus scheme was launched in the year 2011, to expose the students to evolving technologies and opportunities and work as teams on multidisciplinary projects under the mentorship of a faculty member assigned to them, in their area of interest/passion that may not necessarily be their stream of study.

The main objective of looking beyond the syllabus of technical education is to infuse prospective technocrats with the ability to apply basic science and experimentation skills, to understand problems in the industry, academia and humanity at large and also be able to develop practical solutions. It also aims to model them into wholesome professionals in the field of Technology. Looking beyond syllabus also prepares the students to use their skills with a strong base for higher education and acquisition of knowledge to sustain a life-long career in related areas.

This year because of COVID 19, LBS projects were exhibited in e-exhibition on 15th and 16th July 2020.



16.2 AWAKENING THE SCIENTIST

Awakening the Scientist is an opportunity given to the First year students to take the first step in the transformation of a student into a professional. It is a group activity. The objective is to identify the science, engineering, and technology concomitant with gadgets, equipment and tools used in day to day life and also to relate the past, present, and future of them.

16.3 SKILL ENHANCEMENT LECTURES

VESIT believes in giving its students an invigorating and vitalising academic environment. The syllabus of Mumbai University is taught very efficiently by our faculty. An added edge is given to our students by inviting connoisseurs from the industry and research institutions. This endeavor is termed as 'Skill Enhancement Lectures'.

16.4 TINKERERS' LAB: STUDENTS' SECRET TO CREATIVITY

"It's not about ideas. It's about making ideas happen." - Scott Belsky

The Tinkerer's Lab at VESIT was started started in the Academic Year 2015-16.with an agenda to improve technical skills of students through peer learning. It also aimed at using the available resources in the college optimally and to make judicious use of e-waste of various labs. It is currently operational in three departments viz. Department of Electronics, Department of Electronics and Telecommunication and Department of Instrumentation.

The successful journey started from the year 2015-16 were students organized Line Follower Robot Workshop and done Pooja Robot project



In 2016-17, Students worked on a project named Aerial Serving **System Integrated** with Android(ASSIA) which was selected National Level **Project** Competition **DST** Texas Instruments Inc. India Innovation Challenge Design Contest 2016 (TI-IICDC 2016). Also Won Second Prize at National Level NIYANTRA 2016 Competition for the project named SAPER (Semi Autonomous Pipeline Exploration Robot).



In the Year 2017-18, 40 students joined Tinkerer's Lab and worked on about 18 Projects.



302





In the Year 2018-19 students named Saurabh Koturwar ,Yash Patil,Omkar Kajrolkar won Best Team Award in Deep Blue Competition organized by Mastek for Project Named "E- Clean Toilet".



Students named Pitam Mali, Mahesh Kulkarni, Varad Tole, Mugdha Sonawane won Second Prize with cash prize of Rs.25,000/- at Story of Makers for the project "Semi-Automatic Rooftop Solar Panel Cleaning Robot"

21

Facebook page for Tinkerer's Lab https://www.facebook.com/tinkerers.lab.vesit/

16.5 VESIT ON COURSERA

VESIT is offering an opportunity to do free Coursera courses to Faculty, Lab Staff, Non Teaching Staff & students. For students, three subjects per semester are selected and the respective Coursera courses have been identified by the department. Students were supposed to utilize the lockdown period to finish with these three courses.





The number of current members are 1982 with total 6948 enrollments in courses.

16.6 VESIT SWAYAM NPTEL LOCAL CHAPTER

NPTEL is the largest provider of MOOCs in India today, especially the Engineering stream, with a credible proctored certification exam that clearly qualifies and differentiates the learners who do these courses. NPTEL has been offering Open online courses along with certification, developed by IIT and IISc faculty.

The SWAYAM NPTEL Local Chapter of VESIT, is happy to announce that the Faculty and students are actively participating and excelling in the exams at All India Level.

Faculty Achievements

	July - Nov 2019 Semester	Jan - April 2018 Semester	July – Nov 2018 Semester
Total participated	44	40	41
Elita+gald*	2	3	3
Elite+gold*	(Toppers 1% and 5% result)	(Toppers 2% and 5% result)	(Topper 5% result)
Elita Cilvan*	13	13	3
Elite+Silver*	(Toppers 2% and 5% result)	(Topper 5% result)	(Toppers 1% result)
Elite*	21	13	30 (Topper 5% result)
Successfully completed	4	10	5

^{*}Score \geq 90 - Elite+gold, 75<=score \leq 69 - Elite+Silver, score \geq 60 - Elite, 59<=score \leq 40 - Successfully completed

22

Faculty contributed in Translation of Course contents into Indian languages too.

17. MAJOR LABORATORIES

17.1 DEPARTMENT OF ELECTRONICS ENGINEERING

Basic Electrical & Electronics Engineering Lab

This Lab is equipped with 1-phase and 3-phase Motors, different types of Transformers, A.C. and D.C. Generators, Motors, Trainer kits for network theorems, analog and digital measuring instruments etc.

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VLSI Design and Communication Lab

The department is a member of Cadence University Program; subscribed to cadence VLSI design suite and microwind running on Linux & windows clients. Also in this lab all analog and digital communication kits are available for experiments of subjects Digital Communication, Principles of Communication Engineering.



Basic Electronics Circuit Lab

This lab is equipped with sufficient number of CRO, Function Generator, DMM etc. Softwares required for design and simulation of electronic circuits are also available.



Digital Circuit Design Lab

Lab is equipped with softwares for digital and analog circuit simulation FPGA & CPLD training kits, Digital measuring instruments, Advanced Networking Technology etc. This Lab also extends support to the final year students for their Project Work.



Advanced Microprocessor and Embedded System Lab

The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. All the electronic testing equipment are available, Digital Trainer Kits and Demonstration kits (RAM & ROM), Universal Programmer, Cards for ISA, PCI and different connectors are kept for better understanding of computer



architecture. The lab is equipped with 8085, 8051 and 8086 boards along with ARM Cortex M based Microcontroller boards. This lab is also recognized by E Yantra for embedded and robotics experiments.

23

Power Electronics & Robotics Lab

The Lab is adequately equipped with Test equipment for Power Electronic Devices & circuits, various demonstration kits & measuring instruments are available in this lab. This lab also has extended support for Robotics.



Electronics Workshop – PCB lab

Lab Facilities – PCB Exposer, Film Exposer, Dip Coat Machine, Etching Tank, Drilling Machine etc. Students perform their Electronic Workshop project designing, testing and implementation in this lab. This lab is equipped with all the facilities required for PCB fabrication.



Project Lab

This lab is dedicated for various Project work for Second, Third and Final Year students. This facility is exclusive for projects so that students should learn various subjects through Project Based learning. This lab is fully equipped with hardware, software, debugging & testing tools required for Electronics Project.

IOT lab

IOT lab is intended to provide all latest IOT components for related subjects. Also this lab is equipped with IOT components such as Arduino, Raspberry Pi, WiFi Model, Zigbee model and IOT simulation software.

Robotics and AI Lab

It is a state of art facility to create awareness about various Robotics Technologies and Artificial Intelligence fields. This lab is equipped with 3D printer, Humanoid, mobile robots. This lab will nurture the environment of robotics through various Projects. This lab is fully equipped with required resources for students as well as researchers.

17.2 DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

TV, VLSI & Satellite Communication

This lab is equipped with Computers and Professional Softwares like Xilinx Vivado 16.2, Mentor Graphics and hardware like Zybo FPGA kit, Analog discovery kit(Digilant), Papilio FPGA kits(Gadget Factory), Digital trainer kits, CRO, DSO, Function generators, Power supplies with various ICs required for LIC,DE subjects. This lab is also equipped with satellite trainer kits used for Satellite



communication practicals. Few Internship projects are also carried out in this lab.

Analog & Digital Communication

This Lab is Equipped with both Hardware and Professional licensed software facilities.

This lab is equipped with Digital Storage Oscilloscope (DSO), Cathode Ray Oscilloscope (CRO), Spectrum Analyzer, Function Generator, Signal Generator, DMM, Power supply, Computers (HCL,HP, DELL) and Various Analog and Digital Communication



Trainer kits (Kitek, Edkit, Falcon/Akademika, Scientech, Sigma, DCL make)

Major Software for Students used in the lab are CommSim 7. Matlab R2016a, Netsim, Python, Cisco Packet Tracer, Wireshark. Libre office, Scilab, Net protector antivirus etc.

Communication Lab (RF, Microwave & Antenna Communication)

Lab is equipped with ZVH8 Handheld Cable and Antenna Analyzer with Spectrum and Network facility- [100KHz-8GHz] (Rohde and Schwarz), DSO-(Agilent, Aplab, Tektronix, Rigon), Spectrum Analyzer-50MHz (Hameg), Spectrum Analyzer- 3GHz (Agilent), Standard Signal Generators for RF-range, Network Analyzer-3GHz (Agilent), Transmission Line Trainer, Computers with latest configuration etc. Software like MATLAB, Ansoft, PCADD are used for simulation.



DLink Academic, Network & Wireless Communication

License software is NetSim Academic 11.0 and Matlab 2011b (8.5.0.197613) with Image Processing Toolbox and Neural network Toolbox, Specialized D-Link type of equipments: L3 Management Switch, Air Premier N Access point, Unified Access point, Wireless Controller and Unified Switch



Optical fiber Comm & Network

Lab is equipped with Optical Time Domain Reflectometer, Fiber optic kits, Computers with latest configuration etc. COMSOL multiPhysics (5.3) Software, Netsim academic (7.0), MATLAB(2011b) are used for simulation.



Innovation Lab & Tinkerer's Lab

This is an innovation lab for Departmental projects as well as ME projects. This lab is equipped with computers and components required for projects. All projects from competitions are made here. This lab is basically made for helping the students to tinker with projects. Technical workshops and competitions are conducted every year by tinkerer's lab. All these workshops and competitions are run by students with aid from teachers.



Equipment in lab :- Raspberry pi 3b+ board kits including HDMi cables, Camera etc.(20 No.), Arduino Mega boards, Node MCU, DC motors, Soldering gun kits, DC batteries etc.

This lab also includes a few MSP430 kits from Texas instruments.

Microprocessor/Microcontroller & Embedded System/PCB Lab

This Lab is equipped with a wide range of equipment, devices and software, which helps the students to practice electronics circuit assembly and testing and also provides a deep insight into the world of electronics practice. It is the skeleton of applied electronics.



Microprocessor/Microcontroller Lab

The purpose of this laboratory is to train the students to be familiar with the software and hardware of microprocessors/ Microcontrollers so that they can gain enough experiences to meet the demand of the microprocessor era. This Lab is equipped with Microprocessor kits (8086), Microcontroller kits (8051), Interface boards, Interrupt controller, Timer, ADC, DAC, Serial Communication, Stepper motor, Traffic control, Keyboard and display control, multimeter (digital), PC based interface, Oscilloscopes, Function generator, power supply, RS 232c cable & connector, Universal programmer, DSO.

Embedded system Lab

This Lab is equipped with PCs (HP i5/HCLi3/Intel HCLCore 2 Duo 2.83 GHz, 4GB RAM, 500 GB HDD, USB port, HP optical mouse, keyboard, 17" LCD monitor), software packages, embedded processor development modules (STM32F401-Nucleo 64). Various types Electronic sensors & modules. Keil software for ARM processor, C Compiler, RTOS and target boards.

Electronic Devices & Integrated circuits Lab

This lab is specially equipped for second year students. The purpose of this laboratory is to train the students with hardware experiments and simulation softwares. Students can perform the experiment by mounting the components on the breadboard as per the circuit diagram and can check the output in the form of waveforms on CRO. Students can also verify the results of hardware experiments using computer



simulation software like LTSPICE. Lab is equipped with CROs, Frequency generator, Power supplies, multimeters, bread-boards, Trainer kits and different types of electronic components which are required to perform practical's/experiment and mini-projects of related subjects.

Lab is equipped with high end computers on which students can perform simulation of different experiments.

17.3 DEPARTMENT OF COMPUTER ENGINEERING

All the computer laboratories are spacious and well equipped with the latest configuration and well networked machines.

Database Management Systems

A well-equipped lab comprising the latest computers installed with various Database tools & Servers like Microsoft SQL Server 2008, Oracle 11g, Visual Studio 08, Matlab R2015 Server and open source softwares like Netbeans, Turbo C, Java.



Multimedia System

A dedicated lab that caters to requirements of Web Engineering & Multimedia with latest softwares like Maya 9.0, Adobe Macromedia Studio, Macromedia Flash, Dreamweaver, Autodesk 8.0 & Fireworks.



Microprocessor and Robotics

This lab is equipped with the Trainer kits of Microprocessor 8085, 8051 & Advanced 8086 Trainer Kits and several interfacing kits. The lab also houses computers with tools such as TASM & MASM.



Analog & Digital communication Lab

The lab is equipped with Digital logic trainer kits, CRO, function generator, dual DC power supply, communication kits - AM, FM, TDM, PAM, PWM, PPM. The courses conducted are Digital Logic & Computer Architecture.



Networking

This lab aims to provide an environment that aids in effective learning of Networking & Mobile Computing concepts. Systems use the Linux Platform having several open source development toolkits such as Packet Tracer, NetSim Academic, Sun Java Wireless Toolkit, Visual Paradigm along with Matlab R2015, Matlab R2017, Cloudera, CodeBlocks, Turbo C, Scilab.



Object Oriented and Programming

To aid in proper understanding of System Design and programming concepts, this lab houses computers with several system design tools like Visual Paradigm, MS access, MS SQL Server 2008 and Eclipse Ganymede Arch Studio are some of the several software design tools.

27



SPECIALIZED LABS

Cloud Computing Lab

This laboratory caters to the final year course and has open source softwares like Open stack, OwnCloud, VirtualBox, XEN and KVM. These resources are used for various projects.

Machine Learning & Big Data Analytics Lab

This specialized lab is for final year elective courses such as Big Data Analytics and Machine Learning. It is equipped with high ended computers. Cloudera distribution for Apache Hadoop version 5.1.3 which comprises Hadoop, Hive, Pig, Hbase that is used along with other open source tools like R and Mahout.



Project Lab

This lab is used by final year students for their project work. This lab has several computers with softwares like MATLAB, Visual Studio 2010, Visual Paradigm, Turbo C, Neebeans, MS Project, Codeblock, Eclipse.



MODROB Lab

Under (MODROBs grant – Ref No 12/AICTE/RIFD/MOD (Policy4) Pvt109/201213), this specialized lab is an extension of the Multimedia System Lab which is high end lab and includes 3-Apple Machines, 2 Aakash Tablets, 1 iPad and 1 Interactive kiosk under the AICTE MODROB Grant.



High Performance Computing Lab

The lab is equipped with high configuration GPU machines for heavy computational projects.

28

17.4 DEPARTMENT OF INSTRUMENTATION ENGINEERING

Process Automation Lab

Process Automation Laboratory is Equipped with Latest Technology Control systems including PLC'S with simulators along with SCADA software & DELTA V Distributed Control system from well-known industry brands like Allen Bradley & Emerson Process Management. This Laboratory facilitates students to learn Basic PLC Programming, PLC Panels and simulators, DCS Features, DCS based Control systems etc.

Process Instrumentation Lab

Process Instrumentation Laboratory is a State of the Art Experimental Facility made up of 14 pilot plant set ups. This was designed & developed, to enable students to identify the Instruments & Control systems as well as to provide demonstration of working instruments to the students. This laboratory is utilised to study and understand various types of Processes and the role of Instrumentation involved in the same.



It also creates an Industrial Environment for the students and helps them to grow with the Instrumentation utilization. Students can perform various supporting Instrumentation activities like Calibration, Tuning, Mounting, TroubleShooting, Loop Testing, checking which provides them with Hands on exposure to make them comfortable and answerable for their campus preparation. Laboratory consists of various Field Instruments like SMART Transmitters, Flow meters & Control Valves along with accessories. It is also equipped with Single Loop PID Controllers & Variable Frequency Drives.

Transducers & Sensors Lab

This is one of the Fundamental Labs which facilitates students to learn various sensors & Transducers used in Process & Factory Environment. Students can learn Testing & Performance characteristics using Calibrators. It also provides Hydraulic set up that allows studying Flow Measurement Experiments, Level Monitoring set up using various Instruments like Rota meter, Orifice / venturi meters & ultrasonic Level Transmitter. Lab also has Dead weight Testers & Master Pressure Gauge to provide Testing of Pressure Gauges.

The lab setups are also utilised for training L&T industry trainees. Lab has facilities to have hands-on advanced sensors like accelerometers, gyroscope etc.

Lab has various instruments like power supply, CRO, signal generators which are used for performing signal conditioning related experiments.

Lab is also equipped with nuclear instruments like single channel analyzer, nuclear sources and scintillation detector that helps in performing experiments on Nuclear Spectroscopy.

Biomedical /Analytical /Nuclear Instrumentation Lab

This Laboratory has been established to familiarize the students with capturing, analyzing and processing different Bio signals. Biopac is an integrated instrument which the students use for such experiments. Physiology Lab kits and diagnostic systems are also used for training the students in the area of Biomedical Instrumentation.

Analytical laboratory is equipped with sophisticated instruments such as Gas Chromatography and Spectrophotometers which help students to understand the concept and working of analytical instruments. Also nuclear instruments such as Single Channel Analysers are available for better understanding of nuclear concepts and measurements, by the students.

Control Systems & Digital Signal Processing Lab

The lab is equipped with Pentium-IV (IBM) and HCL computers with latest software such as Proteus VSM, Turbo Assembler 6.1, Macro Assembler, Embedded C for AVR. All the electronic testing equipment are available, Digital Trainer Kits and Demonstration kits (RAM & ROM), Universal Programmer, Cards for ISA, PCI and different connectors are kept for better understanding of computer architecture.

Analog & Digital Lab

Laboratory courses on Analog 7 Digital Electronics are conducted in this lab. The Lab is adequately equipped with Test equipment for Electronic Devices & circuits, various demonstration kits & measuring instruments are available in this lab.



17.5 DEPARTMENT OF INFORMATION TECHNOLOGY

The Information Technology department aims to create a high quality academic and research environment, which empowers faculty and students to attain the highest level of excellence as professionals in the field of Information Technology. To achieve its aim, the Department has seven well equipped and spacious Computer Laboratories with more than 270 networked computers and support equipment like Servers, Printers etc.

Big Data Analytics Lab

A dedicated lab that caters to requirements of Big Data Analytics with latest softwares with Microsoft (under dreamspark licence), Visual Studio 2018, Apache Hadoop, Apache Spark, Apache Storm, Mongo DB, R Programming Environment.



Database Lab

A well-equipped lab comprising the latest computers installed with various Database tools & Servers like Microsoft SQL Server 2014 & MS Access, (under dreamspark licence), and softwares like R Studio, Visual Studio 2018, Netbeans, Java etc.



Software Engineering and Project Management Lab

A well-equipped lab comprising the latest computers installed with various microsoft softwares (under dreamspark licence), Visual Studio 2018, Netbeans, Microsoft Project, Java, Linux, VM Player etc. with wired and wireless lan facilities.



Networking and Security Lab

Aims to provide an environment that aids in effective learning of Networking & Systems on the Linux Platform and several development toolkits for mobile computing are some of the features. It consists of Packet Tracer, NetSim Academic, Sun Java Wireless Toolkit and Microsoft (under dreamspark licence).



Programming Lab

To aid in proper understanding of System Design and programming Concepts, this lab houses computers with several system design tools like Visual Paradigm, MS access, MS SQL Server 2015 Microsoft (under dreamspark licence), Python and Netbeans are some of the several software estimation tools.



Project Lab

One of the labs is exclusively reserved for final year students with high end machines to work on their projects.

Specialized Labs

Machine Learning & Big Data Analytics Lab

This specialized lab is for final year Elective courses Big data analytics and machine learning. It is equipped with the latest high end computers. Cloudera distribution for Apache Hadoop version 4.4.1 which comprises Hadoop, Hive ,Pig, Hbase is used along with other open source tools like R, Mahout.

Advanced AI Cloud Computing Lab (Cloudera Lab)

This Lab caters to the final year course and has open source softwares like Open stack, OwnCloud, XEN, KVM, VirtualBox and Cloudera big data framework for Cloudera training program. Also the resources are used for various projects.

IOT Lab

One of the labs is used for IoT projects with an adequate number of components. This is a research platform exploring the potential of crowd sourcing and Internet of Things for multidisciplinary research with more end-user interactions. It gives the power to the crowd to be at the core of the research and innovation process. It gives the power to change the world and the way we understand it.

ME Lab

One of the labs is exclusively reserved for Master of Engineering students with high end machines to work on their Lab practical work and projects.

17.6 DEPARTMENT OF MASTER OF COMPUTER APPLICATIONS

Database Lab

Laboratory courses conducted: - Database Managements Systems with SQL and PL/SQL, Advance Database System - Parallel and Distributed Database, Data warehousing and Data Mining Concepts, Software Testing-Manual Testing and Automated Testing with Selenium Tool and Machine Learning-Supervised and Unsupervised Learning methods. Software: Oracle11g, Pentaho, Weka7, UXD tools, R Studio, Python, Visual Paradigm etc. The Lab is equipped with 30 machines with i5 configuration.

Programming Lab

Laboratory courses conducted: - Programming Concepts, C++ Programming and Data Structure, Software Engineering, Computer Graphics and Image Processing, Object Oriented Concepts through Java, Web Applications through Servlets, JSP, EJB and Operating Systems practicals using Fedora Linux Server, Mobile Applications, etc. Lab is equipped with 30 machines with i5 configuration. Other Software's: Project 2007, Oracle 11g, UXD tools, Android Studio etc.

Software Engineering & Project Lab

Laboratory courses conducted: - Software Engineering, Computer Networks, Software Testing, Unified Modeling Language lab and Summer project. The lab is equipped with 30 machines with i5 configuration. Software's: SQL SERVER 2008 R2, Cisco packet Tracer, Visual Studio 2010, MS Project 2007, Visual Paradigm, etc.

Web Technology Lab

Laboratory courses conducted: - Computer Network Lab, Web Technology, .NET, Mobile Applications, User Experience Design (UXD). Lab is equipped with 30 machines with i5 configuration. Softwares: Android studio, Visual Studio 2010, Visual Studio 15 prof., SQL SERVER 2008 R2, Cisco packet Tracer etc.

All the Labs are well equipped with the latest PC and High Speed and adequate Bandwidth Internet Connection.



17.7 DEPARTMENT OF HUMANITIES AND APPLIED SCIENCES

Engineering Mechanics / AutoCAD

A well-equipped, fully AC & spacious lab as per the syllabus requirements in Engg. Mechanics and AutoCAD. The AutoCAD lab total 35 PC (one student one PC policy is followed in practical duration). Each PC installed with AutoCAD 2017 provided by Autodesk Students version for the Institute. Projector teaching practice is used during practicals. It enhances effective learning of the subject. Engg. Mechanics lab is

equipped with experimental set up as per the syllabus.



Engineering Physics

A spacious and well equipped lab as per the syllabus requirement of Engineering Physics. We have a darkroom with spectrometers, traveling microscopes, Newton's ring setup, different gratings and light sources including He-Ne LASER, have Pt-Resistance thermometers and Photodiodes to perform many experiments related to Optics. The lab also possesses a set of crystal models, Hall effect



setup, Hysteresis setup, photocell, ultrasonic distance meter, cathode ray oscilloscopes, energy band-gap measurement setup and supercapacitors, to name a few.

Engineering Chemistry

Chemistry lab has adequate facilities like fume hood, exhaust fans to keep pollution free atmosphere in the lab. Lab is equipped with instruments like colorimeter, flame photometer which helps students to get exposure to these spectrometry techniques. Lab also has equipments like Redwood viscometer, Abels & Pensky Martin flash



point apparatus along with Electric rectifier for electroplating, electronic balance & oven. sufficient storage cabinets for chemicals & glasswares. Chemistry experiments are planned based on the syllabus given in the curriculum.

C Programming Lab

A well-equipped lab consisting of Computers installed with Windows 7 Professional, Edubuntu 14.04 LTS, LibreOffice 4.1, LTSP (Linux Terminal Server Project), Code::Blocks 13.12, DosBox 0.74. This lab is dedicated only for C Programming Subject for First Year of Engineering (FE) students

34

Drawing Hall

A well equipped lab with approximately 540 drawing boards and 66 drawing tables for students. The overhead projector and different solid shapes model along with the cut section are available at the drawing hall to sharpen their skills to understand the subject of Engineering Graphics.



Workshop

A well-equipped lab to perform basic workshop practices. As per the curriculum it is conducted in sem-I and sem-II. Lab contains major equipment like wood turning m/c, welding equipment, drilling m/c, sheet metal work equipment. In two semesters sheet metal work, fitting, wood turning process, welding etc. are conducted.





35

18. INDUSTRY COLLABORATION

AIA (AUTOMATION INDUSTRY ASSOCIATION)



System Demonstration by Mr. Prasad Reverkar. Siemens LTD

VESIT is the only technical institution in Mumbai, which is authorized by AIA (Automation Industry Association) to be a 'Competency Development Centre' for conducting course on "Industry Automation" as a part of its Campus Connect Initiative. The newly developed Integrated Automation Lab was inaugurated by Padmashri Dr. G. D. Yadav, Director, Institute of Chemical Technology, Mumbai on 2 March 2016.

NI LABVIEW ACADEMY

V.E.S. Institute of Technology has started NI LabVIEW Academy in collaboration with National Instruments (USA), Bengaluru on 9 July, 2013. This is the first of its kind in the Mumbai region. After completing this course, the participants will be able to appear for the CLAD (Certified LabVIEW Associate Developer) examination, conducted by National Instruments (NI), USA. The CLAD distinguishes basic proficiency in using NI LabVIEW to develop applications. The certification and course work conducted by the academy is useful for developing expertise in the LabVIEW environment for career advancement and employment opportunities. NI LabVIEW Academy will provide a strong platform for interaction with industries and R & D centres to undertake research projects. Under this academy, training programmes are conducted for UG and PG Engineering staff and students.

E-YANTRA LAB SETUP INITIATIVE (ELSI)

V.E.S. Institute of Technology is equipped with state of art facility for Robotics, e-Yantra Robotic Lab under eLSI (e-Yantra Lab Setup Initiative). e-Yantra Lab Setup Initiative (eLSI) is an initiative by the IIT-Bombay under which colleges are encouraged to set up robotics labs. It is designed as a scalable and sustainable approach that addresses infrastructure creation and teacher training to create an ecosystem at the



colleges to impart effective engineering education. This facility is available to the students, so that they can focus on embedded system, robotics projects.

e-Yantra Lab is equipped with Firebird V , Spark V , Qu-Bot robots, various hardware, software platforms required to develop embedded projects. Every year 20-25 teams of students participate in Robotic competition.

ACCENTURE LAB

"VESIT-Accenture Innovation Centre" is a specialized lab developed in accordance with VESIT's MoU with Accenture. This lab is used by Accenture to conduct their Head Start Foundation Program (HSFP) for students selected through campus placement, faculty development program and other similar activities.

CLOUDERA LAB

The Big Data Analytics lab in collaboration with Cloudera provides a high end distributed environment for Big Data processing, through which training is provided in specialized area 'Hadoop' and projects are undertaken to apply the concepts using Cloudera tools. A six week course is regularly conducted for the final year students.

VESIT D-LINK ACADEMY LAB

D-Link academy at VESIT started on 1 April 2015. This academy trains students in batches which will equip them for better placement opportunities. As a part of this venture, a specialized laboratory is set up in EXTC Department for wireless communication. Under this academy, our faculty members were trained by D-Link to conduct this course for our students. This is an excellent opportunity for our students to get industry certification in their own campus

MICROSOFT CERTIFICATION

VESIT has recently signed up an MOU with ATS learning solutions for facilitating Microsoft certifications and arranging short term skill development workshops on Cross Platform Mobile Application Development, Internet of Things, and Cyber Security.

VESIT ADITYA JYOT EYE INNOVATION CENTRE



Inauguration of Aditya Jyot Eye Innovation
Center on 21 August 2019

MoU was signed during the Programme. Objective of MoU/ Responsibilities of the Parties

- To conduct research projects and publish the outcome of the research results in journals.
- ✓ Joint funding proposals and grants for collaborative research.
- ✓ Collaborating on innovative technologies and methods which can benefit society.
- ✓ Collaborating in workshops and educational conferences for the benefit of the clinical and engineering communities.

ORACLE ACADEMY

The program provides the tools educators need to engage, inspire, and prepare students around the world to achieve their dreams. As Oracle's global philanthropic educational program, it advances computing education around the world to increase knowledge, innovation, skills development and diversity in technology fields. VESIT is an institutional member of Academy from November 2016 and more than 1000 students have completed certifications. Various courses like Oracle Application Express – Application Development Foundations, Database Design and programming with SQL, Programming with PL/SQL, Artificial Intelligence with Machine Learning in Java etc. are offered free of cost. They can also appear for paid professional certification

LeadingIndia.ai

As a new initiative in Academics and Research, VESIT has become one of the Zonal Lead Partner with LeadingIndia.ai, Nationwide Initiative. The mission of this initiative is to promote Deep Learning and AI skills mainstream in India, to fulfill the trilateral needs of entrepreneurship, Industry-academia partnership, and application-inspired Engineering Research. 30 Artificial Intelligence & Deep learning projects are identified and students are being encouraged to work for foolproof solutions.

vesAIthon'19



Chief Guest with Dignitaries inaugurating vesAlthon'19

VESIT zonal partner with leadingindia.ai (An initiative by Bennett University and NVIDIA. To promote AI and Deep learning skills in the world. VESIT got the opportunity to host Nationwide AI based Hackathon for a duration of 24 hours among Graduate and Postgraduate students from professional institutes.

The **vesAIthon** was conducted on 28th - 29th June 2019. The theme was AI-based solutions for Societal Benefits, targeting to address the issues and provide technology-based AI solutions for these three categories, Senior citizens, Differently abled and Kids.



Special Prize Innovation : Rs. 10,000/-Winners - We are on Fire, CMR COET, Telangana with Trustee, Shri Boolani & Principal, Dr. J M Nair



First Prize: Rs. 50,000/-Winners - Enlighteners, VNR VJIET, Telangana with Capgemini, Leading India and VESIT Officials

- ✓ Second Prize of Rs. 30,000/- wins by Data X, IIT Kharagpur for Category Elderly Caregiver.
- ✓ Third Prize of Rs. 15,000/- wins by Golden AI Age, CMR COET, Telangana for Category Differently Abled.
- ✓ Special Prize User Experience of Rs. 10,000/- wins by Stellar, Vignan's Inst. Of Info. Tech. Andhra P. for Category Kids Talk to my Friend bot (chatbot).

Our Platinum Sponsors included LeadingIndia.ai and Capgemini. Gold Sponsors included Network Marvels and Silver Partner was Adapty. Our Collaborators were AICTE, NVIDIA, Bennett University, Brunel University London.

VESIT TECH-EXPERT TALKS



Vivekanand Education Society's Institute of Technology



Tech-Xpert Talks

National Level Webinar Series VESIT Industry and Academia Connect



Mr.Visweswaran J Territory Manager (Academic Programs-South India) National Instruments Bangalore 22 July'20



Mr.K.Jayaraman Senior Industry Expert, Success Assurance Partner for National Instruments (Ni India) Academic Relations 23 July'20



Mr.Ajay Kumar Founder & CEO, Cognibot Robotics (Academic Program) National Instruments 24 July 20



Mr.Venkata Bhanu Prasad Manager-Accenture Learning & Knowledge Management Unit 29 July'20



Mr.Vinayak M.Khandeparkar Director-Operations V.S Equipments & Automations Pvt.Ltd. 25 July'20



Mr.Chandrashaekhar Iyer Retired as Exective Director, Bharat Petroleum Mumbai Refinery 26 July'20



Mr.Sagar Jorapur Assistant Manager.Wireless & Digital Marketing Amerson Automation Solutions 28 July'20



Mr.Tanay Kumar Sil Head-Business Devlopment & Education Network, B&R industrial Automation Pvt.Ltd. 29 July'20



Mr.Devesh Rajadhyax Founder and CEO, CereLabs Pvt.Ltd. 26 July'20



Mr.Jatinder Kumar Kautish Director-Data Science & Analytics within Insights & Data,Capgemini India 27 July"20



Mr.Suraj Ghimire Apple India Pvt Ltd Hyderabad Online Learning Center Pvt Ltd 25 July'20



Mr.Sudhanshu Ojha AVP-Govt Affairs, CSR & D-Link Academy 28 July'20



Mr.Gaurav Singh CEO,Dalvik Apps WEORG 30 July'20



Mr.Anand Nijampurkar Project Manager & IT Consultant Capgemini 31 July'20



Padma Shri Prof.Dr.S.Natarajan Chairman and Managing Director-Aditya Jyot Eye Hospital Pvt.Ltd 31 july'20

19. RESEARCH AND DEVELOPMENT LAB

R&D Department undertakes various R&D projects from industries and works on various research problems for M.E and Ph.D. students in VESIT.

Some of the successfully completed R&D Projects are:

- > "Electronic Radioactivity Simulator" in collaboration with BARC, which was funded by BRNS.
- ➤ "Analysis and Monitoring of Degradation of Insulation in H V Transformers" in collaboration with L&T.
- > "Nuclear Detector Characterization" in collaboration with I.I.T, Mumbai.
- ➤ "Sensor network for Agricultural application" in collaboration with TCS and "Low level signal generation" signal processing circuit for Rogowski coil based integrator with low time constant in collaboration with L&T.
- ➤ Low level sensing (from 2mA onwards) in industrial environment, DC level shift correction for sensing true AC signal, Peak detection of low level signals in frequency range of 5KHz to 25KHz, High Speed Time to Amplitude Converter and Leakage detection using Fibre Optics with L&T.
- > Simulation of shielding characteristics using GEANT4 (Software for simulation of nuclear data).

Our students are carrying out research work in the areas of:

- ➤ Low Level Signal Processing systems
- > Reconfigurable ADCs
- Optimized Nuclear Spectroscopy System
- Research in New Techniques for on-line monitoring insulation degradation
- ➤ Research for changes in characteristics of optical fibres with changes in various Parameters like temperature, vibration etc.
- > Universal Research Grade Biomedical Signal processing system
- > Timing spectroscopy systems
- > Application of Kalman Filter to Nuclear Engineering Data and Bayesian Estimation of Nuclear Engineering Data

- ➤ Nuclear data evaluation for 241Am using Kalman Filter Technique
- > Design and construction of precision and sliding pulse generator

20. COMMON INFRASTRUCTURE

LIBRARY

Our library has more than 49337 books and 63 journals of national and international level. The number of standard journals / magazines: Journals – 63, e-journals - 1245. It also has 8000 ebooks.

Library also provides:

- Internet and all e-journal access through college campus as well as off campus
- Open access facility for all
- > IIT Library Membership
- ➤ Membership with NDL
- ➤ Best User facility for student
- > NPTEL lecture facilities
- > Best user get additional book facility



Along with technical updates via latest journals, students can also keep themselves abreast with the latest happenings in the world via newspaper, magazines and internet facility. The library is divided into two sections

Reference Books: - This section stocks recent editions of textbooks. Students can borrow one book at a time against their Library Identity Card. The books/journals in this section are meant for reading in the hall only. The previous year question papers of Mumbai University are also available in this section.

Lending Section: - Students can borrow two textbooks for one week from this section. A fine will be charged if the book is not returned within the given period.

Students are required to abide by the following rules of the Library. Any violation of these rules will lead to a fine or punishment by the Principal.

- a. Every student must submit his/her valid identity at the entrance of the library and reading room. Ex-students of the college, who are members of Alumni Association, can also use the library facilities.
- b. Pencil or ink marking, writing on the pages or any other way of mutilating the books will be treated as library indiscipline. The reader will be held responsible until or unless he/she points out to the Librarian at the time of issue of the book.
- c. The reference books, journals, question paper sets, dictionaries etc. will be issued for reference in the Reading Room only and not to be taken out of the Reading Room.
- d. Students must refrain from disturbing other members in the Library and Reading Hall.
- e. The student should inform the Librarian immediately for any loss or damage to the books borrowed by him/her on the library card.
- f. Any other matter not covered by these Rules will rest at the absolute discretion of the Principal and Librarian.
- g. Students should not bring any valuable articles to the Library. If such things are lost, the Library staff cannot be held responsible for it.
- h. Students should maintain complete silence in the Reading Hall.
- **i.** Students should contact the Librarian if they face any problem regarding usage of library facilities. They should not argue with the staff on duty.

323 - 1

OTHER FACILITIES:

PROJECT ROOM

Separate Project Rooms are provided for each department with the laboratory facilities and computers installed so that the students can carry out their project work even after the college hours.

CANTEEN

The college has a huge canteen, with modern kitchen facility, serving fresh and hygienic food which offers a multi-cuisine variety.

AUDITORIUM

Seminars and Students' presentations are held in the Conference Hall, which has a seating capacity of 250 students. It is equipped with a good sound system along with video and data projectors.

MUSIC ROOM

Music Room is well equipped with instruments such as Flute, Violin, Guitar, Tabla etc. Students can learn and practice after the college hours. Student Council wing "Bliss" (Beauty lies in Soulful Symphony) is in-charge of the Music Room and conducts music competitions and workshops for the benefit of students.

SPORTS

Life of an engineer is incomplete without any extracurricular activities. For a healthy body and mind, apart from the usual workload and stress, VESIT provides its students with a sporting extravaganza SPHURTI.

SPHURTI is an integral part of VESIT's history and it has been so since the beginning of VESIT.

- > Play ground: 8177 sqm
- > Gymkhana and Recreation Room
- > Outdoor games: Football, Cricket, Volleyball, Basketball, Throw ball, Athletics, Discuss throw, Shotput.
- ➤ Indoor games: Carom, Chess, Table Tennis

GIRLS COMMON ROOM:

The college has a provision for girls' common room where the girl students can go for recreation during their free lectures.

AMPHITHEATRE:

The college has a splendid amphitheatre where various events of different councils are conducted. It is used for all the major events of college like music, dance, freshers, farewells, literary events, drama competitions etc.

FACILITY FOR DIVYANG:

The college takes care of the needs of the special students by making provision of lifts, toilets, ramps, giving writers for exams and facilities for recreation like music.

21. TEACHING FACULTY AND ACHIEVEMENTS

VESIT believes that the faculty of an institution determines and immensely contributes to its success and in delivering quality education. The college is well known for its highly knowledgeable and immensely proficient teachers who conduct classes with full zeal and robustness. They have contributed greatly to impart quality education at our Institute. Our teaching faculty includes 33 Ph.D. holders and 24 faculty pursuing Ph. D. Each department also takes initiative to organize "Faculty Development Programs" for helping the teachers to apprise their technical knowledge which in turn helps them to deal with their subject and the projects in a professional manner. The staff members are also encouraged to acquire higher qualifications and enroll in various certificate courses related to their area of specialization.

During the year 2019-20, more than 35 staff members have received research grants from various Organization/Institutes, 16 staff members have filed patents, 4 staff members published books, faculty along with students have published more than 160 research papers in various International and National Journals and Conferences of repute during 2019-20. Six papers among them have received the best paper awards.

21.1 BEST PAPER AWARDS

Dr. Gresha Bhatia, Mrs. Abha Tewari, Grishma Gurbani, Sanket Gokhale, Naman Varyomalani, Rishil Kirtikar, Yogita Bhatia and Shefali Athavale received 'First position in Track 2-Big Data Management award' for the paper titled "Extraction of tabular data from PDF to CSV files" in the International Conference on Data Management, Analytics and Innovation 17-19 January 2020.



Dr. Gresha Bhatia, Deepak Tejwani, Shubham Shinde, Rohit Vinod and Kuldeep Khalsa have received Best Paper award for the project titled "Predicting stock movements using News headlines" in 1st International Conference on Computing Technologies for transforming the Automated World-2020 (ICCTAW-2020) organized by Department of Computer Engineering, Atharva College of Engineering, in association with SERSC Journals (Indexing in web of sciences), ISSN: 2233-7857.

Prof. Kavita Tewari and Dr. Ramesh Kulkarni have received Best Paper award for the project titled "Two-dimensional digital image correlation for metal deformation measurements" in IEEE-5th International Conference on Computing Communication Control and Automation (ICCUBEA-19) September 19-21, 2019 and for the project titled "Computation of strain in the deformed pearlitic steel using digital image correlation techniques" in IEEE International Conference on Advance in Computing, Communication Contro (ICAC3-19) December 20-21, 2019.

Kapil Ramrakhiani, Muskan Khatri, Disha Rohra, Saloni Punjabi and Mrs. Abha Tewari have received Best Paper Award for the paper titled "SWAYAM: A Conversation Aid App" in International Conference on Industry 4.0, heldo at Saraswati college of Engineering, Kharghar on 3rd April 2020.



Mrs. Vidya Zope, Gauri Beloshe, Prajakta Madekar, Shweta Mungase and Purvi Sawant, have received Best Paper of the Session award for the paper entitled "EMOTSQUAD: Emotion Detection and Attendance Management System" in 1st International Conference on Computing Technologies for transforming the Automated World-2020 (ICCTAW-2020) organized by Department of Computer Engineering, Atharva College of Engineering, in association with SERSC Journals (Indexing in web of sciences).

Dr.Maya Bhat secured the **second position** in **the Poster Presentation** Competition which was held as a part of the International Conference on Advanced Functional Materials. This conference was held from 23 January 2020 to 25 January 2020 at Nagpur. It was organized by Kamala Nehru Mahavidyalaya in association with ACT(Association of Chemistry Teachers).



21.2 Microsoft AI for Earth



Dr. (Mrs.) Nupur Giri, With AI for Earth May Summit team



Dr. Gresha Bhatia, with AI for Good Summit team

Dr. (Mrs.) Nupur Giri has received sponsorship of Rs. 1,37,000/- to attend Microsoft AI for Earth May Summit at Redmond WA, USA from 14th May to 16th May 2019.

Dr. Gresha Bhatia has received sponsorship for an amount worth Rs. 1,00,000/- to attend the AI for Good Summit in Seattle, USA from 14th Oct to 17th Oct 2019.

21.3 VESIT FACULTY HONORED BY INSPECTOR GENERAL OF POLICE

Being a technical consultant team member of the project from the last few years, Dr. Parameshwar Birajdar, Faculty, Electronics Dept., VESIT received honour from Shri. Brijesh Singh, Inspector general of police, Cyber Maharashtra, in presence of Hon. Chief Minister Shri. Devendra Fadnavis at Auditorium at Commissioner of Police Mumbai office. Automated Multimodal Biometric



Identification System (AMBIS), a dream project of Cyber Maharashtra to strengthen the law enforcement in the state, inaugurated by Hon. Chief Minister Shri. Devendra Fadnavis at Auditorium, Commissioner of Police Mumbai office. The approx. cost of the project is 56 Cr. covering all the police stations of the state.

21.4 Certified Functional Safety Professional (CFSP)

Mrs.Nilima Warke, Associate Prof, Instrumentation Department has acquired the prestigious Professional Certification of exida "Certified Functional Safety Professional (CFSP)".

The CFSP holders are professionals who have thorough understanding and knowledge of Safety Lifecycle activities for industries and they support safety lifecycle projects on an execution level. This is required in Process and allied industries



21.5 Grant from Department of Atomic Energy (DAE) Government of India

Research Project titled "Development of Methodology for Indian Evaluated Nuclear Data Library for applications in S & T" under Dr. (Mrs.) Jayalekshmi M Nair, has received administrative approval and sanction of a total grant of Rs. 17, 81,276/- for 3 years beginning from financial year 2019-20, from Department of Atomic Energy (DAE), Government of India, based on recommendations of Board of Research in Nuclear Science (BRNS)

22. STUDENTS SOCIETY'S/ COUNCILS/COMMITTEES

22.1 PROFESSIONAL SOCIETIES

A unique feature of VESIT is the fact that there are four Professional Societies totally managed by students under the supervision of Faculty In-Charges.

22.1.1 IEEE – INSTITUTE FOR ELECTRICAL AND ELECTRONICS

"Excellence through Innovation" is what the IEEE-VESIT stands for in VESIT society culture. IEEE-VESIT gives its members ample opportunity to develop and inculcate their technical and soft skills, be it with theory, seminar or in practice via interactive workshops.

In-house Publication	"Innovations"
Symposium	"Melange'20"
Website	ww.ieee-vesit.org

Activities during the Year 2019-20:

Film it:, Roadshow, Case Closed, Mastermind,Self Defense Workshop, WIE Workshop, Android Workshop, Quizzard 1,Clash of Conquerors, Quizzard 2:,Online Treasure Hunt, Comflict, Android Workshop 2.0,Technical Paper Presentation & seminar, Save The Earth MUN, Cloud Computing Workshop, Game Development Workshop,Machine Learning Workshop, Galactic Shutdown, Article Writing, TROTA,SE Coordinators' Event.

Galactic Showdown

This game tested participant's mental skills as well as the ability to form an alliance



Galactic Showdown



Android Workshop

Android Workshop

The objective was to make attendees learn to develop applications using the latest operating system with practical implementation

46

22.1.2 ISA- Instrumentation Systems and Automation

ISA is a leading global non-profit organization that caters to the needs of over 30,000 worldwide members and other professionals. ISA-VESIT student chapter is well rooted with the International Organization. ISA-VESIT chapter exposes its members to the upcoming competitive world and tries to give them an edge over the others when it comes to practical knowledge through various events and seminars. Having conducted wide range workshops and fun events over the years, ISA-VESIT decided to take another step forward in the field of technical development by implementing a 6 domain structure under which the society would function. The six domains were Web Designing, Data Analytics, Embedded Systems Hardware, Embedded Systems Software, Process Automation, and Process Maintenance. With this new initiative, ISA-VESIT plan to become interdisciplinary and cover all the aspects of engineering in a wholesome way.

In-house Publication	"Envisage"
Symposium	Engauge
Website	www.isavesit.org

Activities during the Year 2019-20:

Build and Hit, BE and Beyond, Crack the code, Raspberry Pi Workshop, Arduino Workshop, Tinker's Lab, PLC Workshop, WEb Development Workshop, Bluetooth Communication Workshop, Electronic Workshop, Wireless Analogues Sensor Network Workshop, Field Programmable Gate Away Workshop, Image Processing Workshop, 3D Printing Workshop, Workshop, IoT and Wireless Sensor Networks,3-D printer inauguration, Image Intercollege Processing, App Development workshop, Events-Aayam, SYMPOSIUM "Engauge"



Technical Paper Presentation





Students enthusiastically participating in Conflict

47

22.1.3 CSI- COMPUTER SOCIETY OF INDIA

The CSI VESIT stands for ingenuity, innovation and versatility. CSI-VESIT aims to give its members an added dimension to technology by keeping them abreast with the rapid advancements in the wide arena of technology. It is one of the most active student societies with the objective to accelerate student expertise in the field of technology. The CSI-VESIT student chapter has been retaining the award of The Best Student Chapter for the past seven years.

In-house Publication	"Random Thoughts" and "Nexus"
National Board	"Communications"
Annual magazine	"Interface"
Symposium	"Impact"
Website	www.csi-vesit.org

Activities during the Year 2019-20:

Java Workshop ,Code Knights, Sherlock and Watson, The Amazing Race, Powers of two, Brainiac ,Code Unscrabble, Found the Flaws, Cypher Tension,Quizmasters, Bob the Builders, Hidden Cipher, Ode to Training,Github Workshop, IRC Workshop, Hidden Cipher, PHP & MySQL Workshop, Bootstrap Workshop, Python workshop, LAN gaming, Placement Talks, Cricomania, Linux workshop, Laravel workshop, SE Coordinators' Event etc.



Student enthusiastically participating in Hidden Cipher

22.1.4 ISTE-Indian Society for Technical Education

VESIT-ISTE chapter's objective is to bridge the gap between the curriculum and the industry's requirements by giving students a platform to hone their technical and non-technical skills like overall personality, reasoning and presentation skills.

In-house Publication	"Mirage"
Symposium	"Synergy"
Website	www.iste-vesit.org

Activities during the Year 2019-2020:

GRE Workshop, Instagram using Phython Workshop, Technical Debate, Dalal Street, Notice the Unnoticed, Machine learning Workshop, Photoshop, Technical debate, Notice the unnoticed, Group discussion etc.



Student enthusiastically participating in Dalal Street

22.2 COUNCILS

22.2.1 CULTURAL COUNCIL

The Cultural Council has been organising events over the years that bring together fun, knowledge, creativity, innovation and freshness in all aspects of our life. The events usually bring out the creativity of the student's college life by having competitions catering to acting, film-making, singing, dancing and all the various contemporary art forms. The events are like:

PRARAMBH

Prarambh is an event hosted for welcoming the freshers of the undergraduate students. The following activities are included:

- 1. Meme Challenge (Online Challenge)
- 2. Spoof It
- 3. Poster making
- 4. Collage making
- 5. Graffiti making Cultural Workshops



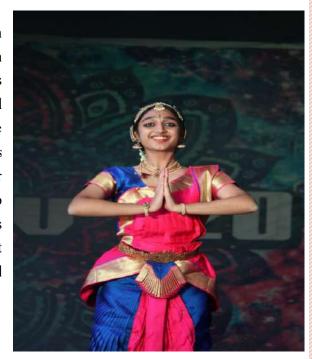
ILLUSION

Illusion is a cultural event with the objective of promoting intra-college bonding and identifying the most phenomenal talent in the institution. Illusion provides a grand arena for students to compete with each other and unlock individual potential. The council discovers a unique formula for a fun filled event which demands excellence from the participants.

49

UTSAV

Utsav is the annual college event that begins in February and ends in Mid-March. It is the time when the students who participated in intra college events are felicitated and given awards. During Utsav, the Council conducts more than 30 events in the sphere of literature, performing arts, music, literature, aesthetics and visual arts. The competitions are conducted for the juniors and seniors separately. The junior group consists of FEs and SEs while the senior group consists of TEs and BEs. The awards include Best Class, Best Player, etc. The pre-Utsav celebration includes Traditional Day, Branch Day, Chocolate Day, Suit Day & Clone Day.



VESIT Dance Crew

The VESIT Dance Crew (VDC) is a team of the students who manage to uphold their legacy by securing top positions at various dance competitions in the academic year.

First Position at Oculus (SPIT, Andheri) and (Shah and Anchor Kuttchi College of Engg.), Manzar(ICT, Matunga), Aavishkar (King Edward Memorial Hospital) Aarakshan (KJ Medical College, Vidyavihar), Ojus (AP Shah Institute of Technology)

and Second Position at Alegria (Pillai's College, New Panvel) and ETAMAX (Father Conceicao Rodrigues Institute of Technology, Mumbai).



Vesit Dance Crew in elated mode after their superb performance

VESIT Drama Club

The VESIT Drama Club is a team of the super talented students who represent the institution in a host of inter-college drama competitions including national competitions. Cultural Workshops for First Year students was conducted during their induction by the Dance Crew and the Drama workshop was held by the VESIT Drama Club. The workshops were conducted from 2:45 pm to 5:00 pm as the people handling the events were from II, III and IV years.

22.2.2 Music Council

Every music enthusiast will agree with Leonard Bernstein on the power of music. Keeping up with this spirit of music, the Music Council for the academic year 2019-20 headed and came up with two major events in the odd semester where the whole college turned into a musical fiesta with just music resonating all around.

Bliss'19

Like every year, BLISS stays true to its words- Beauty Lies in Soulful Symphony. With all the talented musicians of VESIT coming together and giving the most soulful performances, the audience was truly in Bliss. Bliss'19 was held on 16th September, 2019 in the College Auditorium. This year, the theme was "Back to 90s". The Aesthetics team put in a lot of effort with their art-works adorning the college. Around nine bands performed at the event which included the students from all four years and MCA as well. The bands had different genres- Classical, Bollywood, Pop, etc. It was the perfect mix of ingredients for a mesmerizing program

Octaves 20

Octaves is a music fest which was organized by the Music Council of VESIT. There was tremendous participation in this festival. Slay it don't spray it: This was a fun event conducted by the music council so that people can enjoy and also learn about music.

Voice of VESIT: This was the most competitive event for Octaves '20 where singers all around the college competed for the title of "Voice of VESIT" The final participants had to go through 3 extremely difficult rounds which tested their musical ability on different standards. The other events conducted were Guess the Song, Solo Instrumentals and Battle of Bands The winners of all the different events and competitions were awarded with trophies and certificates by the LOOK band. Department wise "Musical Flashmobs" were held during Octaves'20.

22.2.3 SPORTS COUNCIL

Sports Council has been working tirelessly throughout the year mainly for their two grand events: Sphurti Indoor and Sphurti Outdoor conducted in the odd and even semesters respectively. Being the most active Council ever, it is naturally quite popular since the hype is always present, and lack of participation is rarely a problem for this Council since year after year, the best players come back to reclaim themselves, and newbies are given a chance to prove themselves and hone their skills.

Indoor Sphurti: Indoor Sphurti is conducted in the Odd semester of the academic year as the playgrounds are not in good condition. So, we keep the players engaged and interested in the indoor games. This year, the events included in Indoor Sphurti were Carrom, Chess, Table Tennis and Futsal.this year, for the very first time, it was open to students of FE and FYMCA.

Outdoor Sphurti: Outdoor Sphurti is conducted in the Even semester of the academic year. It is open to all students of VESIT. The competitions are conducted differently for different batches. Students of FE and FYMCA compete against each other for the title, on the other hand, students of SE, TE, BE, SYMCA and TYMCA compete with each other for victory title.

This year the sports council also conducted yoga sessions for the newbies during their induction program.







51

STUDENT'S INTERCOLLEGIATE ACHIEVEMENTS IN SPORTS 2019-20

THROWBALL

Juhi Ajwani(TE) from the dept. of electronics and telecommunication participated in NMCE Women's Open Throwball Tournament arranged at Jamnabai Narsee School. She played from "Dazzlers Team" where they secured 3rd place in the tournament. Juhi was awarded with "Best Player" award for her performance.

VESIT Throwball Team was Runners Up at Enthusia Throwball tournament arranged by VJTI college, Mumbai. Also they were winners at Skream Throwball tournament arranged by KSJCE College, Mumbai and winners at Ignitra Throwball tournament,

arranged by SFIT college, Mumbai.



SWIMMING

Omkar Mane (SE) from the Dept. of Computer Engineering has secured 2nd position in 50m breaststroke and 4th position in 100m Breast-stroke at Mumbai University Tournaments.

BADMINTON (Women's Team)

Archita Malgaonkar (SE) and C.V.Ananya (TE) from the Dept. of Electronics, Electronics and Telecommunication engineering respectively secured 2nd Position in women's doubles category at Skream Badminton tournament arranged by KSJCE College, Mumbai.

Archita Malgaonkar (SE) Secured 1st Position in Women's singles category at Skream Badminton tournament arranged by KSJCE College, Mumbai.

Archita Malgaonkar (SE) Secured 3rd Position in Women's singles category at Enthusia Badminton tournament arranged by VJTI College, Mumbai.



C.V.Ananya (D14A) and Archita Malgaonkar (D6B)

VOLLEYBALL

VESIT Volleyball team have not participated in any intercollegiate tournaments for last 4 years. This was the first time, it participated in Spoorthi arranged by SPIT College, Mumbai and secured 4th position in the tournament.



VESIT VOLLEYBALL TEAM

CARROM

Bhavik Boricha (First year MCA) has participated in various Carrom tournaments and has achieved great success! Qualified for finals in both Men's singles and Men's doubles category in Carrom Tournaments arranged by VESIMSR College, Mumbai. Secured 3rd Position in Men's singles category in Enthusia carrom Tournament arranged by VJTI College, Mumbai.



Bhavik Boricha (MCA1B)

FENCING

Venkatesh Arumugam (First year MCA) has participated in Fencing tournaments and has achieved remarkable success! One silver and One Bronze medal in Mumbai University Fencing Tournaments. Gold medal in Vigor Fest arranged by VIT College, Mumbai.



Venkatesh Arumugam (MCA1B)

FOOTBALL

As every year VESIT FOOTBALL team participated in many tournaments and have bagged huge success Runners-Up at VIT Sportsmania Quater Finalists at LTCOE and Quater Finalists at SAKEC Olympus

BADMINTON (Men's Team)

Rahul Tejwani (TE) and Tejas Dhopavkar (SE) from the Dept. of Computer Engineering has secured 2nd position in Men's Doubles category at Badminton tournament arranged by Ruia College.



Rahul Tejwani (D12C) and Tejas Dhopavkar (D7A)

ATHLETICS

Nameira Shaikh (TE) from Dept. of Electronics Engineering has secured 2nd position in 100 meters hurdles event and 3rd position in 100 meters running event in State Level Athletics Competition arranged at Balewadi, Pune. Nameira Shaikh has secured 3rd position in 100 meters hurdles event and 100 meters running event at West Zone Athletics Championship arranged at Rajasthan. She represented Maharashtra at State level.



Nameira Shaikh (D11B)

LAWN TENNIS

Vishal Laungani (BE) from Electronics and Telecommunication Engineering has participated in Lawn Tennis tournaments and is performing really well.

- ✓ Secured 3rd Position in Mens' singles category in Enthusia Tennis Tournament arranged by VJTI college, Mumbai.
- ✓ Secured 1st Position in Men's doubles category in Enthusia Tennis Tournament arranged by VJTI college, Mumbai.
- ✓ Secured 2nd Position in Men's singles category in Skream Tennis Tournament arranged by KJSCE College, Mumbai.
- ✓ Secured 3rd Position in Men's doubles category in Skream Tennis Tournament arranged by KJSCE college, Mumbai.



Vishal Laungani (D19B)

TABLE TENNIS

This is the first time that VESIT has shown some excellence in Table Tennis.

- ✓ Overall 4th Position in Mumbai University Table Tennis Tournaments. Qualified for Semi-finals.
- ✓ Devesh Sety (TE) was qualified for Pre- Quarter Finals in Men's singles category.
- ✓ Runners Up position at Enthusia Table Tennis Tournaments arranged by VJTI College, Mumbai.
- ✓ First Position at SPoorthi Table Tennis Tournaments arranged by SPIT College, Mumbai.



VESIT TABLE TENNIS TEAM

VESIT Table Tennis Team reached semi-final. Devesh Sety (TE) was qualified for prequarter-finals in the round in from a draw of 96 teams in Mumbai University matches i.e. VESIT team has secured 4th position in University Rankings singles category.

CHESS

Chess Girls Team secured 7th position in interzonal Mumbai University Tournaments. Overall 40 teams participated in the tournament. VESIT chess team is in the top 8 rankings of Mumbai University.





22.3 Social Responsibility Team (SoRT)

"Not all of us can do great things, but we can do small things with great love."

- Mother Teresa

"Towards the better, together" With this attitude and utmost dedication, SoRT VESIT is spearheaded by a group of socially enthuiastic students, primarily focuses on empowering the unprivileged throughout the academic year. Every year, the team encourages and sensitizes students in order to motivate them to assist others, in order to proceed in tandem, towards the better. We believe there is nothing more, to make positive changes in our society.



Eye check up drive in college for people of nearby slums



Students selling NGOs product in college to help NGOs



Blood donation drive



Kerala flood donations

(201kg of rice, 100kg of wheat 58 kg of daal and multiple boxes of biscuits donated)

55

22.4 WECHANGE, AN NGO THE BRAINCHILD OF A STUDENT OF VESIT

The power of the youth cannot be argued upon. The youth of our nation has the power to change the face of the world. This drive to change the face of the world has led to the formation of an organization called weCHANGE, an NGO which is the brainchild of a student of VESIT, **Mr.** Shridhar Loknathan, an alumnus from the 2015

He, along with **Ms Sampada Inarkar** (2016 Batch of Instrumentation), laid down the cornerstone of this organization back in 2015.

weCHANGE is an organization based at Thane, which is entirely run by youth. "CHANGE" here stands for Care, Help, Acknowledge, Nurture, Grow and enlighten. The basic focus of the organization is in the areas of Education and Healthcare, and its tagline translates to "Knowledge That Liberates". weCHANGE believes in the power of value -based education. Moreover, as education is the key to achieve development, weCHANGE strives to promote education and knowledge, which in turn can be resourceful to fight any kind of problem.



To achieve this, weCHANGE started off with its flagship initiative of "Be A Mentor" to support about 70 students with individual attention to help them with their academics and ambitions.

Presently they are working with two organizations - Ekta Niradhar Sangh at Kamshet and Kshamata in Thane. In the "Be A Mentor" initiative, members of weCHANGE are assigned beneficiaries, whom they will meet regularly, to support and assist them in their education, personal issues, career-related counseling, etc. The main idea behind this program is to create awareness and help the young beneficiaries realize their dreams and ambitions and support them to achieve it.

22.5 CIRCLES

22.5.1 VESIT PHOTOCIRCLE (VPC)

"There is one thing the photograph must contain, the humanity of the moment."

— Robert Frank

VESIT PhotoCircle is a group of students who come together for their love and passion for photography. The best photographers in VESIT are drafted through 'Recruitment Drive'. VPC captures memories throughout the events in the academic year and share them on their Facebook page. They also conducted a PhotoWalk. VESIT PhotoCircle is a small family in the big community of VESIT. Various events conducted by the College, the Cultural Council, SORT Council, the Sports Council, the Music Council, and the VesLit Circle have been covered by the VESIT PhotoCircle.

VPC also organizes an informative and visual context-based photography workshop for the members and is open for all the students of VESIT.

WORKSHOP AND PHOTOWALK

With the aim of helping students to learn, enhance and understand the ideology behind the art of expressing stories through pictures. A photowalk was conducted on 9th March 2019, starting from CSMT station to the various heritage sites of Mumbai CSMT area.



22.5.2 VESLit

It aims at teaching spoken English skills by taking special sessions for the students who want to hone their spoken English skills and groom their personalities. The Annual Literature Fest of VESIT, The VESLit Week, marks a full week of online and offline events that range from story writing to skits to microfiction. This festival is one of the most hyped and is the only festival, which has events held in collaboration



with companies like The Union Bank of India and The Times of India. This year @ VESlit Circle had a complete makeover. It was structures, with foir divisions, each tasked with a focused objective. The four divisions are: Debate, M UN & Mock Parliament, Quiz and Elocution, and Films.



The VESLit Circle at the finale of VESLit Week

22.6 VESIT RENAISSSANCE CELL

In the academic year 2015-16 VESIT has come up with 'VESIT RENAISSANCE CELL' (VRC) with the mission, "To Suggest and implement innovative schemes to afflux VESIT for achieving academic excellence."

22.6.1 VESIT SUMMER AND WINTER

The Internship aims to provide practical experience to the VESIT students, and the job was assigned to VESIT Renaissance Cell (VRC). In the summer Internship conducted by VRC during 2019 of 75 students successfully completed internship. The winter Internship conducted by VRC during Dec 2018- Jan 2019 more than 80 students were selected for internship.

22.6.2 MICROSOFT CERTIFICATION

VRC has coordinated Microsoft certification at VESIT facilitated by ATS. Three different courses were offered: Cross Platform Mobile App Development, Cyber & Mobile Security and IoT.

22.6.3 PRAXIS



Participants of Robo Soccer



Participants of Bridge the Gap

In order to be successful, one must have a competitive spirit and an optimistic attitude towards learning from mistakes. To nurture and promote this competitive spirit, VESIT organizes PRAXIS every year. PRAXIS is our annual inter-collegiate technical festival, which has an exceptionally competitive Robotics event that has become a much awaited event among engineering students. It is gaining popularity not only in the University of Mumbai but also in other Universities as well. PRAXIS also boones and cultivates a sense of team spirit and event management skills in the students along with a sense of belonging.

VESIT HACKS



A 30 hour hackathon titled VESIT HACKs was conducted in association with Institute Innovation Council (IIC), under the umbrella of Praxis'19 - VESIT's annual TechFest. The event was held on 22nd and 23rd September 2019. The primary agenda of VESIT Hacks was to assist all the students with an opportunity of getting an excellent practice of working and collaboration standards for the events like

Smart India Hackathon, Google Summer of Code etc.53 Teams enrolled with a total of 285 participants. Assessment was done on the basis of the Technologies Used and Team Collaboration. Students had a great learning experience and explored a lot of new technologies.

22.7 VESITConnect

VESITConnect is the monthly newsletter of our institution which has been dedicatedly working since September 2015 to keep the college connected. The motto towards the team's determination is to keep the students, staff, management and the alumni abreast with the various events happening in the college, and also to keep them informed about the upcoming events. The newsletter captures all the joy and excitement around the college.

22.8 Institution Innovation Council

VESIT has joined hands with MHRD's Innovation Cell (MIC) to systematically foster the culture of Innovation amongst all Higher Education Institutions (HEI) and encourage, inspires and nurtures young students by supporting them to work with new ideas. MIC has envisioned the creation of a network of 'Institution's Innovation Council (IICs)' across selected HEIs in India VESIT has been granted permission to



Inauguration of VESIT IIC

establish the IIC in 2018 (VESIT-IIC). Based on the exclusive performance in the conduction of various events and activities in 2018-2019 MHRD has awarded 4-star certificate to the VESIT-IIC In the year 2019-20 VESIT IIC hosted various events like a MHRD innovation contest, Mega Challenge SAMADHAN, COVID-19 Competition, Smart India Hackathon and many e-Sessions on IPR, Entrepreneurship, Innovation, Angel Investors delivered by Entrepreneurs and Startups, including numerous India First Leadership Talks with Shri Anand Mahindra, Chairman, Mahindra Group, Dr. Anand Deshpande, Founder & CMD, Persistent Systems Ltd, and many other successful entrepreneurs and leaders across India.

22.9 VES LEADERSHIP ACADEMY AND RESEARCH CENTRE (VESLARC)

To be successful in today's world of severe competition and stress, it is necessary to have the right attitude and positive thinking. At VESIT, the students are helped by the specially appointed faculty of VESLARC to develop a strong personality with leadership qualities.

22.10 SPICE

SPICE was formed, with an aim to bridge the gap between the college syllabus and the placements. It conducts aptitude tests, sessions by experts and ex-students to make students understand the aptitude level of dream companies as well as prepare them for such placements. It also aides in honing their overall personality by giving them an impetus to speak with confidence through activities like GD and debate sessions and prepare them for the future corporate world.

22.11 OTHER EVENTS AND ACTIVITIES

Annual Day

Annual Day is a cultural extravaganza, which is a complete event management exercise for our students under the supervision of our faculty. Annual prize distribution is also held on the same day.

Teachers' day

The College organizes Teachers' day celebration for staff members every year. Teachers completing 20 years of service are felicitated on this day.

Technology Day

On Technology day, VESIT organizes project exhibition and poster competitions for final year and pre-final year students. Students actively participate and demonstrate their research ability as well as get to know the advancement in technology. This day is celebrated in the memory of our founder trustee **Shri. K. B. Mansharamani.**

VES Premier League

This is organized by a group of Football enthusiasts from our college in collaboration with other VES members. VESIT's premier football tournament, the VESIT Premier League returned with its fourth season this academic year.

VESIT Cricket League

This is similar to the VESIT Premier League but for all the cricket lovers of VESIT. VESIT Cricket League returned this academic year with its third installment. The league consisted of eight teams and all captains were from the third year. The captains initially battled it out in a scintillating auction round. The teams were named after iconic cricketers.

Engineers' Day

Every year 15th September is celebrated as Engineers' Day marking the birthday of Sir Mokshagundam Visvesvarayya.

National Science Day



National Science Day is celebrated in India on 28 February each year to mark the discovery of the Raman effect by Indian physicist Sir C. V. Raman. For his discovery, Sir C.V. Raman was awarded with the Nobel Prize in Physics in 1930. To commemorate this day, VESIT conducts activities like debates, quiz competitions, exhibitions and lectures that involve both students and teachers. All programs and activities revolve around a theme selected, and this

year the theme was 'Women in Science'.

The teachers, along with the VESLit Circle came up with the idea of a Debate Competition. Debates is an academic activity that gives students a creative room to express what they feel.

Vachan Prerna Diwas

On 15th October 2019, the birth anniversary of the former President and The Missile Man of India, **Dr. A.P.J Abdul Kalam**, VESIT celebrated the "Vachan Prerna Diwas" in association with the Free Press Journal. Various books authored by Dr. Kalam and other legendary authors were displayed in the library. A rangoli was also made in the library to honor our former President, Dr. Kalam.



Three VESITians were awarded the "Best User Award". They were selected based on their interest in study beyond the curriculum which was reflected in the usage pattern of the books of the institute library. Certificates were handed over to the awardees by Dr.(Mrs) J. M. Nair, Principal.

The Republic Day

The Republic Day honours the day the Constitution of India came into effect as the governing document of India. V. E. S celebrated the 71st Republic Day at its Degree College ground. The Chief guest on this propitious occasion was V. E. S. Executive Secretary, Smt . Padma Vaswanithe, and Dr Lobo was the special guest.





Shri Satyanarayan Maha Pooja

Piousness and reverence filled the atmosphere as Shri Satyanarayan Pooja was performed for the worship of Lord Vishnu, the highest being who is an embodiment of Truth. The pooja began at 9.30 am in our college on the 24 February 2020, with the room festooned with garlands and flowers which beautified the surroundings and raised the spirits of the devotees. The rituals were performed by Mrs. Neeta Chavan and her husband

61

22.12 UNNAT BHARAT ABHIYAN

The Mission of Unnat Bharat Abhiyan is to enable participating higher educational institutions to work with the people of rural India in identifying development challenges and evolving appropriate solutions for accelerating sustainable growth. It also aims to create a virtuous cycle between society and an inclusive academic system by providing knowledge and practices for emerging professions and to upgrade the capabilities of both the public and the private sectors in responding to the development needs of rural India. Under the UBA program, our institute has adopted a cluster of five villages namely as (1) Gargaon (2) Apti, (3) Asnas, (4) Ambiste Khurd and (5) Amgaon in Wada Taluka, District Thane.





Unnat Bharat Team

Visited- Gargaon, Apti, Asnas, Ambiste Khurd and Amgaon in Wada Taluka, District Thane

22.13 EK BHARAT SHRESHTHA BHARAT

The idea of a sustained and structured cultural connection between denizens of different regions was mooted by Prime Minister Shri Narendra Modi during the Rashtriya Ekta Divas held on 31st October 2015, to commemorate the birth anniversary of Sardar Vallabhbhai Patel. In his view, mutual understanding and trust are the foundations of India's strength and all citizens should feel culturally integrated with all corners of India. In line with this vision of our Prime Minister, Ek Bharat Shreshtha Bharat Cell has been formed, under which Department of Higher Education, MHRD and AICTE has partnered institutes from two different states to work in unison towards this vision country. Our institute, Vivekanand Education Society's Institute of Technology, Mumbai, has been partnered with International Management Institute, Bhubaneshwar under this Ek Bharat Shreshtha Bharat programme.

The First Event: Maharashtra Odisha Paryavaran Jaagrukta Quiz Competition

The first event held under the EBSB programme, was the Maharashtra Odisha Paryavaran Jaagrukta, a Quiz Competition between VESIT and IMI Bhubaneshwar, on the indigenous flora and fauna of the two states i.e. Maharashtra and Odisha on 5 June 2020. With the final round turning, team VESIT emerged victorious with a total of 130 points with team IMI scoring 105 points.

The Second Event: International Day of Yoga

On 21st June 2020, to celebrate International Day of Yoga, Vivekanand Education Society's Institute of Technology, Mumbai, went live at 8:00 a.m. with its second event 'Aarogyam Dhansampada' under the Ek Bharat Shreshtha Bharat programme. VESIT, in collaboration with International Management Institute, Bhuvaneshwar and Dr. D.Y. Patil Institute of Technology, Pimpri, Pune, celebrated 'International Day of Yoga' by having a live Yoga session with Yoga Guru Mrs. Pallavi Pawar.



The Third Event: Online Quiz

Online Quiz Competition on "The June Festivities of Odisha" organised by IMI, Bhuvaneshwar under Ek Bharat Shreshtha Bharat in association with VESIT & DPU, Pune on 28th June 2020 at 4 p.m. The activity covered the questions on the festivals of the state Odisha. Team VESIT emerged victorious in the quiz.

23 ACHIEVEMENTS

Other than in-house events, the Institute also makes sure that the students are allowed to participate in various inter-collegiate events/cultural festivals as it helps them to develop and enhance their talents without affecting regular studies.

23.1 SMART INDIA HACKATHON

Smart India Hackathon (SIH) is World's Biggest Hackathon. The first edition of SIH was started in 2017. VESIT has started with it glorious success in SIH since 2017, the first edition itself.

It's a proud moment for VESITIANS to celebrate the success at National level Smart India Hackathon for being winners for consecutive four years.

In SIH 2017, two teams were shortlisted and one team from VESIT won the prize.

In SIH 2018, six teams were shortlisted and three teams won the prize.

In SIH 2019, eight teams were shortlisted and six teams won the prize.

In SIH 2020, four teams were shortlisted and two teams won the prize.

Smart India Hackathon 2020

Three teams from the department of Computer department and one team from Information Technology are shortlisted for the Grand Finale SIH 2020.

Team Mentor:

Dr. Anjali Yeole, Mrs. Mannat Doultani Department of Computer Engineering

Students: Ankush Shetty, Gayatri Patil from Third Year and Stevert Lobo from Final Year Computer Engineering. Prize money of Rs. 1,00,000/- was awarded to them.



64



Team Mentor:

Mr. Amit Singh

Department of Information Technology.

Students: Ujala Jha, Gayatri Belapurkar, Gaurav Sahu, Abhishek Kaudare from Third Year and Sagar Ganiga from Final Year Information Technology.

Prize money of Rs. 1,00,000/- was awarded to them.



Smart India Hackathon 2019





Winners of Smart India Hackathon 2019
Software Edition



Runners-up of Smart India Hackathon 2019

Hardware Edition

23.2 E-YANTRA

VES Institute of Technology setup eYantra Robotics lab in 2014 in collaboration with IIT Bombay. eLSI (eYantra lab Setup Initiative) is state of art facility availed to students for developing Robotics, Embedded system based projects. Its objective to create a practical outlook to help students recognize problems they observe and solve them using a little robot. eLSI also trains teacher on different robotic Platforms.

eYantra conducts two main competitions every year . eYRC (eYantra Robotics Competition) and eYIC (eYantra Ideas Competition)

eYRC 2020 Finalist

Among Top 30 Teams



eYIC 2020 Regional Finalist



23.3 AVISHKAR

Avishkar Research Convention is organized by the University of Mumbai. It is designed to develop a research culture and scientific temper among the students, scholars, teachers from undergraduates to the Doctoral level in the state of Maharashtra.

In the Inter-Collegiate/Institute/Department Level held at VESIT on 16th December 2019, six groups were selected in UG category, two candidates from PG and two candidates from TH category got selected for Zonal/District Level. Zonal/District Level was held at RAIT College of Engineering, Navi Mumbai on 21st December 2019.

Prof. Kanchan Chavan and Prof. Nilima Warke, Instrumentation Department presented at (Inter-Zonal/Inter-District) round that was held at FCRIT, Vashi on 2nd January 2020, in 14th Inter-Collegiate/Institute/Department Avishkar Research Convention 2019-20.

Prof. Nilima Warke, Instrumentation Department has presented her project titled "New Hardware Methods for Extraction of Low Level Differential Signals over Wide Bandwidth with High CMRR and S/N Ratio" guided by Dr. P. P. Vaidya and Dr. (Mrs.) J. M. Nair and secured 3rd rank in Teacher (TH) category.

Team of Nehru Science Center selected this project as "Innovative Idea and Design" in TH category which is being considered for incubation.



Prof Nilima Warke receiving the 3rd rank certificate and Bronze medal in University round for Avishkar 2019-20

23.4 CONDUENT BLOCKCHAIN HACKATHON

Conduent Incorporated (NYSE: CNDT), a digital interactions company, hosted the Conduent Blockchain Hackathon finale at its office in Hyderabad on June 22 and 23, 2019, held in collaboration with HackerEarth, an enterprise software talent management company.



In the first phase 1276 students participated in a virtual crowdsourcing process over the course of six weeks. It then culminated into a **36-hour**, **on-site hackathon were 76 participants forming 27 teams** created and presented their blockchain solution prototypes.

The only team from Mumbai was represented by VESIT from the Dept. of information Technology by Abhishek

Kaudare, Athul Balakrishnan, Rajpreet Singh and Milan Hazra.

23.5 COMPETITION ON DISASTER MANAGEMENT



The Navi Mumbai Municipal Corporation (NMMC) and the United Nations Development Programme (UNDP) had organized a unique competition for the Colleges on Disaster Management. NMMC received 54 project proposals from various engineering colleges from Mumbai, Navi Mumbai, Panvel, and Thane region including IIT. Mumbai.

Out of these, six projects were shortlisted in which VESIT got the maximum score. Our students from EXTC Mr. Akshay Joshi, Mr. Atharva Patil, Mr. Shreyas Dighe, and Ms Kiran Chhatwani mentored by Dr. Nadir Charniya showcased their project on "IoT based Lake Cleaning Robot". The projects were funded by NMMC and UNDP. The winning institutes were honored by Mayor Mr. Jayawant Sutar at the general body meeting of the Navi Mumbai Municipal Corporation.

23.6 SANKALP SEMICONDUCTOR HACKATHON



The Team of three members from VESIT, Siddhant Satam, Sai Rane and Siddesh Mahadik from the Department Of EXTC participated in the Sankalp Semiconductor hackathon on "IoT Based Solutions for Indian EcoSystem" held at Adamas University, Kolkata on 28 February 2020. The event had 130 teams participating from 35 colleges all across India, primarily

from Electrical & Electronics Engineering (EEE), Electronics & Communication Engineering (ECE) and Information Technology Engineering (ITE) branches. The team from VESIT made a Pollution Monitoring Drone and were selected as one of the top 10 nationwide teams in the Sankalp Hackathon.

68

23.7 SCHNEIDER ELECTRIC HACKWARE 2019 INNOVATION CHALLENGE

The Schneider Electric Hackware 2019 Innovation Challenge, held from 18th to 21st September, was an open innovation platform with a 72-hour hackathon built around the three themes, Decarbonization, Decentralization, and Digitization of EcoStruxure.



Third Year students Vaibhav Singh Rajput (from EXTC), Kalpesh Bhole and Raghav Potdar (from CMPN) made it to the top 6 finalists of the grand event. A total of 29 teams were selected from all over the country, through an elaborate selection process by the esteemed jury for the final round of the Hackware Challenge.

These 29 teams consisted of 15 student teams, 10

startups, and the rest were professional engineers. The participants had to present bold ideas around the areas impacting sectors of Urban, Residential, Buildings and Industry.

In the final round VESIT team was among the 6 shortlisted team to represent their projects before the Vice Presidents of Schneider Electric and Forge, some very top ranking personnel from Bosch and other professionals of Forge.

23.8 BUOYANCI INNOVATION CHALLENGE

Buoyanci Innovation challenge unfolded its 2019 leg with the theme AI for Health Care. It is a competition for health-care projects and prototypes. The competition was held on the online platform with the help of the Zoom Application, and the final round was conducted on October 14th, 2019.

Stevert Lobo, Meenakshi, Ishma Amin and Aditya Mane from final year Dept. of Computer Engineering mentored by Prof. Priya R.L. were announced the winners and got the cash prize of Rs.20,000/-.

Atharva Potdar, Yash Mate, Akash Magdum and Pradyot Chhatwani from third year Dept. of Information Technology mentored by Prof. Pooja Shetty were the runners-up in the competition.

23.9 India Innovation Challenge Design Contest 2019

Two teams from Department of Computer Engineering were qualified for semifinals kit and won worth \$100 USD

Team 1; Anish Vaidya, Chetas Shinde, Mohit Thorat, Vinit Motwani, Sahil Talreja and Shravan Bhat mentored by Prof. Priya R.L on the project title "Health -O -Meter" and

Team 2; Rishabh Sah, Pratik Rane, Purav Rathod and Anuj Yadav mentored by Prof.. Lifna C. S. on the project title "VoteBlock"

23.10 "TRANSFER WINDOW" - IIM-INDORE

Management institutes hold competitions that test various capabilities of their participants. These competitions consist of not only logical but analytical and observational skills too. One such competition by IIM-Indore was "Transfer Window" which was at IIM-Indore, Mumbai Campus.

Out of approximately 300 teams and later 8 teams, all belonging to management backgrounds, two students, Yajnesh Shetty and Adith Nair, Third year Department of Electronics and Telecommunication, displayed an awe-inspiring performance and managed to bag the first prize.

23.11 QUIZ-A-THON



Quiz-a-Thon, an Inter-college Quiz competition, organized by The Times of India, was held at Phoenix Marketcity, Kurla on 19th December 2019. It saw over 500 students from all over Mumbai. VESIT Students from EXTC Third year Ananya, Adith Nair and from Second year Avinash Tripathy secured the Third place in this competitive battle of wits where a total of 170 teams participated. Mihir

Mishra, an Indian television actor, was the Chief Guest for the 'Quiza- thon'

23.12 SAKEC INTERCOLLEGIATE QUIZ



Once again, VESITians have won the SAKEC intercollegiate quiz conducted at Shah and Anchor Kutchhi Engineering College, Chembur. Teams from all over Mumbai participated in this event, of which six qualified for the on-stage event after the written preliminary rounds.

There were two teams who were sent to represent our college. One comprising of Rohan Ghosalkar and Arnav Bagchi from CMPN second year and the other

comprising of Adith Nair with C.V.Ananya from EXTC third year and Avinash Tripathy from EXTC second year. Adith's team secured the First position while Rohan's team came second.

23.13 ANNUAL DEBATE COMPETITION BY JBIMS

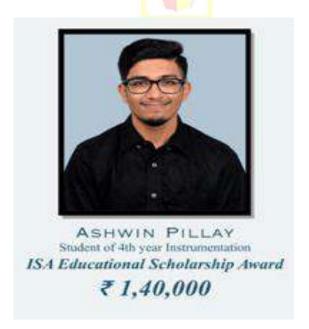
Adith Nair and **CV Ananya** from **EXTC third year** participated in the annual Debate Competition "Pratyaksha" conducted by Jamnalal Bajaj Institute of Management Studies on 19th October 2019 in the college campus.

They secured the runners up position and received a cash prize and a box of donuts as a special treat.

23.14 ISA MHARASHTRA AWARDS BY STUDENTS



Mayuresh Shelar of Final year Dept. of Instrumentation Engineering has been awarded with the prestigious "Best Student Leader" ISA Maharashtra section from the International Society of Automation (ISA) in 2019.



Ashwin Pillay of Final year Dept. of Instrumentation Engineering has been awarded with the "2019 Educational Scholarship" from the International Society of Automation(ISA) amounting to a sum of USD 2000 thereby, earning the accolade of being among the fourteen undergraduate students across the globe who received the prestigious award this year.

23.15 SHRESHTHATA AWARD

The 'Shreshthata award' is an initiative by VESLARC (VES Leadership Academy and Research Centre) to acknowledge and encourage students who endeavor to make a difference in spite of unaccommodating circumstances. Shreshthat awards were given to promising students who are good at studies as well as extracurricular activities with a social touch.

Mr. Pundalik Fadatare has received the VES Shreshthta Award (In memory of Smt. Mira Keswani) and Ms. Dhanashree Shetty has Received the Ram Keswani Protsahan Puraskar (RKPP) for 2019-20.

23.16 SCHOLARSHIP BY JADAVPUR UNIVERSITY ALUMNI TRUST



Student from second year computer being felicitated

On 30 January 2020, VESIT organized the event for scholarship by Jadavpur University Alumni Trust.

The event began on an auspicious note with the Saraswati Vandana and the Lighting of the Lamp ceremony. The Chief Guest was **Ret. CDR S.K Jana**, who was also the past President of the TRUST.

23.17 THE SUMAN RAMESH TULSIANI CHARITABLE SCHOLARSHIP



Shri. Amar T. Asrani, Shri. Suresh Malkani, Shri Manish Rupani

The Suman Ramesh Tulsiani Charitable Scholarship Event was held on the 6th of February 2020.

The Honourable Secretary of VES, Shri. Amar T. Asrani and Honourable Treasurer of VES, Shri. Suresh Malkani were the dignitaries who attended the ceremony and facilitated the scholarship along with Shri Manish Rupani. Students of Vivekanand Education Society's

Institute of technology, Vivekanand Education Society's College of Arts Science and Commerce, Vivekanand Education Society's College of Pharmacy, and Vivekanand Education Society's College of Architecture received the scholarship.

24. GREEN CAMPUS INITIATIVE

Tree Plantation





Vivekanand Education Society emphasizes integrating planet-friendly and socially harmonious practices into the lives, hearts, and minds of our students and staff members Various initiatives are taken by college to conserve and protect our environment.

In order to conserve and save energy the college has commenced the practice of keeping air conditioners and lifts off every Wednesday. Along with it in the canteen also the college restricts the use of plastic cups ,spoons etc. The Social and Responsibility team of college through its various activities makes considerable efforts to play an active role in safeguarding the environment. Plastic collection and e-

waste drive was also conducted. Trees are also planted every year by the college authorities for safeguarding the environment.

Plastic Waste Elimination Drive



Plastic waste elimination drive was conducted in Vivekanand Education Society's Institute of Technology on 30th September 2019 and 31st January 2020 respectively. The prime objective of this campaign was for the students to grasp the impact and individual can create on our planet earth. The humongous amount of plastic waste collected from in and around their household and get it to college so that they could be sent

it for recycling. They managed to collect a whopping 200 kg of plastic which was sent for recycling with the help of Jay Foundation who assisted them in the drive.



Plastic collection drive in association with Jay Foundation for recycling of plastic waste

25. CAREER GUIDANCE, TRAINING, PLACEMENT & ENTREPRENEURSHIP CELL

ACADEMIC YEAR 2019-20

Placement Statistics as on 30th July 2020

	Overall	Electronics Engineering	Computer Engg	Instrumentation Engg	Electronics & Tele.	Information Technolog	MCA*
Job Offered	597	61	215	46	106	70	99
Second Job	106	7	42	7	16	14	20
Placed	491	54	173	39	90	56	79
Strength (Total)	820	134	215	75	213	74	109
Strength (Interested)	700	89	181	55	196	70	109
% Offered (Strength)	72.80	45.52	100.00	61.33	49.77	94.59	90.83
% Offered (Interested)	85.29	68.54	118.78	83.64	54.08	100.00	90.83
% Placed (Interested)	70.14	60.67	95.58	70.91	45.91	80.00	72.48
Minimum Salary	2						
Average Salary	4.5						
Maximum Salary	14						
No of Companies	86#						

^{*} Training and Placement Cell also facilitate Internships for MCA Students during their Semester VI.

[#] Figures likely to increase as placement drives are still going on for 2019-20 batch.

Training and Placement Cell Structure

Training and Placement Officer

Deputy Training and Placement Officer

ETRX
Placement
In-charge

CMPN
Placement
In-charge

INST Placement In-charge

EXTC Placement In-charge

INFT Placement In-charge MCA
Placement
In-charge

Student Placement Coordinators Student
Placement
Coordinators

Training and Placement Cell Activities

Since 1982

Year	Activities
I YEAR	Inputs during Induction
II YEAR	Eligibility, Employability, impact of CGPI Contents of Resume and Building your Resume
III YEAR	Mock tests and Placement Readiness Workshops, Interaction with Final year students
IV YEAR	Interaction with alumni / companies. Online mock aptitude test

26. VESIT ALUMNI ASSOCIATION

The Annual VESIT alumni meet was held on 21st December, 2019. The Alumni Day is organized to strengthen the relationships of the ex-students with the college and the teachers. The last year also marked the Silver Jubilee celebrations for Batch 1994. The function was presided over by the trustees, Shri. B.L. Boolani, Dr. Lulla, Advocate Shri. Kannan, Vice Principal - Dr. Mrs. Vijaylakshmi, Mrs. Saxena, VESIT Alumni Committee members, Heads of Departments and Deputy Heads of Departments. The distinguished Alumni award was received by Mr. Jayesh Kirange, Director of a Hedge Fund Administration and Consulting company SS&C Globeop, who has completed Chartered Financial Analyst (CFA by CFA Institute) and Financial Risk Manager (which is FRM by Global Association of Risk Professionals) after graduating from our institute.



Mr. Jayesh Kirange receiving the Distinguished Alumni of 2019 award



VESIT Alumni San Fransisco chapter

V-REACH '20



Hashu Advani Memorial Complex, Collector's Colony, Chembur (E), Mumbai - 400074. INDIA



V-REACH '20

An Alumni Engagement Initiative

21 Webinars | 37 Alumni Speakers

Under the guidance of Internal Quality Assurance Cell (IQAC) In Association with Institution's Innovation Council (IIC)



Webinar Series by Alumni 22 June - 12 July '20 | 5:00 - 6:30 P.M.

Guidance on Preparing for Better Placements Higher Studies and Career Guidance Research and Innovations Entrepreneurship Development New Trends in Technology



Live on YouTube

Shri B. L. Boolani President, VES

Dr. (Mrs.) J. M. Nair

Dr. (Mrs.) M. Vijayalakshmi Vice Principal



E-Cell (tVEC)

VESIT Entrepreneurship Cell (E-Cell) was founded on 14th March 2014 and it aims to develop and nurture the entrepreneurship skills within the students while creating robust infrastructure to support budding entrepreneurs© Mission.

This year E-Cell has undergone fundamental changes from the basic policies and structure to all the intricate details. It also has a new logo and a new mission statement.

VESIT Entrepreneurship Cell's (new) mission is to nurture Entrepreneurial skills among VESITians and to increase the number of StartUps from VESIT.

Various activities were conducted by the Council in A.Y 2019-2020.

Workshop on Design Thinking and Entrepreneurial talk by ISDI School of Design and Innovation, Mumbai on Tuesday 30th July, 2019 for E-Cell members. In an effort to foster and promote a startup, a seminar was held **on Entrepreneurship and Innovation as a Career Opportunity.**

NLAP

A National Level Awareness Programme by the Ministry of MSME VESIT E-Cell hosted National Level Awareness Program (NLAP) organized by Micro, Small and Medium Enterprises (MSME) on 28th February 2020 (National Science Day). This event was aimed at enlightening budding entrepreneurs about the various government schemes, subsidies, help available for startups and small scale businesses. Delegates from different Government departments of MSME like National Small Industries Corporation (NSIC), Khadi and Village Industries Commission(KVIC) and Coir Board of India were present at the event.





PROJECT DEEP BLUE BY MASTEK AND MAJESCO

Mastek and Majesco initiated "Project Deep Blue" with an agenda 'To transform lives by using technology' in the year 2015 by encouraging engineering students to solve social problems using coding skills and come up with meaningful technical solutions. As the new generation of emerging engineers lack practical knowledge regarding reference to the industry, "Project Deep Blue" serves as a platform that bridges the gap between industry and academia.

VESIT E-Cell was handed over the responsibility of facilitating Mastek-Majesco Project Deep Blue Season 5 for VESIT students. In the same regard. An introductory talk by Mr. Akhil Singhal from Mastek was conducted on 6th August 2019. For this season, a total of 26 teams from VESIT have submitted their proposal, out of which 10 teams were selected for the main event

27. DISCIPLINE

Discipline plays a vital role in every institute. All the students admitted in the first year (F.E.) of the institute will have to give an undertaking that they would observe general discipline and decent behavior throughout the four years of their engineering course.

27.1. IDENTITY CARDS

Every student who has taken admission and duly confirmed, must immediately take the I-Card, which is issued from the Principal's office and must wear it all the time. A student cannot even enter the premises without his/her I-Card and must readily present it when asked for, by the college authorities. At the beginning of a new academic year, each student must take a new I-Card issued by the college

after surrendering the old I-Card.

27.2. LOSS OF I-CARD AND ISSUE OF DUPLICATE I-CARD

In case the I-Card is lost, students can take special permission from the Principal which is valid only for three days. Within the stipulated time period the student should apply for the I-Card by submitting the prescribed fee. Any student using an I-Card (Original or Duplicate) without the signature of the Principal or Institute Rubber Stamp will be penalized.

27.3. EX-STUDENT'S I-CARD

For ex-students, the college has made a special provision of separate I-Cards so that they enjoy and take advantage of the college library facilities. The ex-students however, are permitted to avail the facility only on working days during a stipulated time period.

27.4. LIBRARY AND OFFICE TIMINGS

During the term, the library will remain open on all working days from 8.00 a.m. to 6.00 p.m. For the benefit of the students the college allows them to use the library facilities for extended hours during holidays and preparatory leaves. The students can visit the administrative office between 11 a.m. to 5.30 p.m. for any pending work provided they do not have any scheduled lecture, lab, and tutorial during this time slot.

27.5. USE OF SPORTS GROUND

Students should ensure that they adhere to the Rules and Regulations of the college strictly while using the college playground. Strict action would be taken if they are found playing while the lectures/practicals/tutorials are in progress.

27.6. HOLIDAYS AND VACATIONS

The list of the holidays and vacations will be displayed on the notice board. The vacation period is liable to change every year and the students should be prepared for such exigencies.

27.7. RAILWAY CONCESSION

Students who are less than 25-years-old can avail concession for commuting from College to their residence in and around Mumbai and for travelling to their hometown (where their parents reside as declared in their application form for admission) from Mumbai during vacations. Concession forms for Local Railway tickets are issued from the Office of the Institute on working days.

The long journey concession form will be issued only for the vacation period. The students should write their local and permanent home /town address in the application form, for availing this concession. Concessions are being made within three days from the receipt of their application.

27.8. HOSTEL

There are separate hostels for girls and boys in this College where the students are offered residential accommodation in the best possible living environment. It will be provided on a first-come-first -served basis and preference is always given to outstation candidates. Before leaving for vacation or holidays hostelites must take permission letter from the class teacher/class counsellor and submit a copy of the same to the Warden.(https://ves.ac.in/facilities/hostels/)

27.9. SAFETY PRECAUTION

Safety instructions given by teachers should be stringently followed by the students while doing their practicals. In case of a mishap, the entire accountability shall lie on the student concerned and the Institute will no way be held responsible.

27.10. LOST AND FOUND

The authorities of the institute will not be accountable or responsible for any damage to the possessions or belongings of the students. However, articles that are lost and found are deposited in the office and are returned to the concerned student after identification.

27.11. DRESS CODE

Students are supposed to wear decent dresses and footwear suiting Indian Culture. Indecent, skimpy, sleeveless, tightfitting, short and gaudy dresses and t-shirts with crude messages and pictures are strictly prohibited in the college campus. Students should also keep proper well-groomed hairstyles which depicts a positive outlook and persona.

27.12. CLEANLINESS OF PREMISES

All the students should strive for maintaining the cleanliness of the college premises and help in the maintenance and upkeep of the institutional property. They should not scribble anything on the walls, furniture, etc. Strict action would be taken against the erring students and if not detected, a common fine will be levied on the entire class. Breakage of furniture will be treated in the same manner. All the students should strive for the cleanliness of the premises and help in the better maintenance and upkeep of the institutional property.

27.13. INDULGING IN ANTI-SOCIAL ACTIVITIES

If a student is found in indulging in any anti-social or anti-national activities, he/she is liable to be expelled from the college without any prior notice.

27.14. CHEWING PAN/TOBACCO AND SMOKING

Chewing paan/tobacco or smoking in and around the premises of the institute is strictly prohibited.

27.15. USE OF ELECTRONIC ITEMS/GADGETS

Students are advised to use mobile phones **only outside classrooms/laboratories**. If they are found using mobile during lectures/ labs, the college authorities can confiscate such items and take strict action against the concerned students. Other electronic gadgets/equipment which is needed for project purpose can be brought in with proper permission and documentation.

27.16. REFUND OF CAUTION MONEY

After the completion of the course, an application for the refund of the caution money should be given (along with F.E. fee receipt) within the same financial year. Unclaimed caution money will be forfeited. Lifetime membership fee for alumni will be deducted from the caution money.

27.17. LOCKER FACILITY

Locker facility for the first year students is provided on a first-come-first-served basis. The student should forward an application for using this facility for which he/she has to pay a locker fee.

27.18. RETURN OF ORIGINAL CERTIFICATES

All original certificates like Mark Sheets, Passing Certificate, Caste Certificate, Domicile Certificate etc. will be returned to the students after all the admission formalities of the University /DTE are completed. Normally, these certificates are returned to first year students in the month of March/April.

27.19. NOTICE BOARDS

Students are advised to read the notice boards regularly for various updates/circulars/notices displayed regarding payment of fees, the commencement of examination and other matters of academic interest. Students are expected to pay the Tuition Fees/Exam Fees etc. before the last date. This will avoid penalties as well as inconvenience to them. Students should not go to the office for Railway concession or any other matter while their theory classes /practicals are in progress. Further, whenever they come to the office, they should maintain proper discipline and decorum. They shall always follow the Queue system and shall not crowd near the counters and should not block main entrance and exit points.

2720. GRE/TOEFL/GATE/GMAT

Students will not be allowed to miss the college for the preparation of GRE/TOEFL/GATE/GMAT or other forms of competitive examinations. It may be noted that students need to plan their schedules well in advance so that it does not affect their academic curriculum. Students who desire to appear for the same will be permitted for a maximum period of 15 days, provided their academic record and attendance in the semester is satisfactory.

27.21. CO-CURRICULAR ACTIVITIES

All co-curricular, extracurricular activities and seminars/symposia/conferences/workshops of the students' professional societies of the college will be held only after the regular college timings. No activities will affect the regular working of the college. Students who wish to participate in local and outstation technical seminars, paper presentation contests, project exhibitions and other competitions will have to take prior permission.

27.22. PICNICS AND OUTINGS

Students will not be permitted to organize/participate in private picnics/outings even on Sundays/Holidays without the written consent of the college Principal. All picnics/outings have to be authorized and accompanied by college staff. Any failure to comply with any of the rules will lead to strict action taken against them.

27.23. INTERNET USAGE

Internet facility is provided to our students at various places. Students are expected to use this facility only for academic purposes and not for any other purposes like chatting. Misuse of this facility and indecent behavior on the NET is liable for punishment and strict action.

27.24. THEFT

Students found stealing other's property or institutional property will be dealt with very strictly. If the institutional property is damaged/stolen, the entire batch of students will be penalized and the cost will be recovered as deemed fit.

27.25. SUGGESTION BOX

Students can avail the suggestion box facility provided outside the Principal's Office, in case of any suggestions, complaints etc.

27.26. BIRTHDAY CELEBRATIONS

Birthday celebrations of fellow students are not allowed inside the class or within the premises or in the vicinity of the college.

27.27. PRINCIPAL'S SIGNATURE

Students should come to the Principal's Office for signature or attestation during the recess time only or after their theory classes/practicals are over.

The above conditions are as per the Rules and Regulations of the Institute and all students should strictly follow them. Strict action would be taken against the student who fails to observe them.

Any matter not covered by these Rules will rest at the absolute discretion of the Principal.



28. PROHIBITION OF RAGGING

Ragging within or outside the institute is strictly prohibited. Any student who directly or indirectly participates in. abets or propagates ragging within or outside the institute shall face strict disciplinary action as per the provision of Maharashtra Prohibition of RAGGING ACT, 1999.

If a case is to be made as per law the following procedure is to be followed:-

- ✓ Investigation by the squad
- ✓ Findings reported to the Principal
- ✓ Principal's discussion with the Committee to decide whether to file an FIR (within 24 hrs) with Police or Local Authorities OR punishment is to be awarded by the Anti-Ragging Committee of VESIT

Punishments may be in the form of:

- ✓ Suspension from attending classes and academic privileges.
- ✓ Withholding/ withdrawing scholarship/ fellowship and other benefits.
- ✓ Debarring from appearing in any test/ examination or other evaluation process.
- ✓ Withholding results.
- ✓ Debarring from representing the institution in any regional, national or international meet, tournament, youth festival, etc.
- ✓ Suspension/ expulsion from the hostel.
- ✓ Cancellation of admission.
- ✓ Rustication from the institution for periods ranging from one to four semesters.
- ✓ Expulsion not only from the institution but also consequent debarring from admission to any other institution for a specified period. In case, the persons committing or abetting the act of ragging are not identified, the college will resort to collective punishment.

ADVICE TO ALL STUDENTS:

All students are advised to be aware of anti-ragging measures and report any ragging incident to college authorities in person or by telephone at the earliest.

More information is available on our institute's website: http://vesit.ves.ac.in or http://vesit.ves.ac.in or

Students should fill an online antiragging undertaking (<u>www.amanmovement.org</u> or www. antiragging.in) and submit the hardcopy of the form and acknowledgement at the time of admission.

83

29. ANTI-RAGGING/ DISCIPLINARY COMMITTEE

Sr. No.	Name	Designation	Position	Contact No.
1	Dr. Manoj Sabnis	Faculty, INFT	Chairperson	+91 98215 86595
2	Dr.(Mrs.) M. Vijayalakshmi	Vice Principal	Member	+91 9820882766
3	Dr. R. K. Kulkarni	Faculty, EXTC	Member	+91 98694 16288
4	Mr. A. Nagananda	Faculty, EXTC	Member	+91 99872 68159
5	Mr. Vivek Umrikar	Faculty, H & Sci.	Member	+91 99200 59763
6	Dr. Parmeshwar Birajdar	Faculty, ETRX	Member	+919892187486
7	Dr.(Mrs.) S. Mujumdar	Faculty, H & Sci.	Member	+91 9869155855

30. WOMEN DEVELOPEMENT CELL

Constitution – as per Guidelines from Mumbai University				
Sr. No.	Name	Designation	Position	
1	Dr.(Mrs.) M. Vijayalakshmi	Faculty, MCA	Member	
2	Mrs. Shobha Krishnan	Faculty, EXTC	Member	
3	Dr.(Mrs.) Saylee Gharge	Faculty, EXTC	Member	
4	Mrs. Naveeta Kant	Faculty, ETRX	Member	
5	Mrs.Kavita Tewari	Faculty, ETRX	Member	
6	Dr.(Mrs.) Nupur Giri	Faculty, CMPN	Member	
7	Mrs. Sonali Rahate	Superintendent	Member	

31. ANTI SEXUAL HARASSMENT COMMITTEE

The Government of India had promulgated The Sexual Harassment of Women at Workplace (Prevention, Prohibition & Redressal) Act, 2013 (Act) and the Sexual Harassment of Women at Workplace (Prevention, Prohibition & Redressal) Rules, with effect from 9th December, 2013. The act was laid down to ensure that women, in particular, are protected against sexual harassment at all workplaces, be it in public or private. It has now become mandatory to have an Anti-Sexual Harassment Committee as well as an Internal Complaint Committee to be formed in every institution in addition to the reports being sent to the Government for the same.

It is compulsory for all institutes/colleges under AICTE / UGC to Constitute "Internal Complaints Committee (regulation has come into force since 1st July 2016)

Sr. No.	Name	Designation	Position
1	Mrs. Shobha Krishnan	Faculty, EXTC, VESIT	Chairperson
2	Mrs. Jayshree Pandit	Co-ordinator/ Counselor, Sri Mukta Sanghtana, Chembur	Member (Non-Governmental Organisation) [NGO]
3	Dr. (Mrs.) Nupur Giri	Faculty, CMPN	Member
4	Dr.(Mrs.) S. Mujumdar	Faculty, H&Sci.	Member
5	Mrs. Rohini Temkar	Faculty, MCA	Member
6	Mrs. Sonali Rahate	Office Superintendent, VESIT	Member
7	Student Representative (Girl Student)	Cultural Secretary/SoRT Secretary / Deputy Secretary, VESIT	Member

32. INTERNAL COMPLAINT COMMITTEE

With reference to "The Sexual Harassment of Women at Workplace (Prevention, Prohibition, Redressal) Act, 2013 " with effect from 9th December, 2013.

The constitution of Internal Complaint Committee for Prevention, Prohibition & Redressal is as follows:

Sr. No.	Name of the Member	ICC Posts
1.	Prof. Smita Jangale	Presiding Officer
2.	Mrs. Sonali Rahate	Non- Teaching Employee
3.	Mr. Vikas Pange	Non- Teaching Employee
4.	Prof. Sujata Khedkar	Faculty Member
5.	Prof. Nandini Ammanagi	Faculty Member
6.	Mrs. Jaishree Pandit	External Member(NGO)
7.	(UG-SORT Secretary)	Students
8.	(PG – SORT Member, MCA)	Students
9.	Mrs. Asma Praveen (PhD)	Students

85

33. GRIEVANCE REDRESSAL COMMITTEE

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Shoba Krishnan	Faculty, EXTC Dept.	Chairperson
2.	Dr. (Mrs.) Nupur Giri	Faculty, CMPN Dept.	Member
3.	Dr. Nadir Charnia	Faculty, EXTC Dept.	Member
4.	Mrs. Navita Kant	Faculty, ETRX Dept.	Member
5.	Dr. Shiv Kumar Goel	Faculty, MCA Dept.	Member
6.	Dr. (Mrs.) S.M. Mujumdar	Faculty, H&A Dept.	Member
7.	Dr. (Mrs.) Sayalee Gharge	Faculty, EXTC Dept.	Member
8.	Mrs. Sonali Rahate	Representative of Non-Teaching Staff	Member

34. COMMITTEE FOR SC/ST

With reference to scheduled caste and the scheduled tribes (prevention of Atrocities) act, 1989, No. 33 of 1989 Dtd. 11.09.1989, constitution of the Committee for SC/ST is as follows:

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Sujata Khedkar	Asso. Professor, VESIT	Chairperson
2.	Dr. Nadir Charnia	Professor , VESIT	Member
3.	Mrs. Asha Bharambe	Asst. Professor, VESIT	Member
4.	Mrs. Indu Dokare	Asst. Professor, VESIT	Member
5.	Mrs. Sangeeta Bhalerao	Sr. Lab. Assistant., VESIT	Member
6.	Mrs. Nilima Tambade	Sr. Clerk , VESIT	Member
7.	Mr. Vinayak Doifode	Head Clerk , VESIT	Member
8.	Student Representative	Secretary, SoRT, VESIT	Member

86

35. ADMISSION GRIEVANCE COMMITTEE

STAGE - I

Sr. No.	Name	Department/Address	Position in Committee
1.	Mrs. Shoba Krishnan	Faculty, EXTC Dept., VESIT	Chairperson
2.	Dr.(Mr.) Manoj Sabnis	Faculty, INFT Dept., VESIT	Member
3.	Dr. Shiv Kumar Goel	Faculty, MCA Dept., VESIT	Member
4.	Mrs. Asma Parveen I. Siddavatam	Faculty, INFT Dept., VESIT	Member
5.	Mr. Vinayak Doifode	Administrative Staff, VESIT	Member

STAGE - II

Sr. No.	Name	Department/Address	Position in Committee
1.	Dr. P.P. Vaidya	Faculty, INST Dept., VESIT	Chairperson
2.	Dr. R.K. Kulkarni	Faculty, EXTC Dept., VESIT	Member
3.	Dr. (Mrs.) Gresha Bhatia	Faculty, CMPN Dept., VESIT	Member

STAGE - III

Sr. No.	Name	Department/Address	Position in Committee
1.	Dr. (Mrs.) J. M. Nair	Principal , VESIT	Chairperson
2.	Dr. (Mrs). M. Vijayalakshmi	Vice Principal, VESIT	Member

*** Most Important Note ***

All Admissions and Cancellations will be conducted/done as per the DTE rules and Guidelines.

All available Scholarships of University, State Government, etc will be granted to the eligible candidates as per rules. (Please refer website.)



Vivekanand Education Society's Institute Of Technology



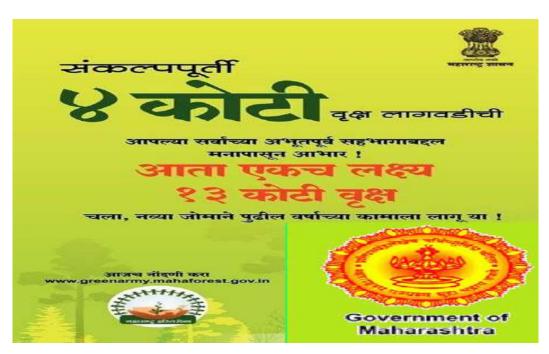




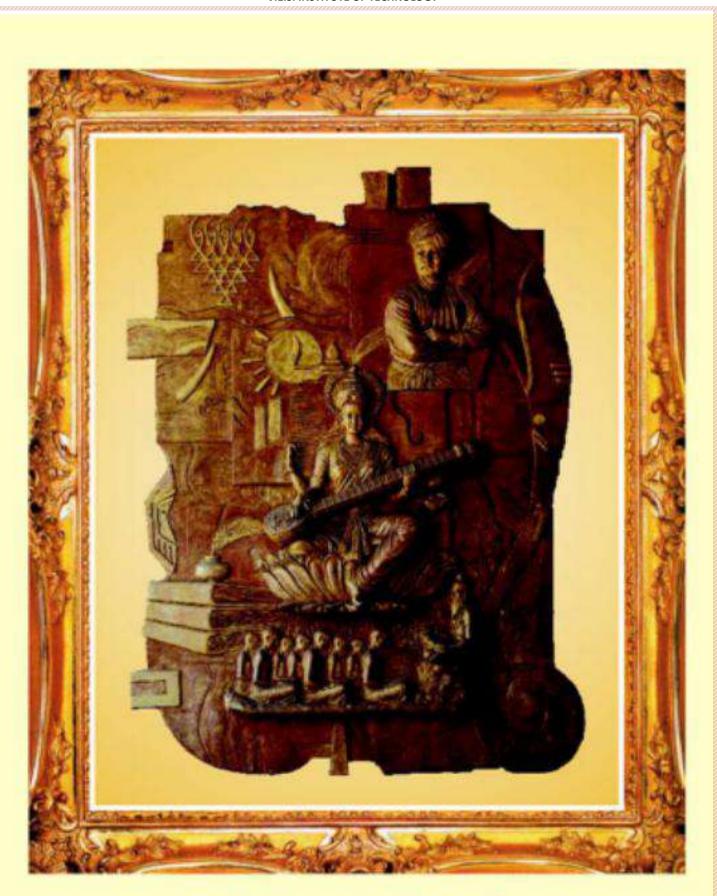








88



We need education that forges our life, generates the ideal man, imbibes noble thoughts and develops the abilities in us.

-Swami Vivekanand.

⁹ 371